

PRODUCT

DATE

MODEL NO.

UKAS ENVIRONMENTAL MARGEMENT

: TRULY SEMICONDUCTORS LTD.

SUPPLIER : TRULY SEMICONDUCTOR

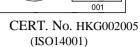
: LCD MODULE

: TFT240400-51-E

: January 23, 2010

CERT. No. QAC0946535 (ISO9001)

Version: 1.0



SPECIFICATION

Revision: 1.0

TFT240400-51-E

This module uses ROHS material

If there is no special request from the customer, TRULY SEMICONDUCTORS LTD. will not reserve the tooling of the product under the following conditions:

- 1. There is no response from the customer in two years after TRULY SEMICONDUCTORS LTD. submit the samples.
- 2. There is no order in two years after the latest mass production.

And correlated data (including quality records) will be reserved for one year more after tooling is discarded.

TRULY SEMICONDUCTORS LTD: CUSTOMER:

Quality Assurance Department:	Approved by:
Approved by:	
Technical Department:	

REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.1	2009-12-25	First release	Preliminary
1.0	2010-1-23	Full spec	/

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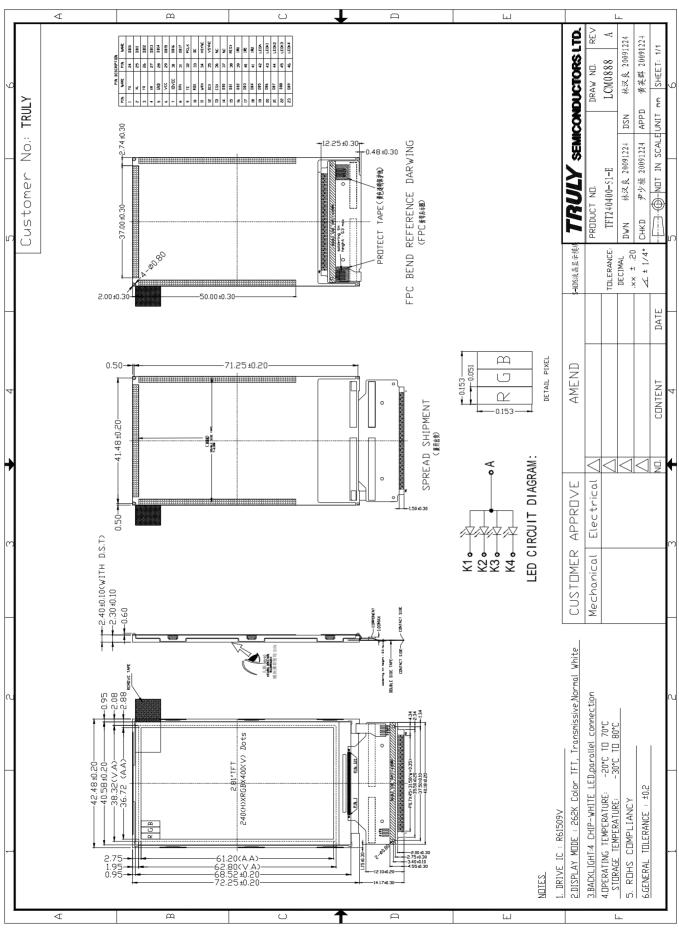
WRITTEN BY	CHECKED BY	APPROVED BY
HUANG YAN XUE	FAN JIE	ZHU JIN BAO

n GENERAL INFORMATION

Item	Contents	Unit
LCD type	TFT/TRANSMISSIVE	/
Viewing direction	12:00 O'Clock	6:00 O'Clock (Good Viewing)
Module area $(W \times H \times T)$	42.48×72.25×2.40	mm ³
Viewing area (W×H)	38.32×62.80	mm ²
Active area (W×H)	36.72×61.20	mm ²
Number of Dots	240(RGB)×400	/
Pixel pitch $(W \times H)$	0.153×0.153	mm ²
Driver IC	R61509V	/
Interface Type	MPU/SPI+RGB	/
Input voltage	2.8	V
Module Power consumption	257	mw
Colors	262K	/
Backlight Type	LED	/

n EXTERNAL DIMENSIONS

RULY®





n ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Supply voltage for logic	VCC/IOVCC	-0.3	4.6	V
Input voltage	VIN	-0.3	IOVCC+0.3	V
Operating temperature	Тор	-20	70	°C
Storage temperature	TST	-30	80	°C
Humidity	RH	-	90%(Max60 °C)	RH

n ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS

Parameter	Symbol	Min	Тур	Max	Unit
Supply voltage for logic	VCC-VSS	2.5	2.8	3.3	V
Input Current	Icc	-	11.55	23.1	mA
Input voltage 'H' level	VIH	0.8IOVCC	-	IOVCC	V
Input voltage 'L' level	VIL	-0.3	-	0.2IOVCC	V
Output voltage 'H' level	VOH	0.8IOVCC	-	-	V
Output voltage 'L' level	VOL	-	-	0.2IOVCC	V
I/O power supply	IOVCC	1.65	2.8	3.3	V

n BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward voltage	Vf	3.0	3.2	3.4	V	If=60mA
Luminance	Lv	2400	2800	3200	cd/m ²	Ta=25°C
Number of LED	-	-	4	-	Piece	-
Connection mode	P	-	Parallel	-	-	-

Using condition: constant current driving method If=60mA(+/-10%).



n ELECTRO-OPTICAL CHARACTERISTICS

LCD MODULE

Item	Symbol	Condition	Min	Тур	Max	Unit	Remark	Note	
Response time	Tr+ Tf		-	26	39	ms	Fig.1	4	
Contrast ratio	Cr	$\theta=0^{\circ}$	228	456	-		FIG 2.	1	
Luminance uniformity	δ WHITE	Ø=0° Ta=25°C	80	89	-	%	FIG 2.	3	
Surface Luminance	Lv	1a-25 C	174	218	-	cd/m ²	FIG 2.	2	
		Ø = 90°	70	80	-	deg	FIG 3.		
Viewing angle range	θ	Ø = 270°	51	61	-	deg	FIG 3.	6	
		$\emptyset = 0^{\circ}$	70	80	-	deg	FIG 3.		
		Ø = 180°	70	80	-	deg	FIG 3.		
	Red x		0.5770	0.6270	0.6770	-		5	
	Red y		0.2989	0.3489	0.3989	-			
	Green x	$\theta=0^{\circ}$	0.2571	0.3071	0.3571	-			
CIE (x, y) chromaticity	Green y	Ø=0°	0.5357	0.5857	0.6357	-	FIG 2.		
	Blue x	Ta=25°C	0.0953	0.1453	0.1953	-	110 2.	3	
	Blue y	1 a-23 C	0.0289	0.0789	0.1289	-			
	White x		0.2129	0.2729	0.3329	-			
	White y		0.2307	0.2907	0.3507	-			

Contrast Ratio(CR) is defined mathematically by the following formula. For more Note1. information see FIG 2.:

Average Surface Luminance with all white pixels (P 1,P2, P 3,P4, P5) Average Surface Luminance with all black pixels (P1, P2, P3,P4, P5)

Surface luminance is the LCD surface from the surface with all pixels displaying white. Note2. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3,P4, P5)

The uniformity in surface luminance (δ WHITE) is determined by measuring luminance at Note3. each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

Minimum Surface Luminance with all white pixels (P₁, P₂, P₃, P₄, P₅) **Maximum** Surface Luminance with all white pixels (**P**₁, **P**₂, **P**₃, **P**₄, **P**₅)

- Note4. Response time is the time required for the display to transition from White to black(Rise and from black to white(Decay Time, Tf). For additional information see FIG 1...
- CIE (x, y) chromaticity, The x,y value is determined by screen active area position 5. For Note5. more information see FIG 2.
- Note6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's Note7. ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and the testing data is base on TOPCON's BM-5 photo detector.

TFT module, Gray scale reverse occurs in the direction of panel viewing angle Note8.

FIG.1. The definition of Response Time

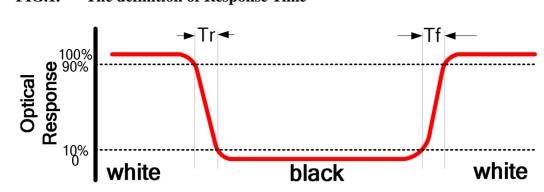


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A:5 mm

 $B:5\ mm$

H,V: Active Area

Light spot size $\varnothing = 5$ mm, 500mm distance from the

LCD surface to detector lens

measurement instrument is TOPCON's luminance

meter BM-5

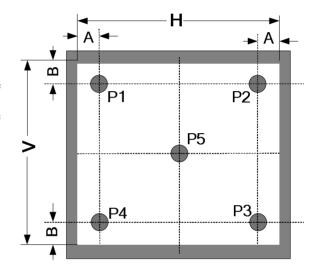
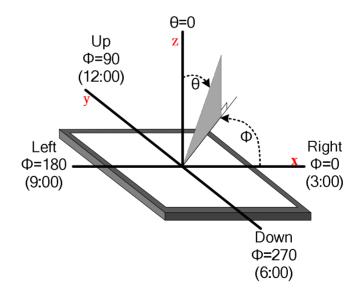


FIG.3. The definition of viewing angle





n INTERFACE DESCRIPTION

Pin No.	Symbol	Level	Description	When not in use
1	YU(NC)		No connection	-
2	XL(NC)		No connection	-
3	YD(NC)		No connection	-
4	XR(NC)		No connection	-
5	GND	0V	Ground	-
6	VCC	2.5~3.3V	Power supply for Internal VDD regulator. VCC ≥ IOVCC	-
7	IOVCC	1.65~3.3V	Power supply for interface pins.	-
8	DIN	-	Data input pin.	GND or IOVCC
9	TE	H/L	Tearing effect output pin to synchronies MCU to frame writing, activated by S/W command. When this pin is not activated, this pin is low. If not used, please open this pin.	Open
10	RDX	H/L	Read strobe signal when 80-system bus interface is selected. Data are read when Low level. (Amplitude: IOVCC-GND)	IOVCC
11	WRX	H/L	Write strobe signal when 80-system bus interface is selected. Data are written when Low level. Synchronous clock signal when clock synchronous serial interface is selected. (Amplitude: IOVCC-GND)	IOVCC
12	DCX	H/L	Display data / Command selection pin	IOVCC
13	CSX	H/L	Chip selection signal. (Amplitude: IOVCC-GND) Low: The R61509V is selected and accessible. High: The R61509V is not selected and not accessible.	-
14	DB0			
15	DB1	1		
16	DB2			
17	DB3		18-bit parallel bi-directional data bus for 80-system	
18	DB4		interface	
19	DB5		operation (Amplitude: IOVCC-GND).	
20	DB6]	8-bit I/F: DB17-DB10 are used.	
21	DB7		9-bit I/F: DB17-DB9 are used.	
22	DB8	11/1	16-bit I/F: DB17-DB10 and DB8-1 are used.	GND or
23	DB9	H/L	18-bit I/F: DB17-DB0 are used.	IOVCC
24	DB10		18-bit parallel bi-directional data bus for RGB interface	
25	DB11	1	operation	
26	DB12	1	(Amplitude: IOVCC-GND).	
27	DB13	1	16-bit I/F: DB17-DB13 and DB11-1 are used.	
28	DB14	1	18-bit I/F: DB17-DB0 are used.	
29	DB15	1		
30	DB16	1		
31	DB17	1		
32	PCLK	H/L	Pixel clock output.	GND or IOVCC

33	DE	H/L	Lov Hig The	rata enable signal for RGB interface operation. ow: accessible (selected) igh: Not accessible (Not selected) he polarity of ENABLE signal can be inverted by setting the EPL bit. (Amplitude: IOVCC-GND).						GND or IOVCC
34	HSYNC	H/L		•		nous signal, Low a	ctive.			GND or IOVCC
35	VSYNC	H/L			-	onous signal. Low OVCCGND).	active.			GND or IOVCC
36	NC		No	conr	ectio	n				1
37	NC		No	conr	ectio	n				-
38	RESX	H/L	Mal	Reset pin. The R61509V is reset when RESETX is low. Make sure to execute a power on reset after turning power on.(Amplitude: IOVCC-GND)						ı
39	IM0		Sele IM2 0		IM0 0	System Interface. (Ampli System Interface 80-system 18-bit interface	tude: IOVCC DB pins in use DB17-0	-GND) Colors 262,144		
40	IM1	H/L	0 0 0	0 1 1	1 0 1	80-system 9-bit interface 80-system 16-bit interface 80-system 8-bit interface Clock synchronous	DB17-9 DB17-10, 8-1 DB17-10	262,144 (Note 1) 262,144 (Note 2) 65536		-
41	IM2		1 1 N	1 1 ote 1:		serial interface Setting inhibited Setting inhibited 6 colors in one-transfer 6 colors in two-transfer		_ _ _		
42	LEDA	_	LEI) lig	ht an	ode				-
43	LEDK4	0V	_	LED light anode LED light cathode						-
44	LEDK3	0V		LED light cathode						-
45	LEDK2	0V								-
46	LEDK1	0V	_	ED light cathode ED light cathode						_

n reference application circuit

Please consult our technical department for detail information.



n RELIABILITY TEST CONDITIONS

LCD MODULE

No.	Test Item	Test Condition	Inspection after test
1 2 3 4	High Temperature Storage Low Temperature Storage High Temperature Operating Low Temperature Operating	$80\pm 2^{\circ}$ C/200 hours $-30\pm 2^{\circ}$ C/200 hours $70\pm 2^{\circ}$ C/120 hours $-20\pm 2^{\circ}$ C/120 hours	Inspection after 2~4hours storage at
5	Temperature Cycle	$-20\pm2^{\circ}\text{C} \sim 25 \sim 70\pm2^{\circ}\text{C} \times 10 \text{ cycles}$ (30min.) (5min.) (30min.)	room temperature, the sample shall be free from
6	Damp Proof Test	$50^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\% \text{RH}/120 \text{ hours}$	defects:
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, X, Y, Z direction for total 3hours(Packing condition)	1.Air bubble in the LCD; 2.Sealleak; 3.Non-display; 4.missing segments;
8	Dropping test	Drop to the ground from 1m height, one time, every side of carton. (Packing condition)	5.Glass crack; 6.Current Idd is twice higher than initial value.
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time	

Remark:

- 1. The test samples should be applied to only one test item.
- 2. Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance>10M Ω) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL
- 6. Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

n INSPECTION CRITERION

TRULY ® OUTGOING QUALITY STANDARD	PAGE 1 OF 4
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	MDS Product

This specification is made to be used as the standard acceptance/rejection criteria for Color mobile phone LCM.

1 Sample plan

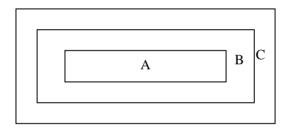
Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65 Minor defect: AQL 1.5

2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

3. Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.



Classification of

TRULY ® OUTGOING QUALITY STANDARD	PAGE 2 OF 4
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	MDS Product

4. Inspection standards

4.1 Major Defect

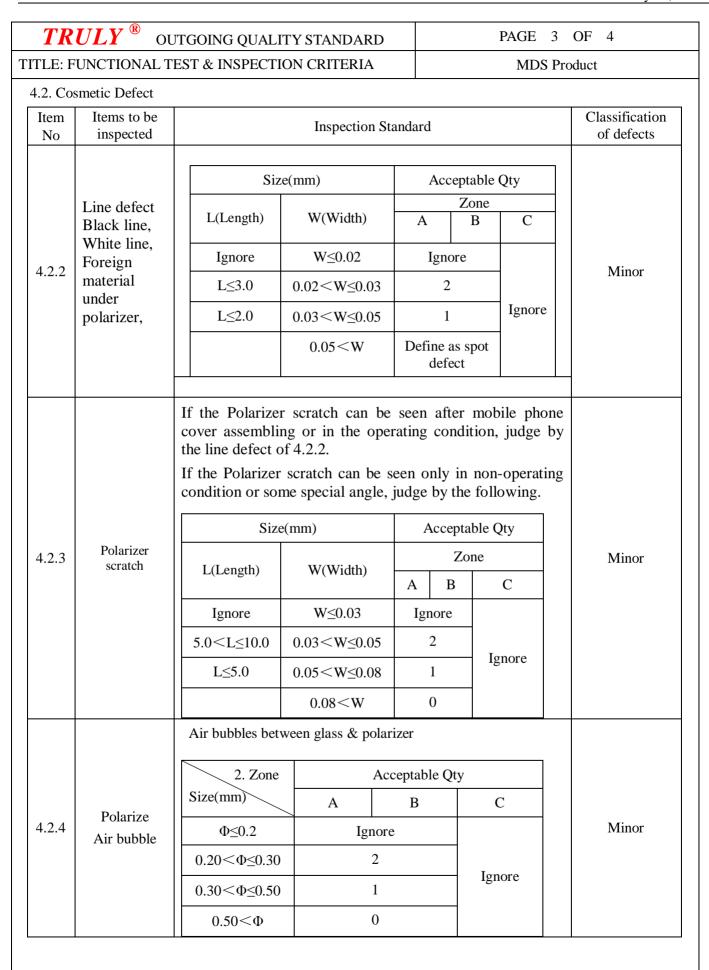
Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1.1	All functional defects	 No display Display abnormally Missing vertical, horizontal segment Short circuit Back-light no lighting, flickering and abnormal lighting. 	
4.1.2	Missing	Missing component	Major
4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	

4.2 Cosmetic Defect

Items to be

Item

Item No	Items to be inspected	Inspection Standard			Classification of defects	
	Clear Spots	For dark/white spot, as $\Phi = \frac{(x+y)}{2}$	sizeΦis defin	ed	x ty	
	Black and white Spot	1. Zone	,	Acceptable	Qty	
	defect Pinhole,	Size(mm)	A	В	С	Minor
	Foreign Particle, Dirt under	Ф≤0.10	Ig	nore]
		0.10<Φ≤0.15		2	Ignore	
	polarizer $0.15 < \Phi \le 0.2$			1	Ignore	
4.2.1		Ф>0.20		0		
	Dim Spots	2.				
	Circle	2. Zone	A	cceptable Q	ty	
	shaped and dim edged	Size(mm)	A	В	С	
	defects	Ф≤0.2	Igno	re		Minor
		0.20<Φ≤0.40	3 Ignore		Ignore	
		0.40<Φ≤0.60	2		Ignore	
			0.60<Φ≤0.80	1		
		0.80<Φ	0			





TRULY ® OUTGOING QUALITY STANDARD	PAGE 4 OF 4
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	MDS Product

4.3. Cosmetic Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects
		(i) Chips on corner X Y Z ≤2.0 ≤S Disregard Notes: S=contact pad length Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.	Minor
4.3.5	Glass defect	(ii)Usual surface cracks X	Minor
	(iii) Crack Cracks tend to break are not allowed.		
4.3.6	Parts alignment	 Not allow IC and FPC/heat-seal lead width is more than 50% beyond lead pattern. Not allow chip or solder component is off center more than 50% of the pad outline. 	Minor
4.3.7	SMT	According to the <acceptability assemblies="" electronic="" of=""> IPC-A-610C class 2 standard. Component missing or function defect are Major defect, the others are Minor defect.</acceptability>	

n PRECAUTIONS FOR USING LCD MODULES

1 Handing Precautions

- 1.1 The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- 1.2 If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- 1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- 1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- 1.5 If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- 1.6 Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.

- 1.7 Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- 1.8 Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- 1.9 Do not attempt to disassemble or process the LCD module.
- 1.10 NC terminal should be open. Do not connect anything.
- 1.11 If the logic circuit power is off, do not apply the input signals.
- 1.12 Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
 - To reduce the amount of static electricity generated, do not conduct assembling



and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

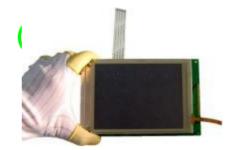
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- 1.13 Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist the LCM.

LCD MODULE

2 Handling precaution for LCM

- 2.1 LCM is easy to be damaged. Please note below and be careful for handling.
- 2.2 Correct handling:





Version: 1.0

As above picture, please handle with anti-static gloves around LCM edges.

2.3 Incorrect handling:



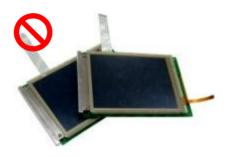
Please don't touch IC directly.



Please don't hold the surface of panel.



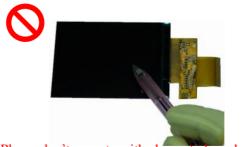
Please don't hold the surface of IC.



Please don't stack LCM.



Please don't stretch interface of output, such as FPC cable.



Please don't operate with sharp stick such as pens.



3 Storage Precautions

- 3.1 When storing the LCD modules, the following precaution are necessary.
 - 3.1.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
 - 3.1.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
 - 3.1.3 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).

3.2 Others 其它

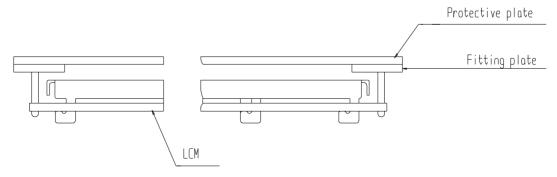
- 3.2.1 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- 3.2.2 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3.2.3 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
 - 3.2.3.1 Exposed area of the printed circuit board.
 - 3.2.3.2 Terminal electrode sections.

4 USING LCD MODULES

4.1 Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

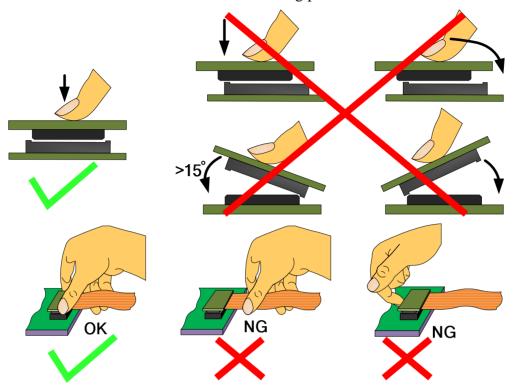
4.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



4.1.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

4.2 Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows





4.3 Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS Product	290°C ~350°C.	330°C ~350°C.	300°C ~330°C.
	Time : 3-5S.	Speed: 15-17 mm/s.	Time : 3-6S.
			Press: 0.8~1.2Mpa
RoHS Product	340°C ~370°C.	350°C ~370°C.	330°C ~360°C.
	Time : 3-5S.	Speed: 15-17 mm/s.	Time : 3-6S.
			Press: 0.8~1.2Mpa

- 4.3.1 If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- 4.3.2 When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 4.3.3 When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

4.4 Precautions for Operation

- 4.4.1 Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- 4.4.2 It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- 4.4.3 Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.
- 4.4.4 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- 4.4.5 A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50% RH or less is required.
- 4.4.6 Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
- 4.4.7 Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

4.5 Safety

- 4.5.1 It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- 4.5.2 If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

4.6 Limited Warranty

Unless agreed between TRULY and the customer, TRULY will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with TRULY LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to TRULY within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of TRULY limited to repair and/or replace on the terms set forth above. TRULY will not be responsible for any subsequent or consequential events.

4.7 Return LCM under warranty

- 4.7.1 No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :
 - 4.7.1.1 Broken LCD glass.
 - 4.7.1.2 PCB eyelet is damaged or modified.
 - 4.7.1.3 -PCB conductors damaged.
 - 4.7.1.4 Circuit modified in any way, including addition of components.
 - 4.7.1.5 PCB tampered with by grinding, engraving or painting varnish.
 - 4.7.1.6 Soldering to or modifying the bezel in any manner.
- 4.7.2 Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

n PACKING SPECIFICATION

Please consult our technical department for detail information.

n PRIOR CONSULT MATTER

- 1 For Truly standard products, we keep the right to change material, process ... for improving the product property without prior notice to our customer.
- 2 For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.
- 3 If you have special requirement about reliability condition, please let us know before you start the test on our samples.

n FACTORY CONTACT INFORMATION

FACTORY NAME: TRULY SEMICONDUCTORS LTD.

FACTORY ADDRESS: Truly Industrial Area, ShanWei City, Guang Dong, China

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