

TFT LCD Tentative Specification

MODEL NO.: G070Y3-T01

Customer: _____

Approved by: _____

Note:

Liquid Crystal Display Division	
QRA Division.	OA Head Division.
Approval	Approval

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REVISION HISTORY

Version	Date	Section	Description
Ver 0.0	Jan,23, 07'	All	G070Y3-T01 Tentative specifications was first issued.

1. GENERAL DESCRIPTION

1.1 OVERVIEW

G070Y3-T01 is a 6.95 inch TFT Liquid Crystal Display module with a LED backlight unit and a-50-pin-and-1ch-TTL interface. This module supports 800 (R.G.B)x 480 WVGA mode which main application is the automotive Monitor and industrial field.

1.2 FEATURES

- Wide viewing angle.
- Fast response time
- WVGA (800 x 480 pixels) resolution
- Wide operating temperature
- Reversible - scan function
- Digital interface

1.3 APPLICATION

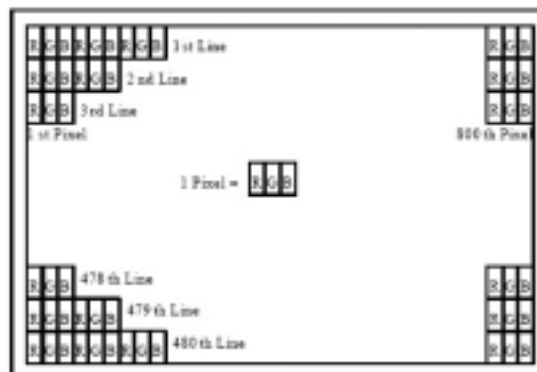
- Automotive Monitor
- Industry Application

1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Diagonal Size	6.95 Inches	mm	
Active Area	156x82.8	mm	(1)
Bezel Opening Area	158.3x85.1	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	800xR.G.B.x480	pixel	-
Pixel Pitch	0.1950x0.1725	mm	-
Pixel Arrangement	RGB vertical stripe	-	(2)
Display Colors	262.144 (6 bits)	color	-
Display Mode	Normal White	-	-
Surface Treatment	Hard Coating (TBD), AG (Haze 25 %)	-	-
Weight	170(Typ)	g	

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

Note (2)



1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	167.7	168	168.3	mm	(1)
	Vertical(V)	93.7	94	94.3	mm	
	Depth(D)	-	5.7	6.0	mm	

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

No.	Test Item	Test Condition	Note
1	High Temperature Storage	90 , 240 hours	(1) (2)
2	Low Temperature Storage	-40 , 240 hours	
3	Heat Shock Operating	{(-40 , 0.5 hour) (85 , 0.5 hour)}, 100 cycles	
4	High Temperature Operating	85 , 240 hours	
5	Low Temperature Operating	-30 , 240 hours	
6	High Temperature & High Humidity Operating	60 , RH 90%, 240hours	
7	Damp Heat Cyclic	{(25 , 90%RH, 12 hours) (55 , 90%RH, 12 hours)}, 6 cycles	
8	Shock (Non-Operating)	100G, 6ms, +/-XYZ 3 times	(3)(5)
9	Vibration (Non-Operating)	3G, 10 to 200 Hz, sine wave	(4)(5)

Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. ($T_a \leq 40$).

(b) Wet-bulb temperature should be 39 °C Max. ($T_a > 40$).

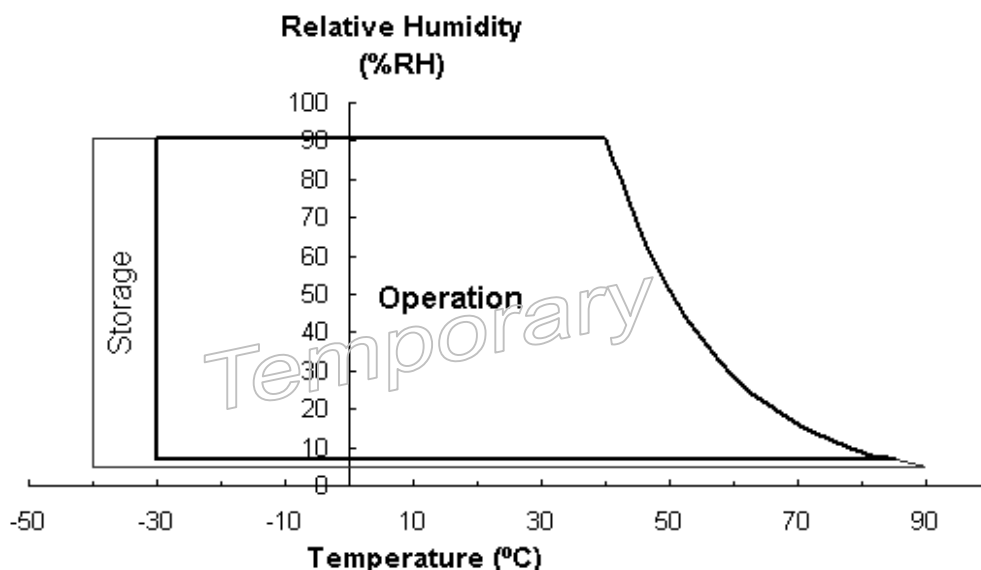
(c) No condensation.

Note (2) The temperature of panel display surface area should be 90 °C Max.

Note (3) 6ms, half sine wave, 3 times for +/-X, +/-Y, +/-Z.

Note (4) 3 directions: X, Y and Z axes, 60min per each direction; 6 cycles; sweep time = 5 minutes; peak acceleration = 3G; frequency = 10 to 200 Hz; sine wave.

Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.



2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	VCC	-0.3	-	5	V	-
	AVDD	-0.3	-	13.5	V	-
	VGH	-0.3	-	42	V	-
	VGL	VGH-42	-	0.3	V	-
Digital Input Voltage	V_i	-0.5	-	Vcc+0.5	V	(1)
Gamma Supply Voltage	V1~V5	0.4AVDD	-	AVDD+0.3	V	-
	V6~V10	-0.3	-	0.6AVDD	V	-

Note (1) V_i means all input logic signal.

2.2.2 LED BACKLIGHT UNIT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Voltage	V_f	25.2	32.4	V	(1), (2), $I_f = 20$ mA
Current	I_f		20	mA	(1), (2)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to 3.3 for further I_f information).

3. ELECTRICAL CHARACTERISTIC

3.1 Recommended Operation condition (GND = AVSS = 0V)

Ta = 25 ± 2 °C

Parameter	Symbol	Value			Unit	Note	
		Min.	Typ.	Max.			
Power Supply Voltage	VCC	3.0	3.3	3.6	V		
	AVDD	10.29	10.5	10.71	V		
	VGH	17.5	18	18.5	V		
	VGL	-7.5	-7	-6.5	V		
Input Signal Voltage	V1~V5	0.4AVDD	-	AVDD+0.3	V	(1)	
	V6~V10	-0.3	-	0.6AVDD	V	(1)	
	VCOM	-	4.035	-	V		
Digital Input Voltage	High Level	V _{IH}	0.7VCC	-	VCC	V	
	Low Level	V _{IL}	0	-	0.3VCC	V	

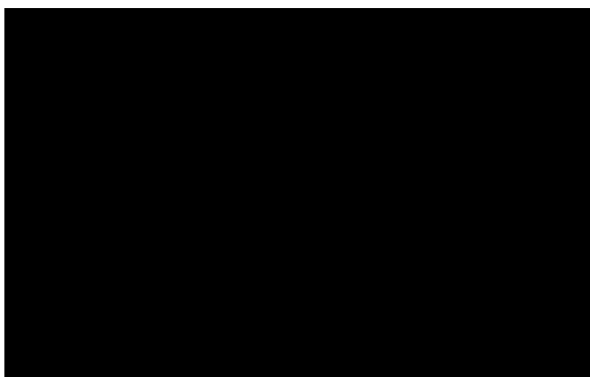
Note: (1) Please refer to 6.3 application notes.

3.2 Current Consumption (GND = AVSS =0V)

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Supply Current for Source/Gate Driver (Digital)	I _{CC}	-	(2.86)	TBD	mA	(1)
Supply Current for Source Driver (Analog)	I _{DD}	-	(21)	TBD	mA	(1)
Supply Current for Gate Driver (High Level)	I _{GG}	-	(0.13)	TBD	mA	(1)
Supply Current for Gate Driver (Low Level)	I _{EE}	-	(0.13)	TBD	mA	(1)

Note: (1) The specified power supply current is under the conditions at VCC = 3.3 V, Ta = 25 ± 2 °C, f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.

Black Pattern



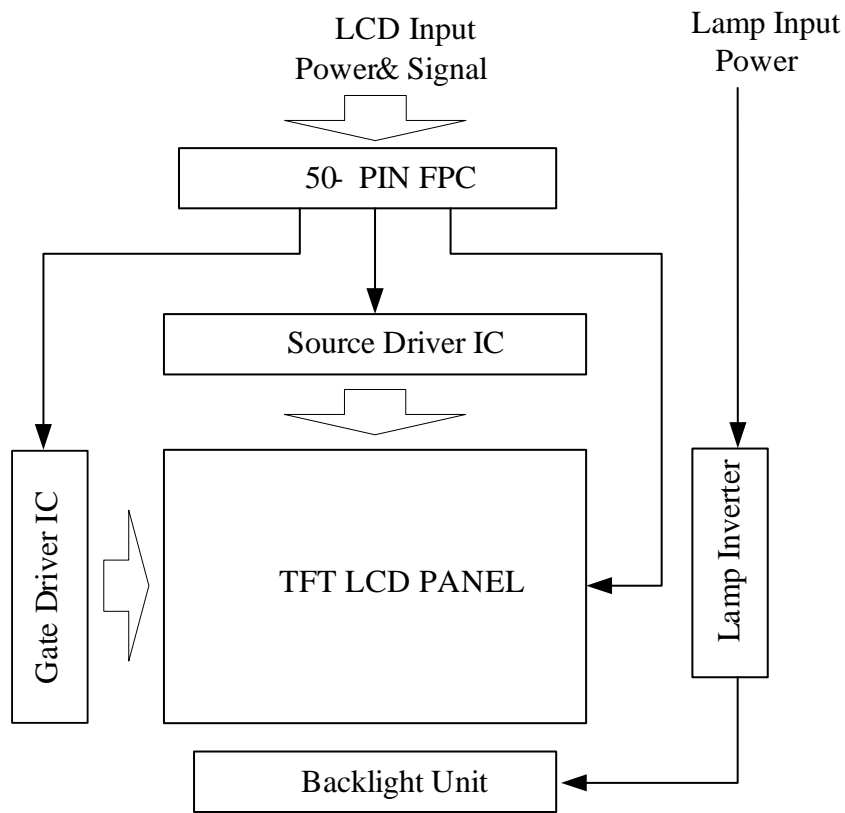
Active Area

3.3 BACKLIGHT UNIT

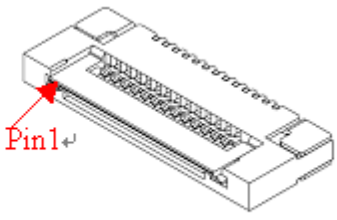
TBD

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 BACKLIGHT UNIT

<p>Pin1: V_{CC}, (25.2~32.4V) (V_o will depend on LED V_f) Pin2: Channel 1(9Series), Pin3: Channel 2(9Series), Pin4: Channel 3(9Series)</p>	 <p>(下接触式)</p>
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5. INPUT TERMINAL PIN ASSIGNMENT

5.1 FPC I/O Pin Assignment

Pin	Name	I/O	Description
1	VCOM	I	VCOM Voltage
2	DIO1	I/O	Start Pulse Signal Input/Output (Horizontal)
3	CLK	I	Source Driver Shift Clock Input
4	SHL	I	Source Driver Shift Direction Control Input
5	D00	I	Red Data
6	D01	I	Red Data
7	D02	I	Red Data
8	D03	I	Red Data
9	D04	I	Red Data
10	D05	I	Red Data
11	D10	I	Green Data
12	D11	I	Green Data
13	D12	I	Green Data
14	D13	I	Green Data
15	D14	I	Green Data
16	D15	I	Green Data
17	V1	I	Gamma Voltage 1
18	V2	I	Gamma Voltage 2
19	V3	I	Gamma Voltage 3
20	V4	I	Gamma Voltage 4
21	V5	I	Gamma Voltage 5
22	V6	I	Gamma Voltage 6
23	V7	I	Gamma Voltage 7
24	V8	I	Gamma Voltage 8
25	V9	I	Gamma Voltage 9
26	V10	I	Gamma Voltage 10
27	D20	I	Blue Data
28	D21	I	Blue Data
29	D22	I	Blue Data
30	D23	I	Blue Data
31	D24	I	Blue Data
32	D25	I	Blue Data
33	LD	I	Latching and Data Switching Input
34	REV	I	Data Inverting Input
35	POL	I	Polarity Inverting Input
36	VCC	I	Digital Supply Voltage
37	GND	I	Ground
38	AVDD	I	Source Driver Analog Supply Voltage
39	DIO2	I/O	Start Pulse Signal Input/Output (Horizontal)
40	GND	I	Ground

41	XAO	I	Output All-on Control
42	OE	I	Gate Driver Output Enable Control
43	UD_RL	I	Up/Down Scan Selection
44	CKV	I	Gate Driver Shift Clock input
45	STVU	I/O	Start Pulse Signal Input/Output (Vertical)
46	STVD	I/O	Start Pulse Signal Input/Output (Vertical)
47	VCC	I	Digital Supply Voltage
48	VGL	I	TFT Low Voltage
49	VGH	I	TFT High Voltage
50	GND	I	Ground

Note (1) User's connector Part No: 089H50-000000-G2-C (Starconn) or equivalent

5.2 SCANNING DIRECTION

The following figures are seen from a front view and the arrow shows the direction of scan.

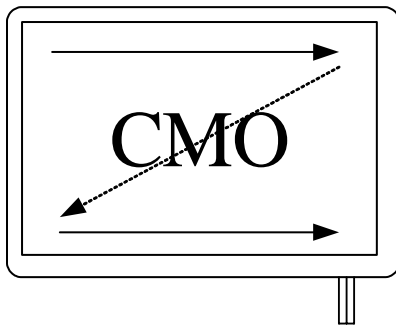


Figure1.Normal scan

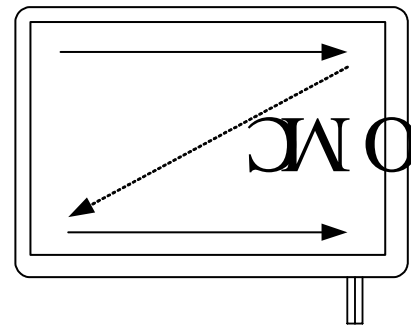


Figure 2. Reverse scan

Note : (1) Normal Scan

SHL	U/D	DIO1	DIO2	STVU	STVD	Shift
1	0	Input	Output	Input	Output	Up to down Left to right

(2) Reverse Scan

SHL	U/D	DIO1	DIO2	STVU	STVD	Shift
0	1	Output	Input	Output	Input	Down to Up Right to left

5.3 BACKLIGHT UNIT

TBD

5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

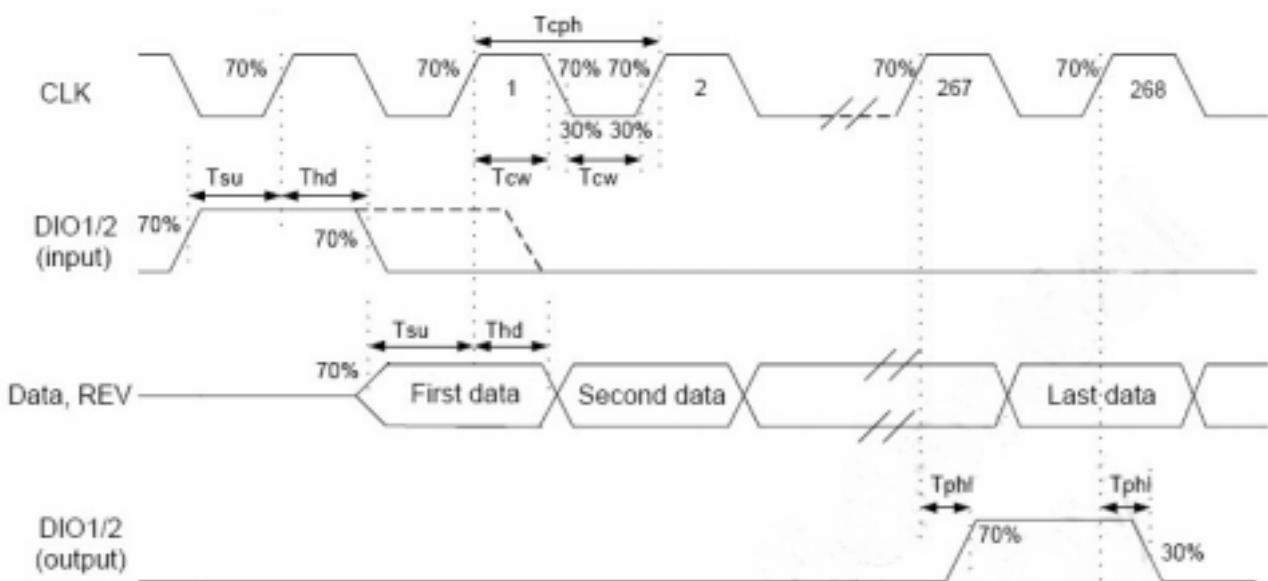
Note (1) 0: Low Level Voltage, 1: High Level Voltage

6. INTERFACE TIMING

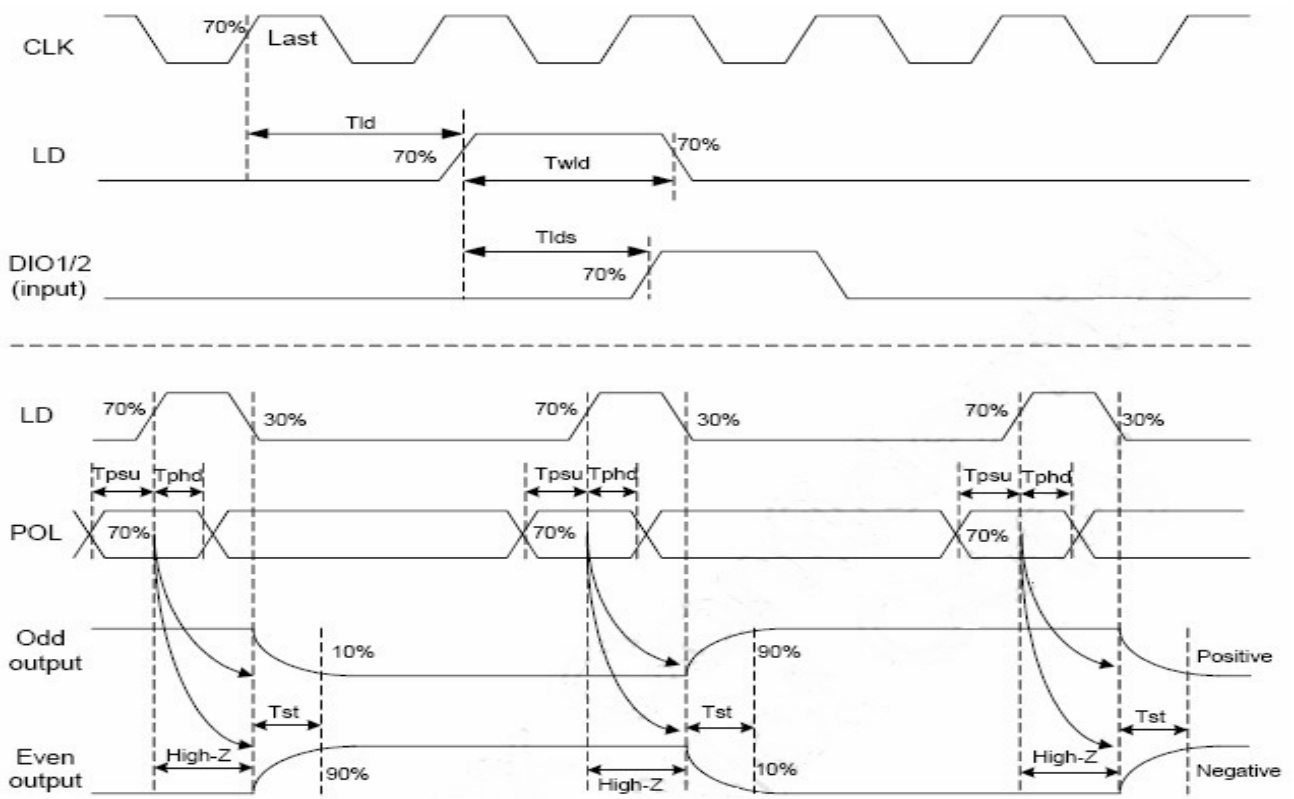
6.1 AC Electrical Characteristics (VCC = 3.3V, AVDD = 8.4V, AVSS = GND = 0V, Ta = 25)

Parameter	Symbol	Value			Unit	Condition
		Min.	Typ.	Max.		
CLK frequency	Fclk	-	40	45	MHz	-
CLK Pulse width	Tcw	40%	-	60%	T _{CLK}	-
Data setup time	Tsu	4	-	-	ns	D00~D25, REV and DIO1/2 to CLK
Data hold time	Thd	2	-	-	ns	D00~D25, REV and DIO1/2 to CLK
Propagation delay of DIO2/1	Tphl	6	10	15	ns	CL = 25pF (Output)
Time that the last data to LD	Tld	1	-	-	T _{CLK}	-
Pulse width of LD	Twld	2	-	-	T _{CLK}	-
Time that LD to DIO1/2	Tlds	5	-	-	T _{CLK}	-
POL setup time	Tpsu	6	-	-	ns	POL to LD
POL hold time	Tphd	6	-	-	ns	POL to LD
Output stable time	Tst	-	-	12	us	10% or 90% target voltage, CL = 60pF, R = 2KΩ
CKV period	t _{CPV}	5	-	-	us	-
CKV pulse width	t _{CPVH} , t _{CPVL}	2.5	-	-	us	50% duty cycle
OE pulse width	t _{WOE}	1	-	-	us	-
XAO pulse width	t _{WXAO}	10	-	-	us	-
Data setup time	t _{su}	700	-	-	ns	-
Data hold time	t _{hd}	700	-	-	ns	-
Output delay time (1)	t _{Pd1}	-	-	1000	ns	CL = 300pF
Output delay time (2)	t _{Pd2}	-	-	800	ns	CL = 30pF
Output delay time (3)	t _{Pd3}	-	-	800	ns	CL = 300pF
Output delay time (4)	t _{Pd4}	-	-	10000	ns	CL = 300pF

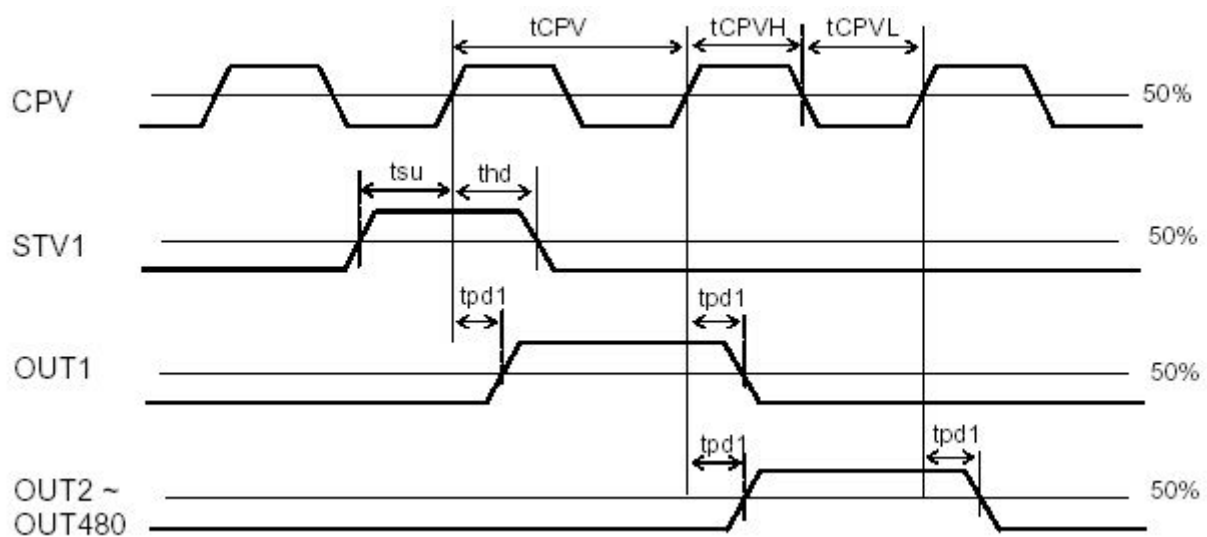
■ Timing Diagram 1

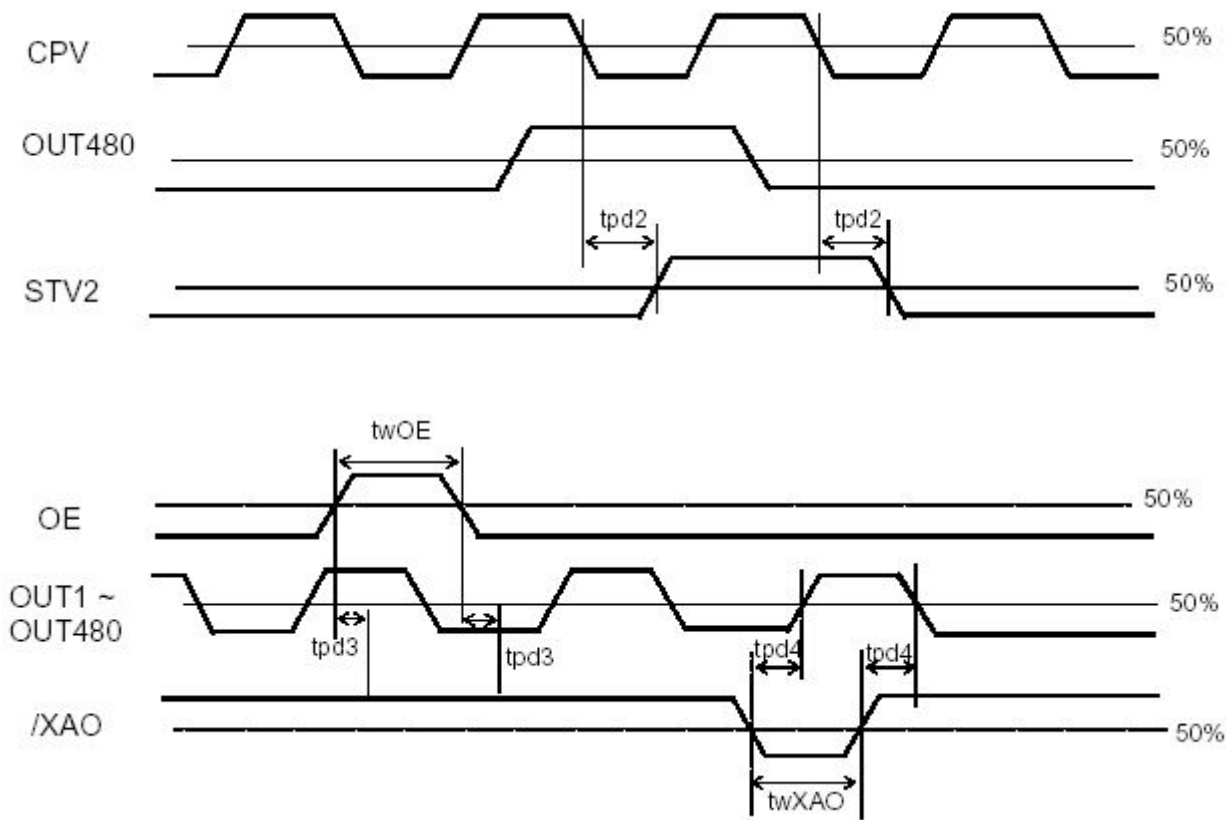


■Timing Diagram 2

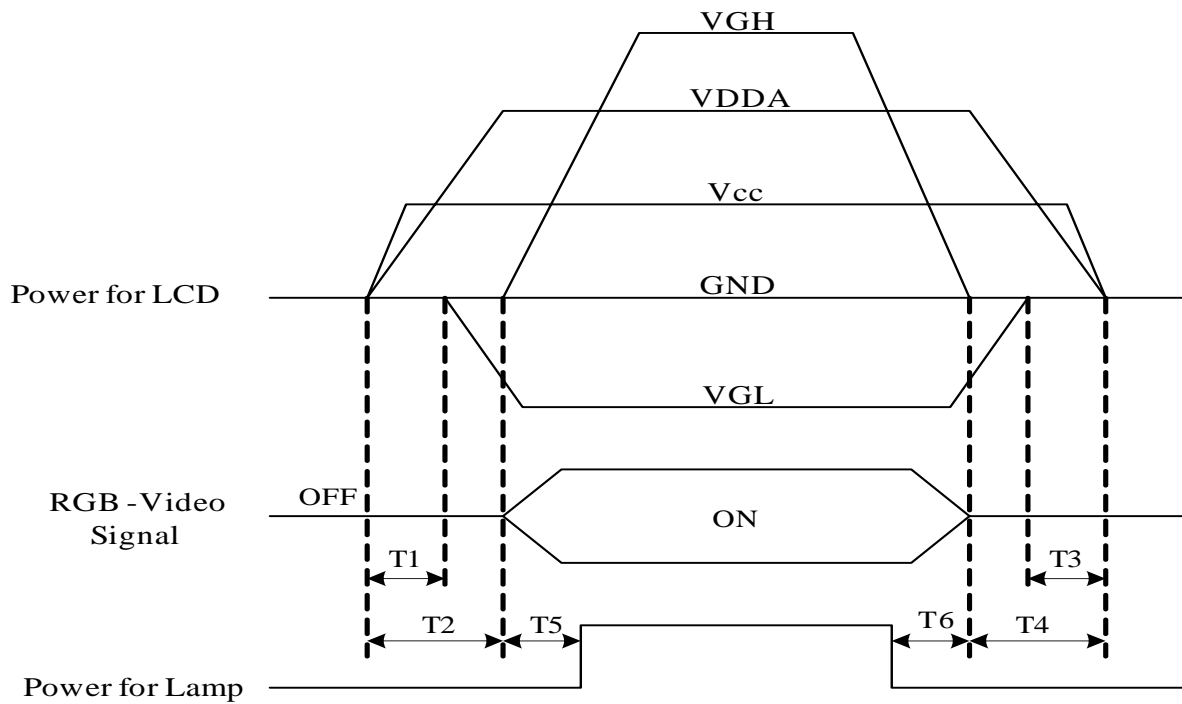


■Timing Diagram 3





6.2 POWER ON/OFF SEQUENCE



Timing Specifications:

0ms $T1 < T2$

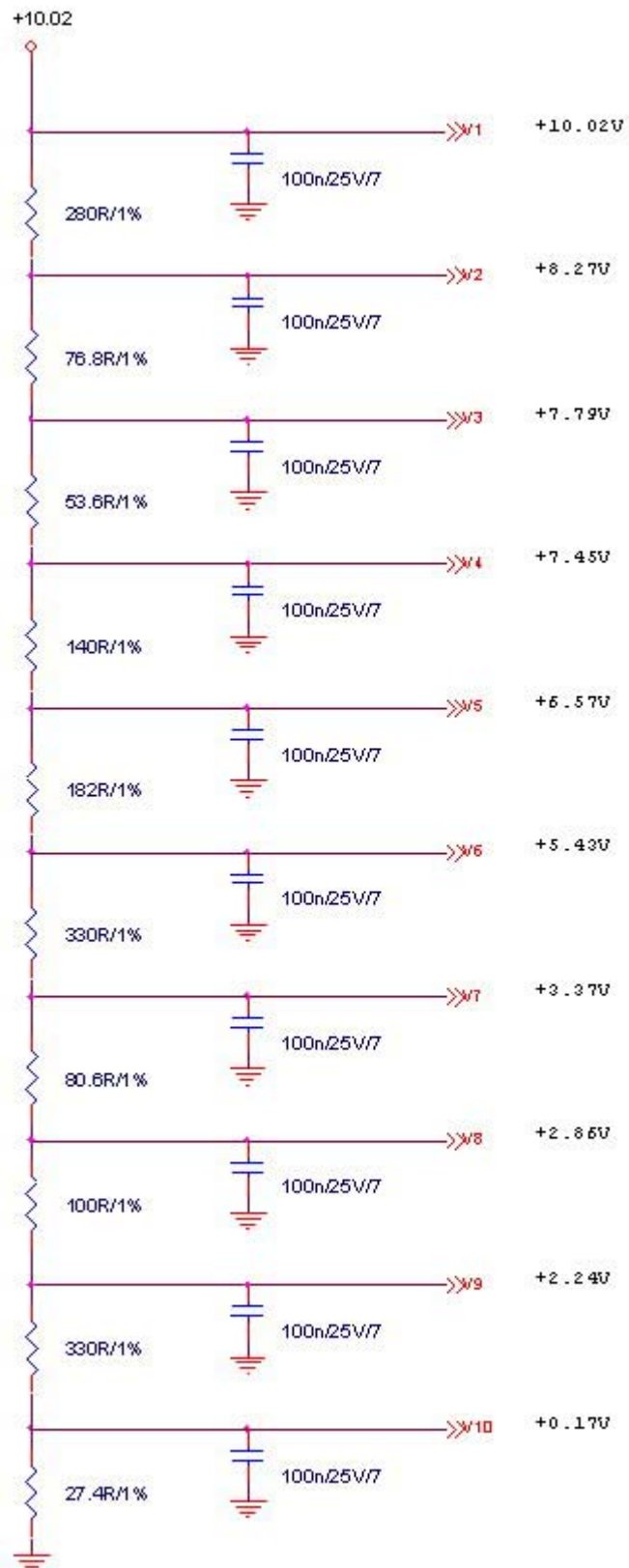
0ms $< T3 < T4$

0ms $T5$

0ms $T6$

6.3 APPLICATION NOTES

AVDD	10.5
V1	10.02
V2	8.27
V3	7.79
V4	7.45
V5	6.57
V6	5.43
V7	3.37
V8	2.86
V9	2.24
V10	0.17
VCOM	4.035



7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V _{CC}	3.3	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Current	I _f	20	mA

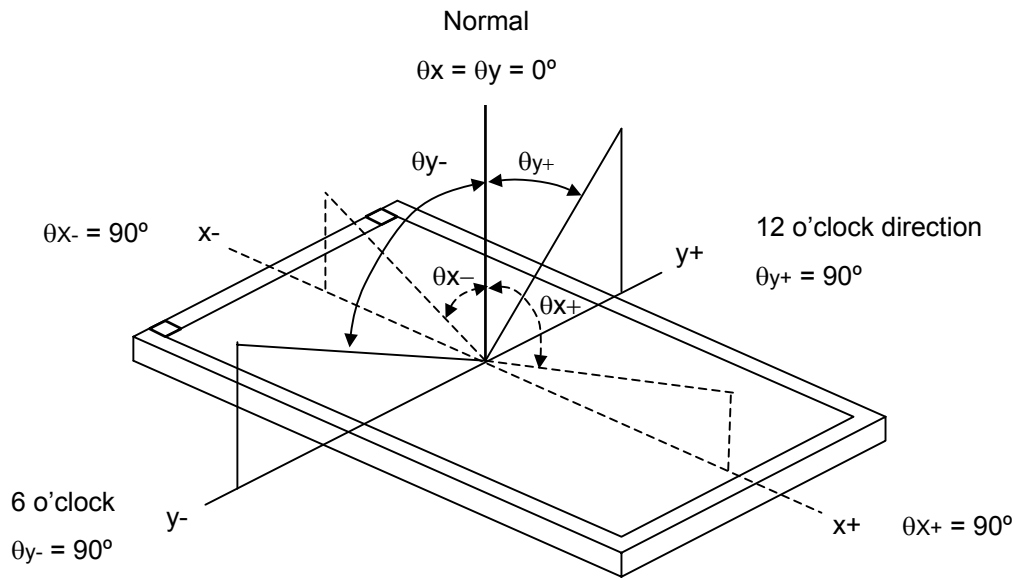
Note (1) I_f means the forward current of each channel

7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (6).

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Color Chromaticity	Red	Rx	Typ - 0.03	TBD	Typ + 0.03		(1), (6)	
		Ry		TBD				
	Green	Gx		TBD				
		Gy		TBD				
	Blue	Bx		TBD				
		By		TBD				
	White	Wx		0.313				
		Wy		0.329				
Center Luminance of White	L _C	θ _x =0°, θ _y =0° Viewing Normal Angle	TBD	(500)	-	cd/m ²	(4), (6)	
Contrast Ratio	CR		TBD	(500)	-	-	(2), (6)	
Response Time	T _R		-	(5)	(10)	Ms	(3)	
	T _F		-	(11)	(16)	Ms		
White Variation	δW		-	(1.25)	(1.4)	-	(5), (6)	
Viewing Angle	Horizontal		θ _{x+}	(65)	(70)	-	Deg.	(1), (6)
			θ _{x-}	(65)	(70)	-		
	Vertical		θ _{y+}	(55)	(60)	-		
			θ _{y-}	(55)	(60)	-		
			CR 10					

Note (1) Definition of Viewing Angle (θ_x, θ_y):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

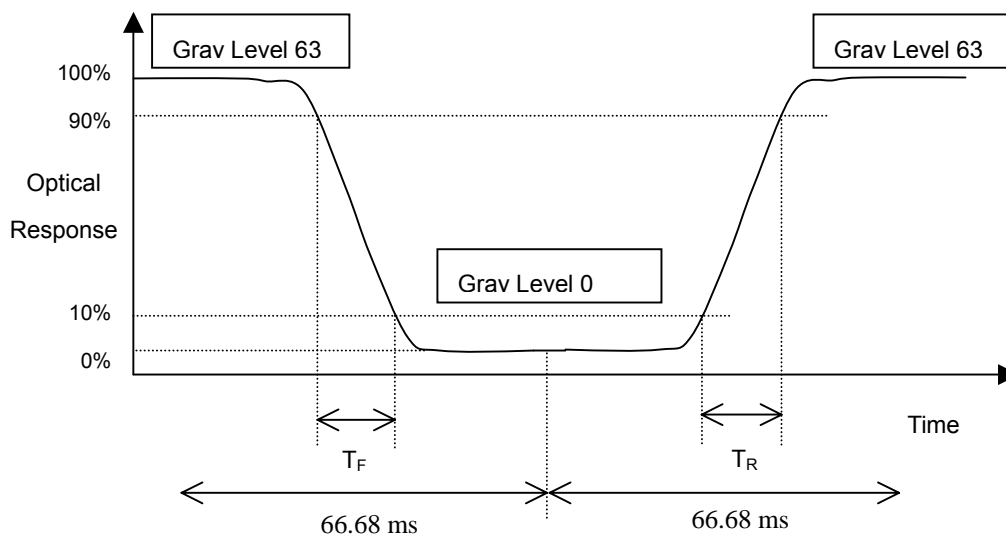
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

$$\text{CR} = \text{CR} (5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5).

Note (3) Definition of Response Time (T_R, T_F) and measurement method:



Note (4) Definition of Luminance of White (L_C):

Measure the luminance of gray level 63 at center point

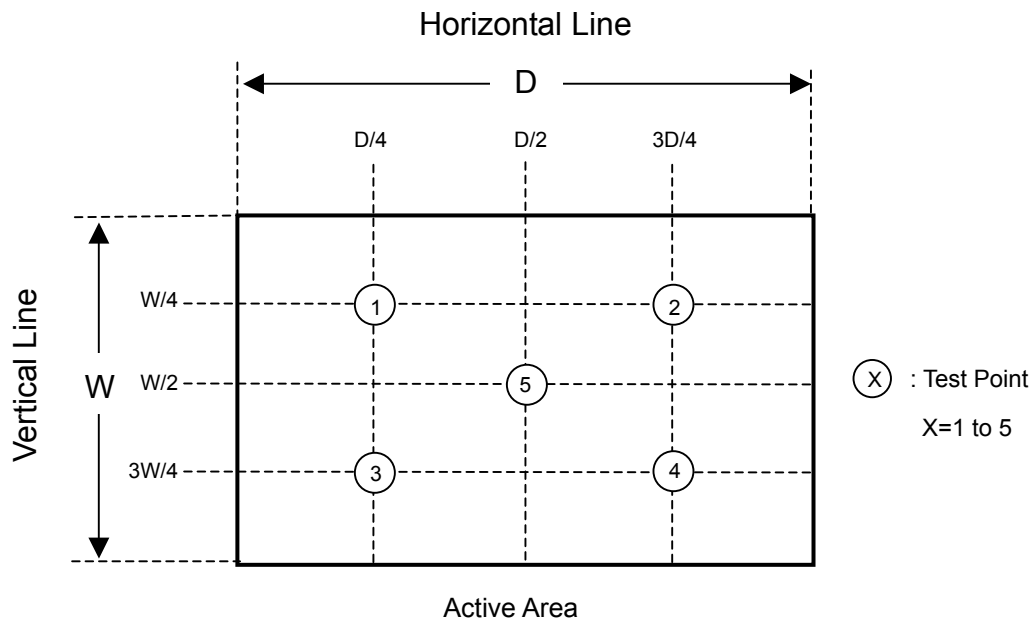
$$L_C = L(5)$$

$L(x)$ is corresponding to the luminance of the point X at Figure in Note (5).

Note (5) Definition of White Variation (δW):

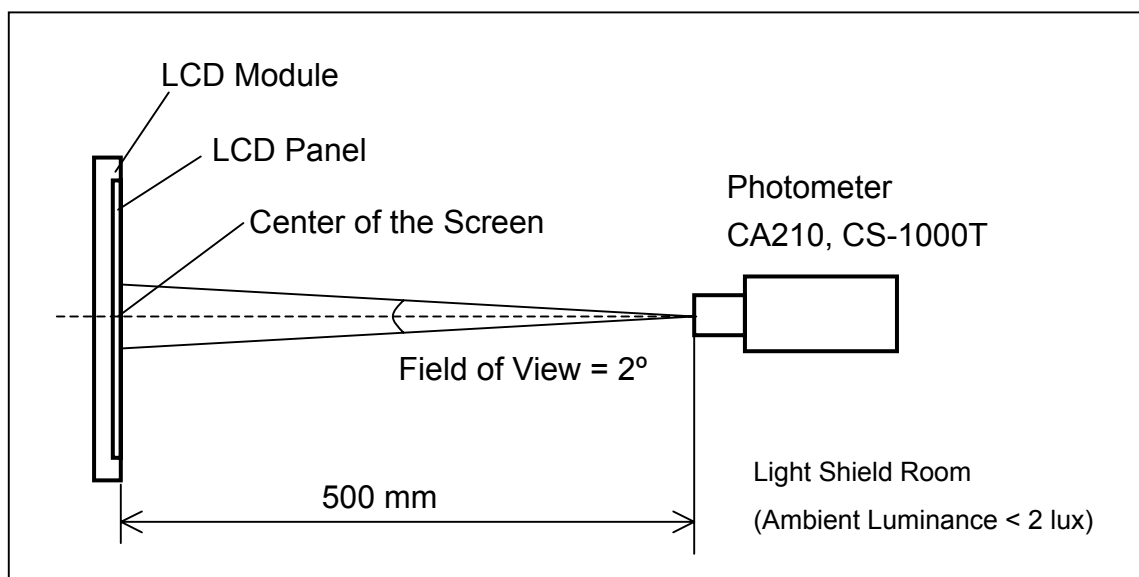
Measure the luminance of gray level 63 at 5 points

$$\delta W = \text{Maximum} [L(1), L(2), L(3), L(4), L(5)] / \text{Minimum} [L(1), L(2), L(3), L(4), L(5)]$$



Note (6) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



8. PACKAGING

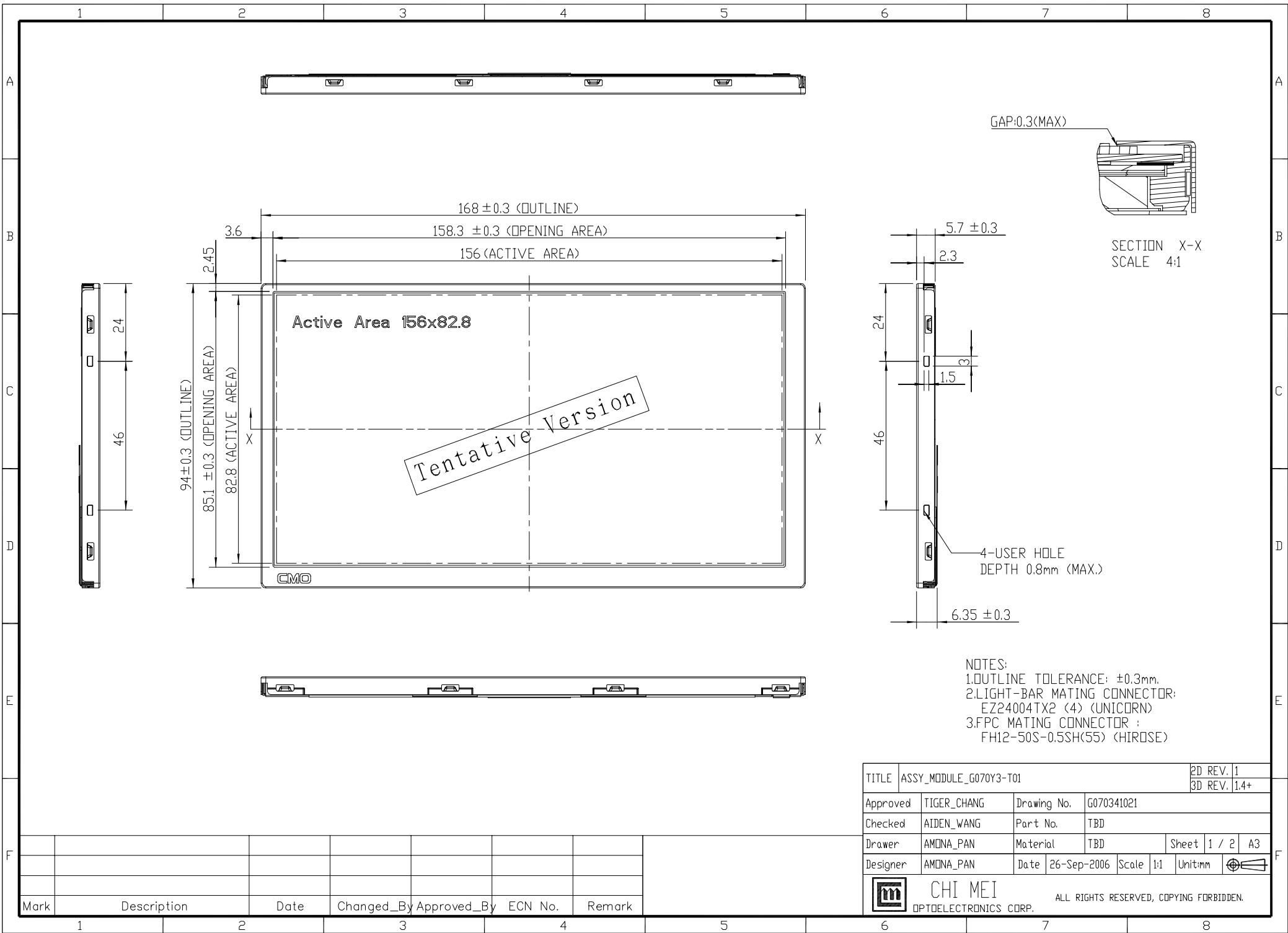
TBD

9. DEFINITION OF LABELS

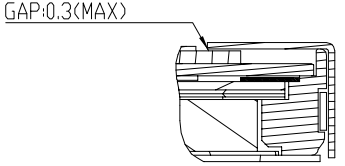
TBD

10. PRECAUTIONS

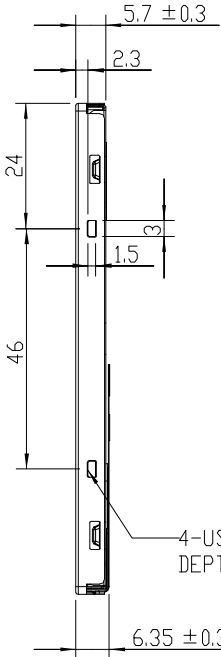
TBD



Tentative Version



SECTION X-X
SCALE 4:1



4-USER HOLE
DEPTH 0.8mm (MAX.)

- NOTES:
 1. OUTLINE TOLERANCE: ± 0.3mm.
 2. LIGHT-BAR MATING CONNECTOR:
 EZ24004TX2 (4) (UNICORN)
 3. FPC MATING CONNECTOR :
 FH12-50S-0.5SH(55) (HIROSE)

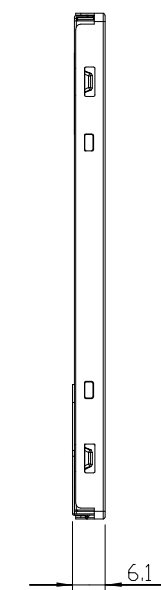
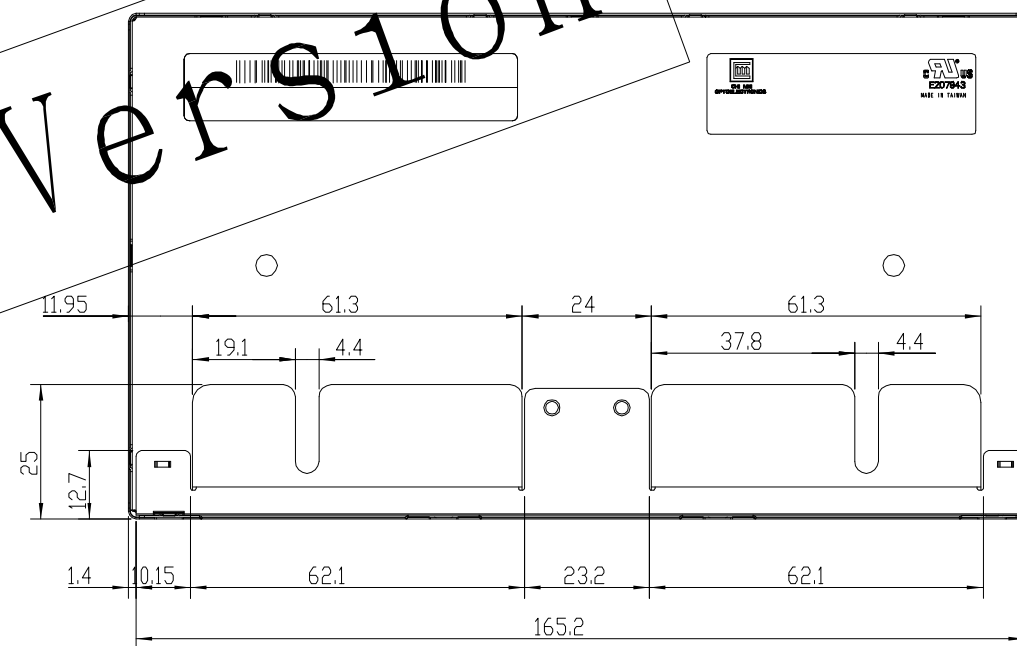
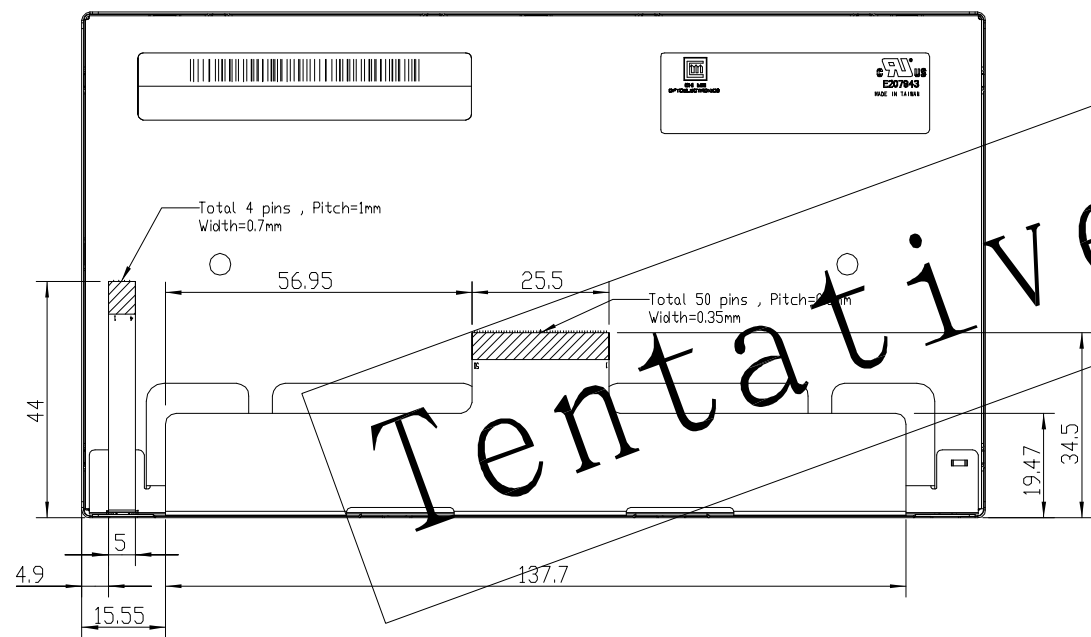
TITLE		ASSY_MODULE_G070Y3-T01		2D REV. 1	
				3D REV. 1,4+	
Approved	TIGER_CHANG	Drawing No.	G070341021		
Checked	AIDEN_WANG	Part No.	TBD		
Drawer	AMONA_PAN	Material	TBD	Sheet	1 / 2 A3
Designer	AMONA_PAN	Date	26-Sep-2006	Scale	1:1 Unit:mm



CHI MEI
OPTOELECTRONICS CORP.

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Mark	Description	Date	Changed_By	Approved_By	ECN No.	Remark



Tentative Version

BACK VIEW WITHOUT FPC&LIGHTBAR

NOTES:
 1. OUTLINE TOLERANCE: $\pm 0.3\text{mm}$.
 2. LIGHT-BAR MATING CONNECTOR:
 EZ24004TX2 (4) (UNICORN)
 3. FPC MATING CONNECTOR:
 FH12-50S-0.5SH(55) (HIROSE)

TITLE		ASSY_MODULE_G070Y3-T01		2D REV. 1	
				3D REV. 1.4+	
Approved	TIGER_CHANG	Drawing No.	G070341021		
Checked	AIDEN_WANG	Part No.	TBD		
Drawer	AMDNA_PAN	Material	TBD	Sheet	2 / 2 A2
Designer	AMDNA_PAN	Date	22-Jun-2007	Scale	1:1 Unit:mm
		CHI MEI		OPTOELECTRONICS CORP.	
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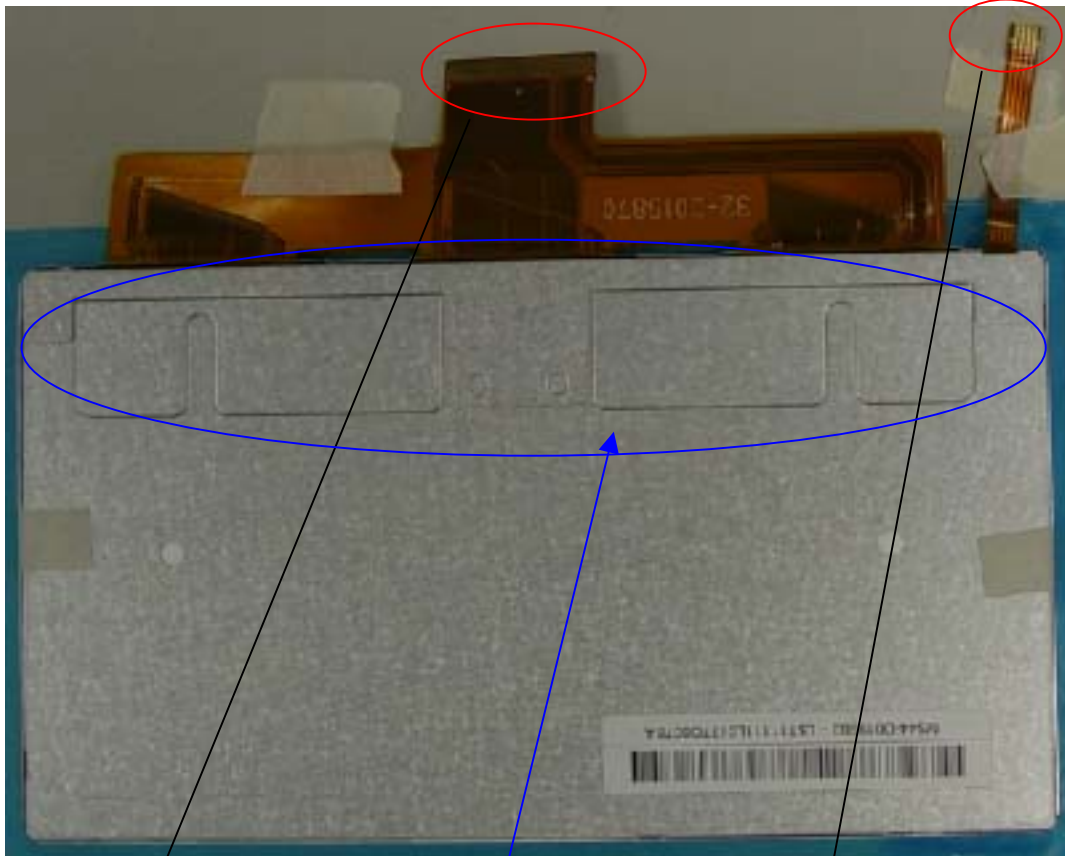
Mark	Description	Date	Changed_By	Approved_By	ECN No.	Remark
1						
2						
3						

MODEL NO.: G070Y3-T01

To : All customer

Subject: New and Old FPC I/F pin define comparison

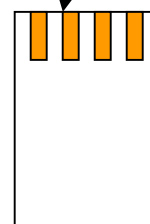
New replaceable Light Bar module FPC gold print on bottom side



Replaceable LED light bar



Gold print



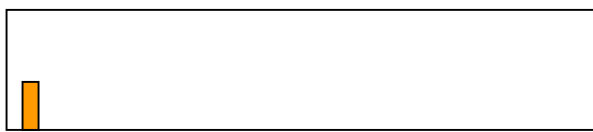
LED FPC pin define

Pin 1 : VCC
Pin 2 : CH1 feedback
Pin 3 : CH2 feedback
Pin 4 : CH3 feedback

I/F FPC pin define

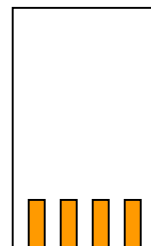
Pin 1 : V-COM	Pin 27 : B0
Pin 2 : DIO1	Pin 28 : B1
Pin 3 : CLK	Pin 29 : B2
Pin 4 : SHL	Pin 30 : B3
Pin 5 : R0	Pin 31 : B4
Pin 6 : R1	Pin 32 : B5
Pin 7 : R2	Pin 33 : TP1
Pin 8 : R3	Pin 34 : REV
Pin 9 : R4	Pin 35 : POL
Pin 10 : R5	Pin 36 : VCC
Pin 11 : G0	Pin 37 : GND
Pin 12 : G1	Pin 38 : VAA
Pin 13 : G2	Pin 39 : DIO2
Pin 14 : G3	Pin 40 : GND
Pin 15 : G4	Pin 41 : XAO
Pin 16 : G5	Pin 42 : OE
Pin 17 : Gamut 1	Pin 43 : UD/RL
Pin 18 : Gamut 2	Pin 44 : CKV
Pin 19 : Gamut 3	Pin 45 : STVU
Pin 20 : Gamut 4	Pin 46 : STVD
Pin 21 : Gamut 5	Pin 47 : VCC
Pin 22 : Gamut 6	Pin 48 : VGL
Pin 23 : Gamut 7	Pin 49 : VGH
Pin 24 : Gamut 8	Pin 50 : GND
Pin 25 : Gamut 9	
Pin 26 : Gamut 10	

Old BLU module FPC gold print on top side



1

Gold print



1

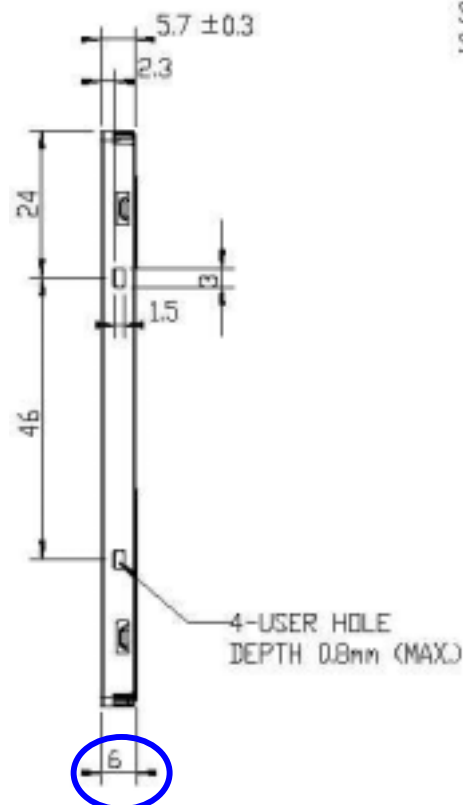
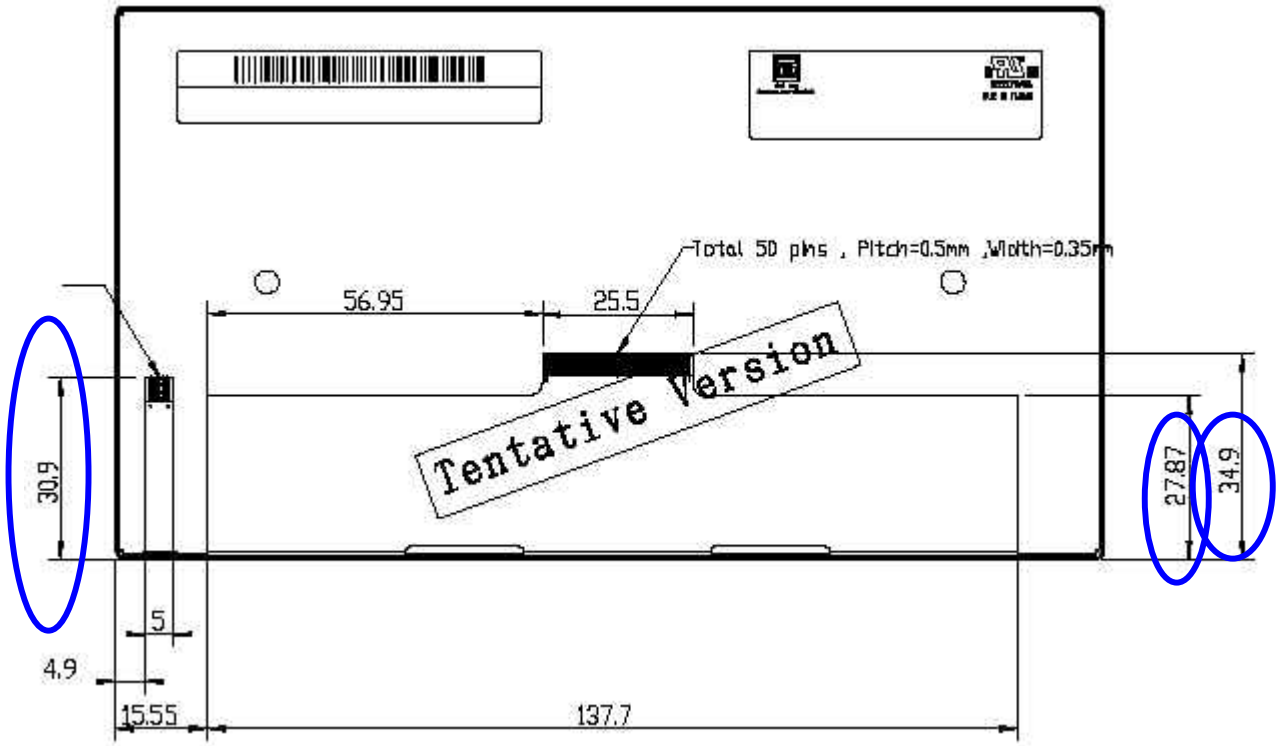
LED FPC pin define

Pin 1 : VCC
Pin 2 : CH1 feedback
Pin 3 : CH2 feedback
Pin 4 : CH3 feedback

I/F FPC pin define

Pin 1 : V-COM	Pin 27 : B0
Pin 2 : DIO1	Pin 28 : B1
Pin 3 : CLK	Pin 29 : B2
Pin 4 : SHL	Pin 30 : B3
Pin 5 : R0	Pin 31 : B4
Pin 6 : R1	Pin 32 : B5
Pin 7 : R2	Pin 33 : TP1
Pin 8 : R3	Pin 34 : REV
Pin 9 : R4	Pin 35 : POL
Pin 10 : R5	Pin 36 : VCC
Pin 11 : G0	Pin 37 : GND
Pin 12 : G1	Pin 38 : VAA
Pin 13 : G2	Pin 39 : DIO2
Pin 14 : G3	Pin 40 : GND
Pin 15 : G4	Pin 41 : XAO
Pin 16 : G5	Pin 42 : OE
Pin 17 : Gamut 1	Pin 43 : UD/RL
Pin 18 : Gamut 2	Pin 44 : CKV
Pin 19 : Gamut 3	Pin 45 : STVU
Pin 20 : Gamut 4	Pin 46 : STVD
Pin 21 : Gamut 5	Pin 47 : VCC
Pin 22 : Gamut 6	Pin 48 : VGL
Pin 23 : Gamut 7	Pin 49 : VGH
Pin 24 : Gamut 8	Pin 50 : GND
Pin 25 : Gamut 9	
Pin 26 : Gamut 10	

Old FPC dimension



New BLU module Dimension

