

Kaohsiung Opto-Electronics Inc.

FOR MESSRS:	DATE: Jun. 18 th ,2012

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

SP14Q006-TZA

Contents

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14	TOUCH PANEL SPECIFICATION	7B64PS 2714- SP14Q006-TZA-10	14-1/4~4/4

ACCEPTED BY:	PROPOSED BY: Leullen
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RECORD OF REVISION

		·								
DATE	SHEET No.	SUMMARY								
Oct.22,'03	7B64PS2709-	Changed LED I/F: JAK/1L-G-4S-S3C2								
	SP14Q006-TZA-2	↓								
	Page 9-2/2	JAK/IL-G-4S-S3C2								
Mar.31.'04	7B64PS2708-	8.3 POWER ON/OFF TIMING SEQUENCE								
	SP14Q006-TZA-3	Revised tDLD min. $200 \rightarrow 50$								
	Page 8-3/3	Revised tCH max. 200 → 30								
	1 agc 0-5/5									
Jun.04,'04	7B64PS 2705-	5.1 ELECTRICAL CHARACTERISTICS								
	SP14Q006-TZA-4	Added								
	Page 5-1/2	ITEM SYMBOL MIN. TYP. MAX								
		Power Supply Voltage Logic VDD-VSS 3.2 3.3 3.4								
		21.0 22.0 23.0								
		Recommend LC Driving Voltage VDD-V0 20.0 21.0 22.0								
		19.0 20.0 21.0								
	7B64PS 2706-	6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT								
	SP14Q006-TZA-4	Added The LCD driving voltage should be adjusted at the								
	Page 6-3/3	voltage where the peak contrast is obtained.								
	7B64PS 2710-	10.1 APPEARANCE INSPECTION CONDITION								
	SP14Q006-TZA-4	Revised 45°→25°								
	Page 10-1/3									
	7B64PS2711	14.1.2 OPERATING CONDITIONS								
	SP14Q006-TZA-4	Revised Operating Voltage : 5VDC→5.0 /3.3 VDC								
	Page 14-1/4									
May.04,'07	7B64PS2703	3. GENERAL SPECIFICATIONS								
	SP14Q006-TZA-5	Added								
	Page 3-1/1	(11) Backlight Type LED(Color : white)								
		(44) Dealtight Tone (150/0ales out to)								
		(11) Backlight Type LED(Color : white) Life time : 40Kh @ 25℃								
		Note: Life time for half of initial brightness								
	7004000705	5.2 ELECTRICAL CHARACTERISTICS OF LED BACKLIGHT								
	7B64PS2705	Revised								
	SP14Q006-TZA-5	rkevised								
	Page 5-2/2	# 50 T								
		\$\frac{1}{2} \text{ (g) 30 } \frac{1}{2} \text{ (g) 40}								
		0 20 40 60 80 100 Temperature(°C)								
		₩ 5 20 20 10 H								
		9 0 20 40 60 80 100 9 0 20 40 60 80 100								
		0 20 40 60 80 100								
		AMBIENT TEMPERATURE("(
L										

RECORD OF REVISION

DATE	CLIEFT No	CLIMMA DV								
DATE	SHEET No.	SUMMARY								
way.04,707	7B64PS2712	12. DESIGNATION OF LOT MARK								
	SP14Q006-TZA-5	Added								
	Page 12-1/1	REVISION A								
May 13 '08	7B64PS 2714-	14.1.2 OPERATING CONDITIONS								
		Changed:								
	SP14Q006-TZA-6									
	Page 14 - 1/4	ITEM SPECIFICATION Actuation Force (PS Silicone rubber)								
		Actuation Force 80g max. (R8,Silicone rubber)								
		\downarrow								
		ITEM SPECIFICATION								
		Actuation Force 1.2N max. (R8,Silicone rubber)								
14 00 100	-D04D0 0740									
Mar.06,'09	7B64PS 2712	12. DESIGNATION OF LOT MARK								
		Revised reversion from REV. A to REV.B								
	PAGE 12-1/1									
Nov.12,'10	7B64PS 2714-	14.6 APPEARANCE SPECIFICATION								
	SP14Q006-TZA-8	Changed : Blistering Puffiness 0.4mm max. → 0.6mm max.								
	PAGE 14-4/4									
May.01,'12	All pages	Company name changed:								
		KAOHSIUNG HITACHI ELECTRONICS CO.,LTD.								
		\downarrow								
		KAOHSIUNG OPTO-ELECTRONICS INC.								
	7B64PS 2714-	14.7 SAFETY AND ATTENTIONS								
	SP14Q006-TZA-9	Added: Item 1)								
	PAGE 14-4/4	,								
Jun. 18,'12	7B64PS-2714-	14.4.4 LINEARITY								
	SP14Q006-TZA-10	•								
	Page 14-2/4	(a) X axis linearity testing method , 100g.→150g.								
		(b) Y axis linearity method , 100g. →150g.								

2-2/2

3. GENERAL SPECIFICATIONS

(1) Part Name SP14Q006-TZA

(2) Outer Dimensions 167.0(W)mm×109.0(H)mm×11.4(D) mm max.

(3) Effective Area 120(W)mm min. × 89(H)mm min.

(4) Dot Size 0.345(W)min. × 0.345(H)min.

(5) Dot Pitch 0.360(W)mm × 0.360(H)mm

(6) Dot Number (Resolution) 320 (W) × 240 (H) dots

(7) Duty Ratio 1/240

(8) LCD Type Transflective type F-B / W STN

With glare type upper polarizer

(9) Viewing Direction 6 O'clock

(10) Viewing Angle Viewing Angle in Rear - Front

(12:00) (6:00)

 $R-F=90^{\circ}(typ.)$

(11) Backlight Type LED(Color: white)

Life time : 40Kh @ 25° C

Note: Life time for half of initial brightness

(12) Touch Panel Analog resistive

Transparency: 76% min.

Surface type: anti glare

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARKS
Power Supply for Logic	VDD-VSS	0	6.0	V	
Power Supply for LC Driving	VDD-VEE	0	27.5	V	
Input Signal Voltage	Vi	-0.3	VDD+0.3	V	Note 1
Otatia Flantwick.	VESD0	1	±100	V	Note 2,3,4
Static Electricity	VESD1	ı	±10	kV	Note 2,3,5

VSS=0V: STANDARD

Note 1: DOFF, FLM, CL1, CL2, D0~D3.

Note 2: Make certain you are grounded when handling LCM.

Note 3: Energy storage capacitance 200pF, discharge resistance 250 Ω Ta=25 $^{\circ}$ C, 60%RH.

Note 4: Contact discharge to I/F connector pins.

Note 5: Contact discharge to front metal bezel.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STO	RAGE	COMMENT
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-20 ℃	70 ℃	-30 ℃	80 ℃	Note 2,3,6,7
Humidity	Not	te 1	Note 1		Without Condensation
		2.45m/s ²		11.76m/s ²	
Vibration	-	(0.25G)	-	(1.2G)	Note 4
				Note5	1h max.
		29.4m/s ²		490.0m/s ²	
Shock	-	(3 G)	-	(50 G)	X · Y · Z Directions
				Note5	
Corrosive Gas	Not Acc	ceptable	Not Acceptable		

Note 1: $Ta \le 40^{\circ}C$: 85%RH max.

Ta>40 $^{\circ}$ C: Absolute humidity must be lower than the humidity of 85%RH at 40 $^{\circ}$ C

Note 2: Ta at -30° C ---< 48h, at 80° C ---< 168h.

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4: 5Hz~100Hz (Except resonance frequency)

Note 5: This module should be operated normally after finish the test.

Note 6: The response time will be slower under low temperature.

Note 7: Operation temp not include touch panel.

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5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
Power Supply Voltage	VDD-VSS		4.75	5.0	5.25	V	
for Logic	VDD-V33	-	3.2	3.3	3.4	V	
Power Supply Voltage for LC Driving	VEE-VSS	-	-23.1	-22.0	-20.9	>	
Input Cianal Valtage	Vi	H LEVEL	0.8VDD	ı	VDD	>	Note 1
Input Signal Voltage	VI	L LEVEL	0	ı	0.2VDD	>	Note i
Power Supply Current	IDD	VDD-VSS=5.0V		6.0		mA	Note 2
for Logic	טטו	VEE-VSS= -22.0V	-	0.0	_	IIIA	Note 2
Power Supply Current	IEE	VDD-VSS=5.0V	_	5.0	_	mA	Note 2
for LC Driving	ILL	VEE-VSS= -22.0V	-	5.0	_	ША	Note 2
Decemmended I C		Ta= 0° C , ϕ = 0°	21.0	22.0	23.0	V	
Recommended LC	VDD-V0	Ta=25 $^{\circ}$ C , ϕ = 0 $^{\circ}$	20.0	21.0	22.0	V	Note 3
Driving Voltage		Ta=50 $^{\circ}$ C , ϕ = 0 $^{\circ}$	19.0	20.0	21.0	٧	
Frame Frequency	fFLM	-	70	75	80	Hz	Note 4

Note 1: DOFF, FLM, CL1, CL2, D0~D3.

Note 2: FLM=75Hz , test pattern is all "Q". VDD-V0=21.0V , Ta=25 $^{\circ}\mathbb{C}$

Note 3: Recommended LC driving voltage may fluctuate about $\pm 1.0 V$ by each module. Test pattern is all "Q"

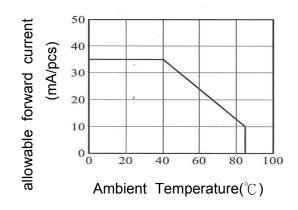
Note 4: Please set the frame frequency so as to avoid flicker and rippling on the display.

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5.2 ELECTRICAL CHARACTERISTICS OF LED BACKLIGHT

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
Power Supply Voltage for LED	VLED	-	-	5.0	5.2	V	
Power Supply Current for LED	ILED	VLED=5.0V	-	160	-	mA	Note1

Note 1: The ILED changes depending on ambient temperature.



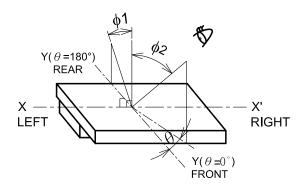
6. OPTICAL CHARACTERISTICS 6.1 OPTICAL CHARACTERISTICS

Ta=25°C (Backlight OFF)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
Viewing Area	ı	K≧2.0 θ=0° φ1+φ2	-	90	ı	deg.	Note1
Viewing / trea	ı	K≧2.0 θ=90° φ1+φ2	-	80	ı	deg.	Note1
Contrast Ratio	K	φ=0°, θ=0°	-	5	ı	ı	Note2,3
Response Time (Rise)	tr	φ=0°, θ=0°	-	336	-	ms	Note4
Response Time (Fall)	tf	φ=0°, θ=0°	-	148	ı	ms	Note4

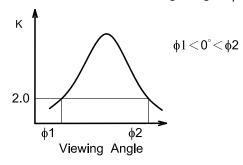
(Measure condition by KOE)

Note 1 : Definition of θ and ϕ (Normal) Viewing direction



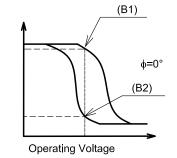
REAR 12:00 LEFT **RIGHT** 9:00 3:00 **FRONT** 6:00

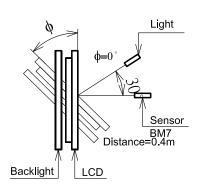
Note 2: Definition of viewing angle ϕ 1 and ϕ 2



Contrast ratio K vs viewing angle ♦

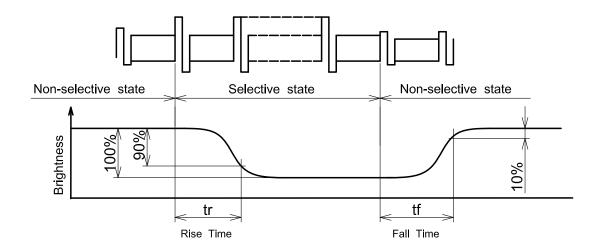
Note 3 : Definition of contrast"K" Brightness on non-selected dot (B1) Brightness on selected dot (B2)





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Note 4: Definition of optical response



6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

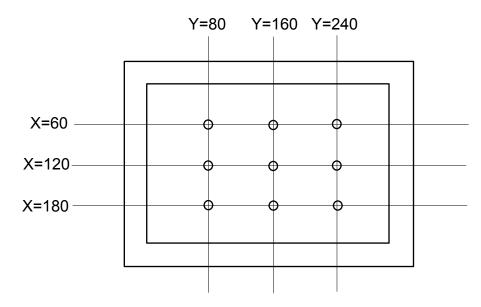
(Backlight ON)

ITEM	MIN.	TYP.	MAX.	UNIT	REMARKS
Brightness	-	40	-	cd/m ²	ILED=160mA
Brightness Uniformity	-	-	±30	%	Note 1

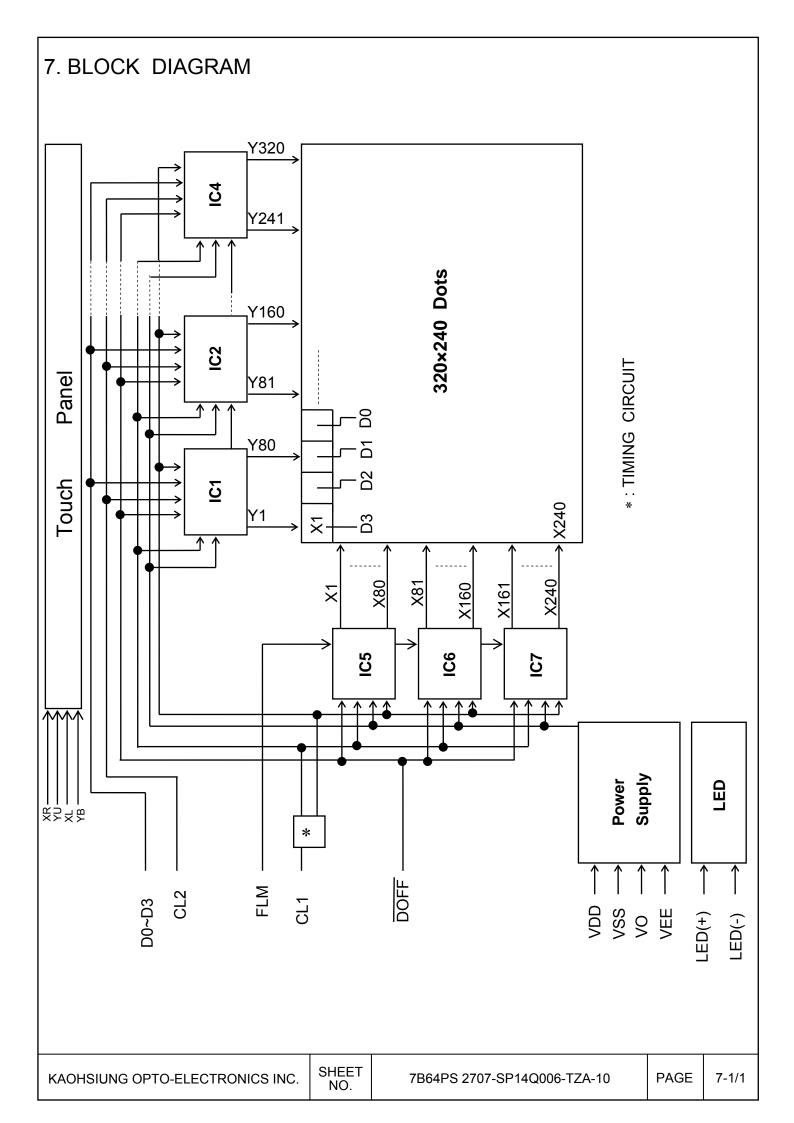
Ta=25°C, Display data should be all "ON".

The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.

Note 1: Measure of the following 9 places on the display.



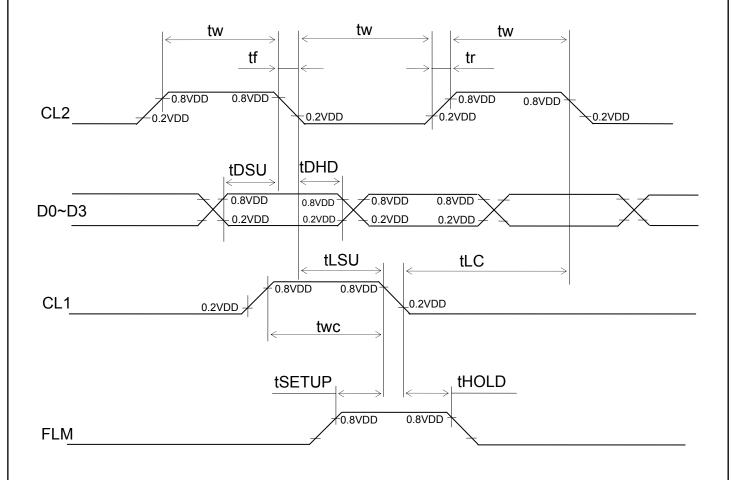
Definition of the brightness tolerance.

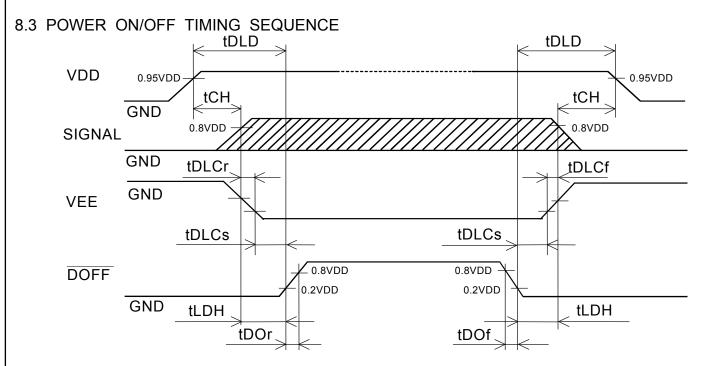


8. INTERFACE TIMING CHART 8.1 INTERFACE TIMING CHART $52.1\mu s \le T \le 59.5\mu s$ CL1 CL2 X1 X240> (Y1 XY5) D3 $\overline{(Y2)}$ D2 Y3 XY7 Y319 D1 (Y4 XY8 Y320 D0 FLM CL1 240×T FLM -% ₹ -X239 X240 X1 D0~D3 SHEET PAGE KAOHSIUNG OPTO-ELECTRONICS INC. 7B64PS 2708-SP14Q006-TZA-10 8-1/3 NO.

8.2 TIMING CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
CL2 frequency	fCP	-	-	6.5	MHz
CL2 pulse width	tw	45	-	-	ns
CL2 rise, fall time	tr,tf	-	-	15	ns
Data set up time	tDSU	30	ı	-	ns
Data hold time	tDHD	30	ı	-	ns
CL1 set up time	tLSU	80	ı	-	ns
CL1 clock time	tLC	120	ı	-	ns
"FLM" set up time	tSETUP	100	ı	-	ns
"FLM" hold time	tHOLD	100	-	-	ns
"CL1" pulse width	twc	125	-	-	ns





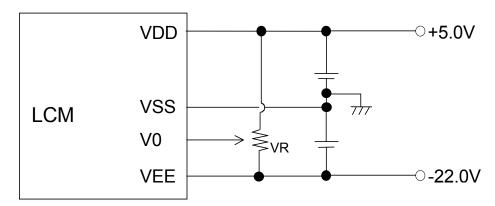
SYMBOL	MIN.	MAX.	UNIT	REMARKS
tDLD	50	-	ms	
tCH	0	30	ms	Note 1
tLDH	0	-	ms	
tDOr	-	100	ns	
tDOf	-	100	ns	
tDLCr	0	-	ms	Note 2
tDLCf	0	-	ms	
tDLCs	20	-	ms	

Note 1: Please keep the specified sequence because wrong sequence may cause permanent damage to the LCD panel.

Note 2: KOE recommends you to use $\overline{\text{DOFF}}$ function.

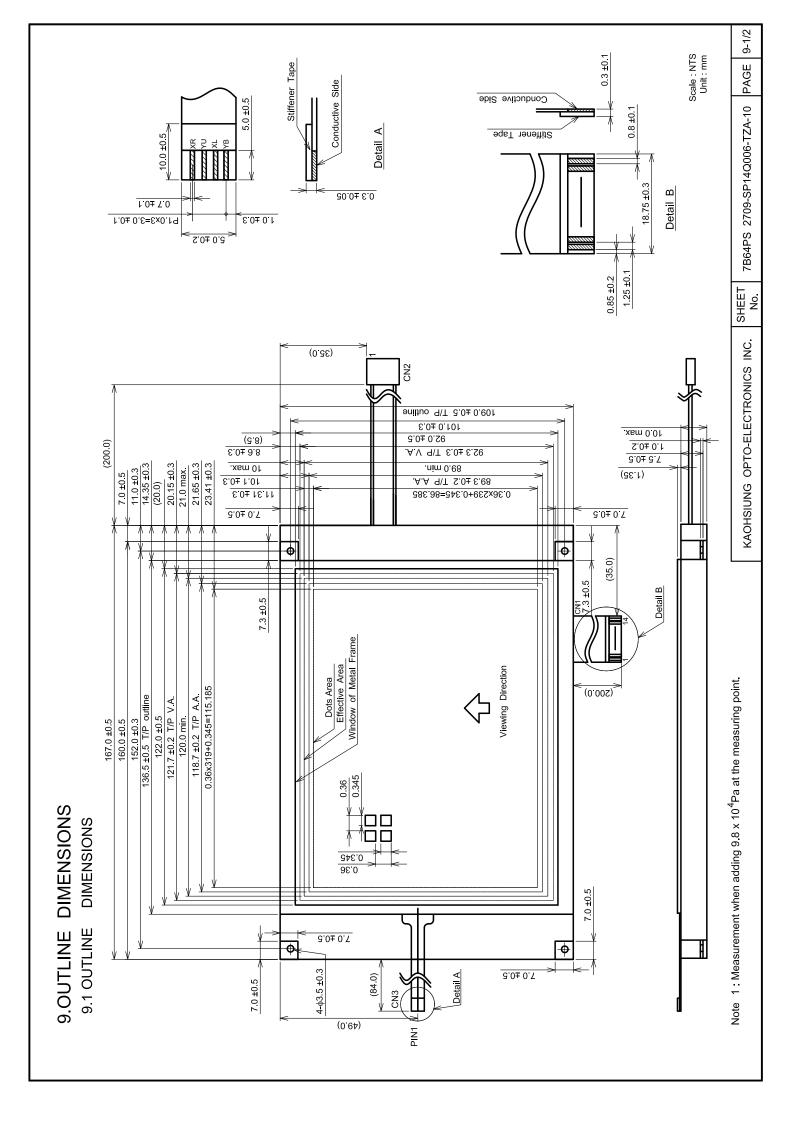
Display quality may deteriorate if you don't use $\overline{\text{DOFF}}$ function.

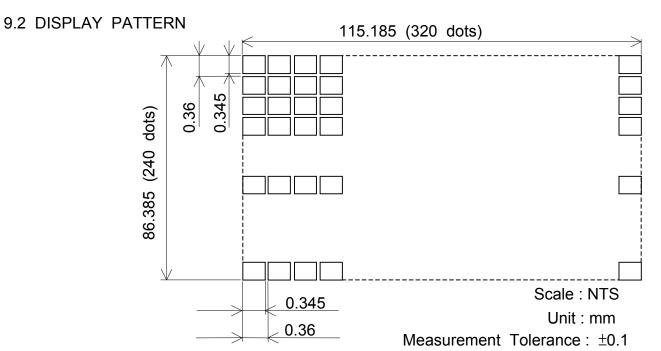
8.4 POWER SUPPLY FOR LCM (EXAMPLE)



Note 1: VR : $10k\Omega$

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9.3 INTERFACE PIN CONNECTION

FPC: pitch 1.25mm 14 pins

INTER	RFACE	PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN1	1	D0	H/L	
		2	D1		Display Data
		3	D2		
		4	D3		
		5	DOFF	H/L	H:ON / L:OFF
		6	FLM	Н	First Line Marker
		7	N.C	-	-
		8	CL1	H→L	Data Latch
		9	CL2	H→L	Data Shift
		10	VDD	-	Power Supply for Logic
		11	VSS	-	GND
		12	VEE	-	Power Supply for LC
		13	V0	-	Operating Voltage LC Driving
		14	VSS	-	GND

INTER	RFACE	PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN2	1	VLED(+)	-	Power Supply for LED
		2	N.C	-	-
		3	N.C	-	-
		4	VLED(-)	-	LED GND

LED I/F: J.A.E / IL-G-4S-S3C2-SA

INTER	RFACE	PIN No.	SIGNAL	FUNCTION
		1	XR	Analog Signal from Digitizer Right
T/P	CN3	2	YU	Analog Signal from Digitizer Up
1/P	CNS	3	XL	Analog Signal from Digitizer Left
		4	YB	Analog Signal from Digitizer Bottom

FPC: pitch 1.0mm 4pins

Recommend suitable connector: (HIROSE) FH12-10(4)SA-ISH

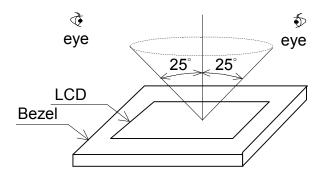
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10. APPEARANCE STANDARD

10.1 APPEARANCE INSPECTION CONDITION

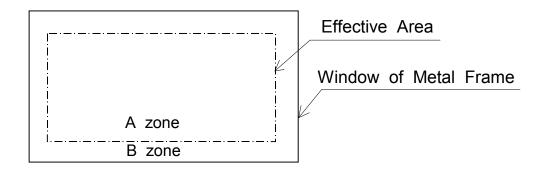
Visual inspection should be done under the following condition.

- (1) The inspection should be done under in the dark room.
- (2) The CFL should be lighted with the prescribed inverter.
- (3) The distance between eyes of an inspector and the LCD module is 25cm.
- (4) The viewing zone is shown the figure . Viewing angle $\leq 25^{\circ}$



10.2 DEFINITION OF EACH ZONE

A zone: Within the effective area specified at page 9-1/2 of this document. B zone: Area between the window of metal frame and the effective area line specified at page 9-1/2 of this document.



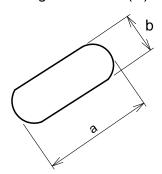
10.3 APPEARANCE SPECIFICATION

*) If a problem occurs in respect to any of these items, both parties(Customer and KOE) will discuss in more detail.

No.	ITEM	CRITERIA					В
	Scratches	Serious one is not a	llowed			*	-
	Dent	Serious one is not a	llowed			*	-
	Wrinkles in polarizer	Serious one is not a	llowed			*	-
	Bubbles	Average Diar	neter	Max	rimum Number		
		D(mm)		,	Acceptable		
		D≦0.			Ignore		_
		0.2 <d≦0< td=""><td>.3</td><td></td><td>12</td><td></td><td>_</td></d≦0<>	.3		12		_
		0.3 <d≦0< td=""><td>.5</td><td></td><td>3</td><td></td><td></td></d≦0<>	.5		3		
		0.5 <d< td=""><td></td><td></td><td>None</td><td></td><td></td></d<>			None		
	Stains,	Filamentous					
	Foreign Materials,	Length	Wid	th	Maximum Number		
	Dark Spot	L(mm)	W(m		Acceptable		_
١.		L≦2.0	W≦0.03		Ignore		_
L		L≦3.0	0.03 <w≦0.05< td=""><td>6</td><td></td><td></td></w≦0.05<>		6		
		L≦2.5	L≦2.5 0.05 <w≦0.1< td=""><td></td><td></td></w≦0.1<>				
			Rou				
С		Average Diameter	Maximum		Minimum		
		D(mm)	Accep	table	Space	1	
		D<0.2	Igno	re	-	\bigcirc	-
		$0.2 \leq D < 0.33$	8		10mm		
D		0.33≦D	Nor		-		
		Total	Filamentou				
		Those wiped out e	easily are a	· · · · · · · · · · · · · · · · · · ·		\bigcirc	\bigcirc
	Pinhole	Average Diar	neter		imum Number		
		D(mm)		,	Acceptable		
		D≦0.15			Ignore		
		$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$					
				<u> </u>			
	Contrast Irregularity	_	Maximum Number			\bigcirc	-
	(Spot)	D(mm)	Accep		Space		
		,		ore	-		
		0.25 <d≦0.35< td=""><td>10</td><td></td><td>20mm</td><td></td><td></td></d≦0.35<>	10		20mm		
		0.35 <d≦0.5< td=""><td>4</td><td></td><td>20mm</td><td></td><td></td></d≦0.5<>	4		20mm		
		0.5 < D	Nor	ne	-		

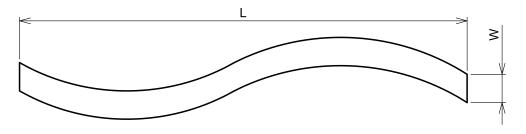
No.	ITEM					Α	В
L C D	Contrast Irregularity (Line) (Filamentous)	Width D(mm)	Length L(mm)	Maximum Number Acceptable Minimum Space			
		W≦0.25	L≦1.2	2	20mm		
		W≦0.2	L≦1.5	3	20mm		_
		W≦0.15	L≦2.0	3	20mm		
		W≦0.1	L≦3.0	4	20mm		
		To	otal	6	3		

Note 1: Definition of average diameter (D)

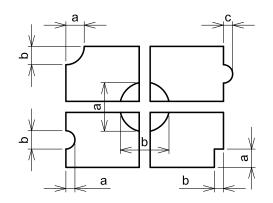


$$\frac{a+b}{2}$$
 = D......Average Diameter

Note 2: Definition of length (L) and width (W)



Note 3: Definition of pinhole



c : Salience

11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VEE) AND VIEWING ANGLE RANGE
Setting VEE out of the recommended condition will be a cause for a change of viewing angle range.

11.2 PRECAUTIONS AGAINST STATIC CHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band etc. And don't touch I/F pins directly.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage (VDD). If above sequence is not kept, C-MOS LSIs of LCD modules may be damaged due to latch up problem.

11.4 PACKAGING

- (1) No leaving product is preferable in the place of high humidity for a long period of time. For their storage in the place where temperature is 35 °C or higher ,special care to prevent them from high humidity is required. A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer peel-off. Please keep the temperature and humidity within the specified range for use and storage.
- (2) Since polarizers tend to be easily damaged, They should be handled full with care so as not to get them touched, pushed or rubbed.
- (3) As the adhesives used for adhering polarizers are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following solvents are recommended for use: normal hexane

Please contact us when it is necessary for you to use chemicals.

- (4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois, soaked in the chemicals recommended without scrubbing it hardly. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.
- (6) Foggy dew deposited on the surface due to coldness will be caused for polarizer damage, stain and dirt on product. When necessary to take out the products from some place at low temperature for test, etc. It is required for them to be warmed up in a container once at the temperature higher than that of room.
- (7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands.

 (Some cosmetics are detrimental to polarizers.)

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(8) In general the quality of glass is fragile so that it tends to be cracked or chipped in handling, specially on its periphery. Be careful not to give it sharp shock caused by dropping down, etc.

11.5 CAUTION FOR OPAERATION

- (1) It is an indispensable condition to drive LCDs within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. An electrochemical reaction due to direct current causes LCDs undesirable deterioration, so that the use of direct current driver should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCDs show dark blue color in them. However those phenomena do not mean malfunction or out of order with LCDs which will come back in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- (4) A slight dew depositing on terminals is a cause for electrochemical reaction resulting in terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.

11.6 STORAGE

In case of storing for a long period of time (for instance, for years) for the purpose of replacement use, the following ways area recommended.

- (1) Storage in a polyethylene bag with the opening sealed, so the fresh air will not be entered from outside.
- (2) Placing in a dark place where neither exposure to direct sunlight nor light is , keeping temperature in the range from 0 $^{\circ}$ C to 35 $^{\circ}$ C.
- (3) Storing with no touch on polarizer surface by anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery from us.)

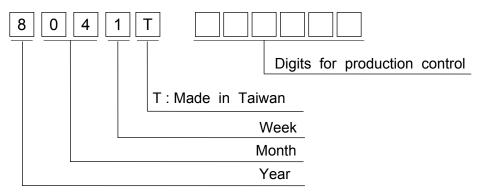
11.7 SAFETY

- (1) It is recommendable to crash damaged or unnecessary LCDs into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- (2) When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 6 digits for production control.



Year	Figure in
	lot mark
2012	2
2013	3
2014	4
2015	5
2016	6

Month	Figure in lot mark	Month	Figure in lot mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Figure in
(day in calendar)	lot mark
1~ 7	1
8~14	2
15~21	3
22~28	4
29~31	5

12.2 SERIAL No.

Serial No. is consisted of 6 digits number (000001~999999).

12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.

12.4 REVISION(Rev.) CONTROL

Rev No.	ITEM
_	Mcount IC :MN73099HED(Panasonic)
Α	Transistor :2SA1036K(ROHM)
В	Mcount IC :IT7001M(ITE)
В	Transistor :2SA1576(ROHM)



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	1	2	23	4	ļ!	5	6	
_	_	_		_		_	_	

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13. PRECAUTION FOR USE

- 13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- 13.2 On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in this specifications.
 - (3) When an inspection specifications change or operating condition change in customer is reported to KOE, and some problem is arisen in this specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any request, please contact KOE.

14. TOUCH PANEL SPECIFICATION

14.1 RATINGS

14.1.1 ABSOLUTE MAXIMUM RATINGS

ITEM	SPECIFICATION	COMMENT
Operating Voltage	7V	Without
Contact Current	20mA	Condensation

14.1.2 OPERATING CONDITIONS

ITEM	SPECIFICATION
Operating Voltage	5.0 / 3.3 VDC
Contact Current	10 ~ 20 mA
Actuation Force	1.2N max. (R8,Silicone rubber)

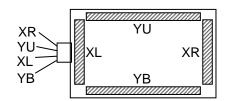
14.2 SURFACE HARDNESS 2H

14.3 OPTICAL CHARACTERISTICS 14.3.1 TRANSPARENCY: 76%.min. (WAVE LENGTH: 450 ~ 700nm)

14.4 ELECTRICAL CHARACTISTICS

14.4.1 CONDUCTIVE RESISTANCE

TERMINAL	CONDUCTIVE RESISTANCE
XR-XL	150~1300Ω
YU-YB	150~1300Ω



14.4.2 INSULATION RESISTINCE

TERMINAL	INSULATION RESISTANCE	TESTING VOLTAGE
X-Y	20ΜΩ	25VDC

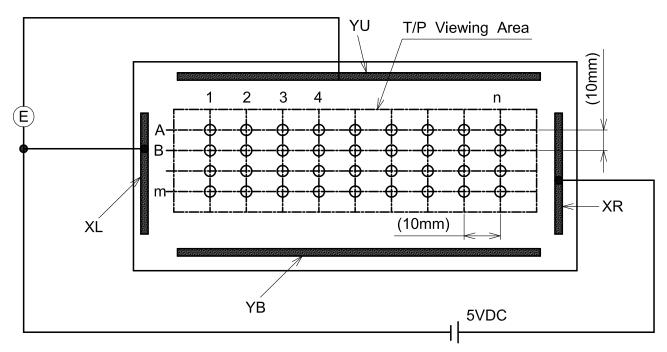
14.4.3 BOUNCE CHATTERING 10ms max.

14.4.4 LINEARITY

(1) LINEARITY

Linearity Deviation: 2% max.

- (2) TESTING CIRCUIT
 - (a) X axis linearity testing method ,150g , VXR-VXL=5V , VOUT=VYU.

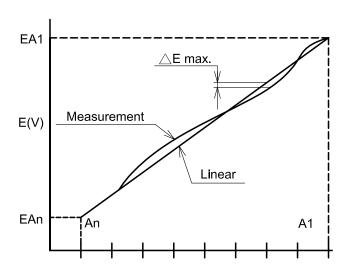


(b) Y axis linearity method, 150g VYU-VYB=5V, VOUT=VXR

(3) CALCULATION

(a) X axis linearity

LINEARITY=
$$\frac{\triangle \text{ E max.}}{\text{EA1} - \text{EAn}}$$
 x100(%)



Input Position

14.5 ENVIRONMENTAL TESTING

ITEM	CONDITIONS	CRITERIA
High Temperature	60°ℂ : 120h & 25°ℂ: 24h	
Storage		
Low Temperature	-20℃ : 120h & 25℃ : 24h	
Storage		After testing mount to
Temperature	-20°C ←→ 70°C : 10 Cycles within	After testing must to
Cycle	(30) (60) (30) : minutes & 25°C	meet the specifications
	: 24h (Without Condensation)	of the Electrical,
Humidity Storage	60°C , 90%RH. 120h	Mechanical & Optical Characteristics.
Durability for	150g , R8, HS40 Silicon Rubber	Characteristics.
Keystroke	(Speed: 330mm/sec)	
	: 1000000 Activations	

14.6 APPEARANCE SPECIFICATION

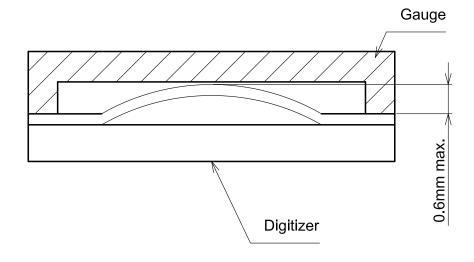
No.	ITEM	CRITERIA					В
		FILAMENTOUS					
		Length		dth	Maximum Number		
	Hair Flaws	L(mm)	W(r	nm)	Acceptable	0	
	Hall Flaws	L≦12	1	N≦0.05	Ignore		-
		L≦5	0.05 < V	V≦0.1	3		
		L>2	0.1 < V	V	None		
		Average Diam		M	Maximum Number		
Т	Dot-shaped Impurities	D(mm)		Acceptable			
/		D≦0.	.1	Ignore		0	-
Р		$0.1 < D \le 0.3$			5		
		0.3 <d< td=""><td colspan="2">None</td><td></td><td></td></d<>		None			
			FILAI	MENTOUS	8		
		Length	Wi	dth	Maximum Number		
	Scratch	Scratch $L(mm)$ $L \le 12$		nm)	Acceptable		
	Scratch			<i>N</i> ≦0.05	Ignore	Ο	-
		L≦12	0.05 < V	V≦0.1	5		
		L>12	0.1<\	V	None		

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14.6.3 GLASS INDENTATION

ITEM	SPECIFICATIONS	
Common Indentation	X	$\begin{array}{ c c c }\hline X & Y & Z \\ & \leq 5.0 & \leq 3.0 & \leq t \\ \hline \\ But \ , indentation \ can \ not \\ including \ seal \ area. \\ t : Glass \ thuickness. \\ \hline \end{array}$
Corner Broken	X	$\begin{array}{ c c c c }\hline X & Y & Z \\ & \leq 2.0 & \leq 5.0 & \leq t \\ \\ \hline \text{But , indentation can not including seal area.} \\ \\ \hline \end{array}$
Indentation Witnin Pattern		Y≦1 Is ignore But , Must to meet the specification of conducting pattern indentation.
Proceeding Crack		None

14.6.4 BLISTERING (PUFFINESS): 0.6 mm max.



14.7 SAFETY AND ATTENTIONS

1) UV protection is recommended to avoid the possibility of performance degrading when touch panel is likely applied under UV environment for a long period of time.

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