

Kaohsiung Opto-Electronics Inc.

FOR MESSRS : \_\_\_\_\_

DATE : May 21th, 2014

### CUSTOMER'S ACCEPTANCE SPECIFICATIONS

# TX20D33VM2BAA

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ACCEPTED BY: \_\_\_\_\_

PROPOSED BY: S.Z Lin.

#### 2. RECORD OF REVISION DATE SHEET No. SUMMARY 4. ABSOLUTE MAXIMUM RATINGS Aug.02,'13 7B64PS 2704-TX20D33VM2BAA -2 Changed : PAGE 4-1/1 Symbol Item Max. Unit Remarks **Backlight Input Voltage** $V_{LED}$ 14 V \_ $\downarrow$ Symbol Max. Unit Remarks Item **Backlight Input Voltage** V $\mathsf{V}_{\mathsf{LED}}$ 15 7B64PS 2705-5.1 LCD CHARACTERISTICS TX20D33VM2BAA -2 Changed : PAGE 5-1/2 Item Symbol Condition Min. Power Supply Voltage $V_{DD}$ \_ 2.3 $\downarrow$ Item Symbol Condition Min. Power Supply Voltage 3.0 $V_{DD}$ \_ 7B64PS 2710-10.2 REAR VIEW TX20D33VM2BAA -2 The dimension of (A) is changed from 69 to 73 mm. PAGE 10-2/2 $\oplus$ Pin1 (61.155) CN1 19.6) 19.6) Ð **11.2 LCD APPEARANCE SPECIFICATION** Jan. 10,'14 7B64PS 2711 -Revised : TX20D33VM2BAA-3 3/5 3/5 PAGE 11 - 2/3 Area (1) Area (2) 1/5 1/5 Area (2) Area (1) 3/5 3/5 1/5 1/5 **11.2 LCD APPEARANCE SPECIFICATION** 7B64PS 2711 -Removed : LED BACKLIGHT APPEARANCE TX20D33VM2BAA-3 PAGE 11 - 3/3 SHEET KAOHSIUNG OPTO-ELECTRONICS INC. 7B64PS 2702-TX20D33VM2BAA-4 PAGE 2 - 1/2NO.

DATE	SHEET No.					
May 21,'14	7B64PS 2711-	11.2 LCD APPE				
May 21, 14	TX20D33VM2BAA -4	Changed :				
	PAGE 11-2/3		em	Criteria		
				Average diameter (r	nm)	
		Bubbles on	polarizer	D≦0.3		
		1) Stains		Average diameter (r	nm)	
		2) Foreign I 3) Dark Spo		D≦0.2		
				$\downarrow$		
		lte	em	Criteria		
				Average diameter (r	nm)	
		Bubbles on	polarizer	D<0.3	-	
		1) Stains		Average diameter (r	nm)	
		2) Foreign M 3) Dark Spo		D<0.2		
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### 3. GENERAL DATA

#### 3.1 DISPLAY FEATURES

This module is a 8.0" WVGA of 16:9 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially. This display is RoHS compliant, and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX20D33VM2BAA
Module Dimensions	189.0(W) mm x 120.0(H) mm x 7.5 (D) mm
LCD Active Area	174.0(W) mm x104.4(H) mm
Pixel Pitch	0.2175 (W) mm x 0.2175 (H) mm
Resolution	800 x 3(RGB)(W) x 480(H) Dots
Color Pixel Arrangement	R, G, B Vertical stripe
LCD Type	Transmissive Color TFT; Normally White
Display Type	Active Matrix
Number of Colors	262k Colors (6-bit RGB)
Backlight	Light Emitting Diode (LED)
Weight	160 g
Interface	CMOS 40 pins
Power Supply Voltage	3.3V for LCD; 12.0V for backlight.
Power Consumption	0.36W for LCD; 1.3W for backlight.
Viewing Direction	12 O'clock (without image inversion and least brightness change) 6 O'clock (contrast peak located at)

## 4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V <sub>DD</sub>	-0.5	5.0	V	-
Input Voltage of Logic	VI	-0.5	5.0	V	Note 1
Operating Temperature	Тор	-30	80	°C	Note 2
Storage Temperature	Tst	-30	80	°C	Note 2
Backlight Input Voltage	$V_{LED}$	-	15	V	-

Note 1: The rating is defined for the signal voltage of the interface such as DE, CLK and RGB data bus.

Note 2: The maximum rating is defined as above based on the panel temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than  $25\,^\circ\mathrm{C}\,.$ 

- Operating under high temperature will shorten LED lifetime.

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## 5. ELECTRICAL CHARACTERISTICS

#### 5.1 LCD CHARACTERISTICS

5.1 LCD CHARACTERISTICS							$T_a = 25 \ ^\circ C, \ \text{Vss} = 0 \text{V}$	
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks	
Power Supply Voltage	V <sub>DD</sub>	-	3.0	3.3	3.6	V	-	
Input Voltage of Logic	Vı	"H" level	$0.7 \ V_{DD}$	-	V <sub>DD</sub>			
		"L" level	0	-	$0.3 V_{\text{DD}}$	mV	Note 1	
Power Supply Current	I <sub>DD</sub>	$V_{DD}$ - $V_{SS}$ =3.3V	-	110	130	mA	Note 2,3	
Frame Frequency	$f_{\it Frame}$	-	-	60	66	Hz		
CLK Frequency	$f_{CLK}$	-	27.2	33.3	40	MHz	-	

Note 1: The rating is defined for the signal voltages of the interface such as DE, CLK and RGB data bus.

Note 2: An all black check pattern is used when measuring  $I_{DD}$ ,  $f_{Frame}$  is set to 60 Hz.

Note 3: 0.315A fuse is applied in the module for I<sub>DD</sub>. For display activation and protection purpose, power supply is recommended larger than 1A to start the display and break fuse once any short circuit occurred.

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#### 5.2 BACKLIGHT CHARACTERISTICS

5.2 BACKLIGHT CHARACTERISTICS							
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
LED Input Voltage	$V_{\text{LED}}$	Backlight Unit	11.5	12.0	12.5	V	Note 1
LED Forward Current	I <sub>LED</sub>	Backlight Unit	-	108	-	mA	-
LED Lifetime	-	I <sub>LED</sub> =108 mA	-	50K	-	hrs	Note 2

Note 1: Fig. 5.1 shows the LED backlight circuit. The circuit has 27 LEDs in total and R is  $261 \Omega$ .

Note 2: The estimated lifetime is specified as the time to reduce 50% brightness by applying 108 mA at 25°C.

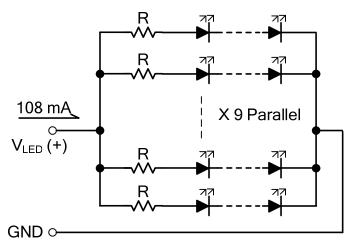


Fig 5.1

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### 6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The ambient temperature is 25  $^{\circ}\mathrm{C}\,.$

- In the dark room less than 100 lx, the equipment has been set for the measurements as shown in Fig 6.1.

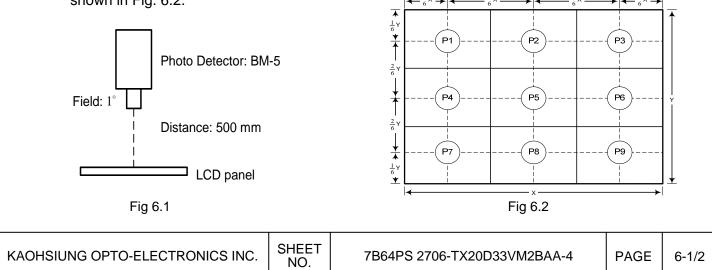
						$T_a = 25 ^{\circ} C$	$C, f_{Frame} = 60 H$	z, Vdd = 3.3V
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Brightness of	White	-	100	300	400	-	cd/m <sup>2</sup>	Note 1
Brightness Ur	niformity	-	$I_{\text{LED}}$ = 108mA $\phi = 0^{\circ}, \theta = 0^{\circ}$	70	-	-	%	Note 2
Contrast F	Ratio	CR	$\phi = 0$ , $\theta = 0$	300	600	-	-	Note 3
Response	Time	Tr + Tf	$\phi = 0^\circ, \theta = 0^\circ$	-	20	-	ms	Note 4
NTSC R	atio	-	$\phi = 0^\circ, \theta = 0^\circ$	-	60	-	%	-
		$\theta$ x	$\phi = 0^{\circ}, CR \ge 10$	60	80	-		
		θ x'	φ = 180°, CR ≥ 10	60	80	-	Desmo	Note 5
Viewing A	ingle	heta y	$\phi = 90^{\circ}, \mathrm{CR} \ge 10$	50	60	-	Degree	
		$\theta$ y'	φ = 270°, CR ≥ 10	70	80	-		
	Duit	Х		0.59	0.64	0.69		
	Red	Y		0.29	0.34	0.39		
	Graan	Х		0.31	0.36	0.41		
Color	Green	Y		0.53	0.58	0.63		
Chromaticity	Blue	Х	$\phi = 0^\circ, \theta = 0^\circ$	0.09	0.14	0.19		Note 6
	Blue	Y	]	0.03	0.08	0.13		
	White	Х	]	0.24	0.29	0.34		
	vvnite	Y		0.26	0.31	0.36		

Note 1: The brightness is measured from 9 point average value of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

Brightness uniformity =  $\frac{\text{Min. Brightness}}{\text{Max. Brightness}}$  X100%

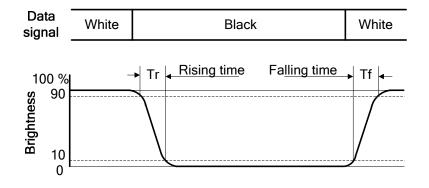
which is based on the brightness values of the 9 points in active area measured by BM-5 as shown in Fig. 6.2.



Note 3: The Contrast ratio is measured from the center point of the panel, P5, and defined as the following equation:

CR = Brightness of White Brightness of Black

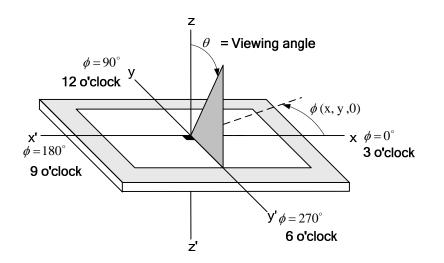
Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 90% brightness to 10% brightness when the data is from white to black. Oppositely, falling time is the period from 10% brightness rising to 90% brightness.





Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle  $\phi$  is used to represent viewing directions, for instance,  $\phi = 270^{\circ}$  means 6 o'clock, and  $\phi = 0^{\circ}$  means 3 o'clock. Moreover, angle  $\theta$  is used to represent viewing angles from axis Z toward plane XY.

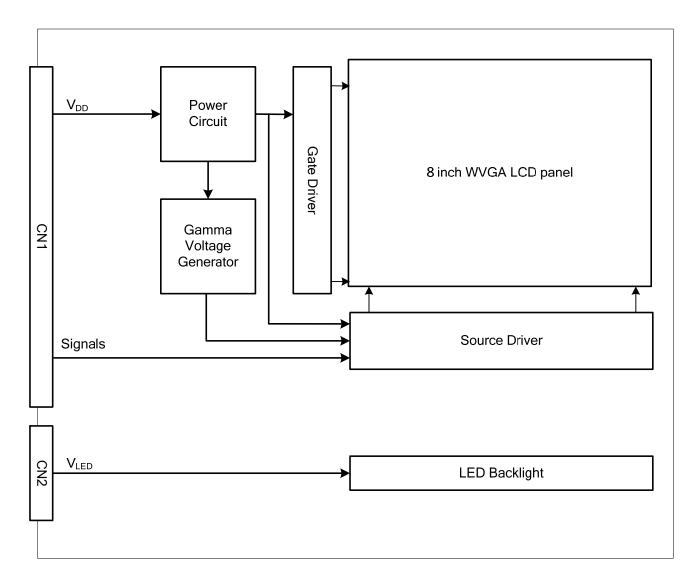
The viewing direction of this display is 12 o'clock, which means that a photograph with gray scale would not be reversed in color and the brightness change would be less from this direction. However, the best contrast peak would be located at 6 o'clock.





Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

## 7. BLOCK DIAGRAM



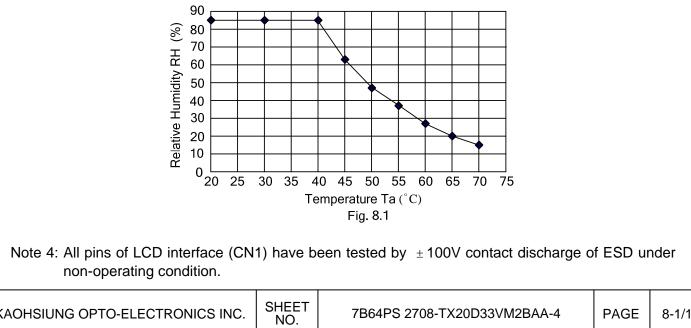
Note : Signals are CLK, DE and RGB data bus.

## 8. RELIABILITY TESTS

Test Item	Condition				
High Temperature	1) Operating 2) 80 °C	240 hrs			
Low Temperature	1) Operating 2) -30 °C	240 hrs			
High Temperature	1) Storage 2) 80 °C	240 hrs			
Low Temperature	1) Storage 2) -30 °C	240 hrs			
Heat Cycle	1) Operating 2) –20 °C ~70 °C 3) 3hrs~1hr~3hrs	240 hrs			
Thermal Shock	<ol> <li>Non-Operating</li> <li>-35 °C ↔ 85 °C</li> <li>0.5 hr ↔ 0.5 hr</li> </ol>	240 hrs (Note 3)			
High Temperature & Humidity	<ol> <li>1) Operating</li> <li>2) 40 °C &amp; 85%RH</li> <li>3) Without condensation</li> </ol>	240 hrs			
Vibration	<ol> <li>Non-Operating</li> <li>2) 20~200 Hz</li> <li>3) 2G</li> <li>4) X, Y, and Z directions</li> </ol>	1 hr for each direction			
Mechanical Shock	1) Non-Operating 2) 10 ms 3) 50G 4) $\pm X, \pm Y$ and $\pm Z$ directions	Once for each direction			
ESD	1) Operating 2) Tip: 200 pF, 250 $\Omega$ 3) Air discharge for glass: ± 8KV 4) Contact discharge for metal frame: ± 8KV	1) Glass: 9 points 2) Metal frame: 8 points (Note 4)			

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

- Note 2: The display is not guaranteed for use in corrosive gas environments.
- Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 40°C, the humidity needs to be reduced as Fig. 8.1 shown.



## 9. LCD INTERFACE

### 9.1 INTERFACE PIN CONNECTIONS

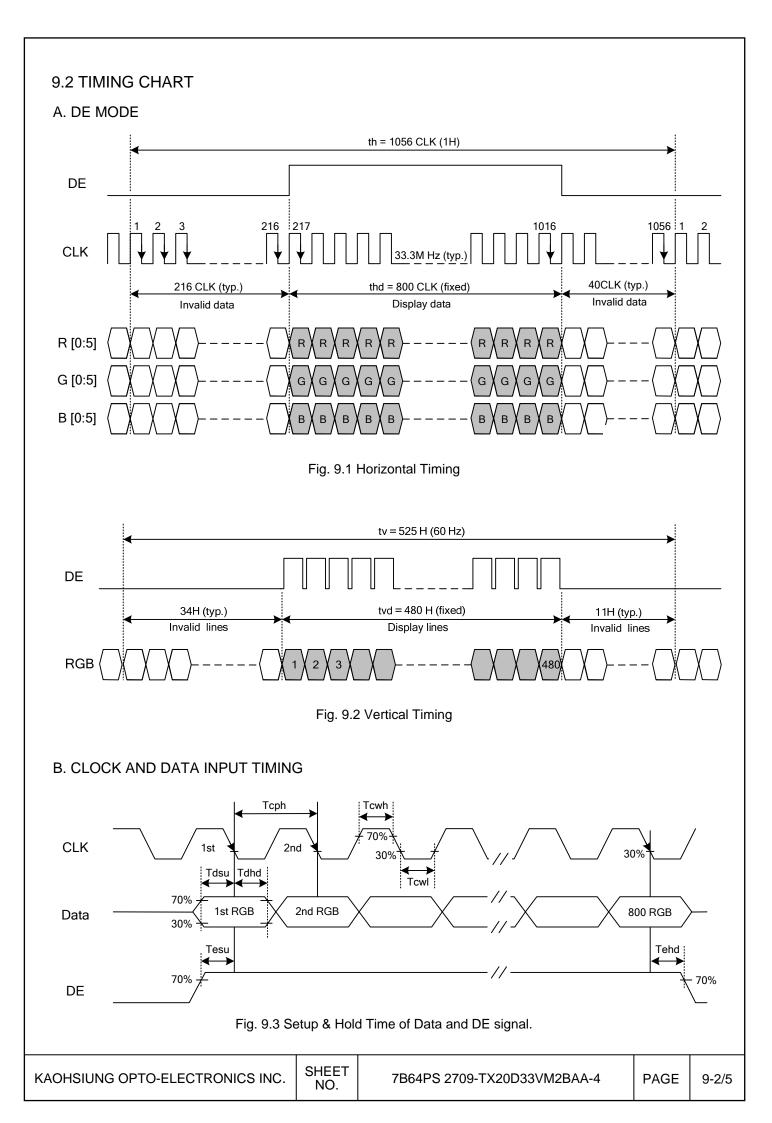
The display interface connector is FA5B040HP1R3000 made by JAE (Thickness:  $0.3 \pm 0.05$ mm; Pitch:  $0.5 \pm 0.05$ mm) and more details of the connector are shown in the section of outline dimension.

Pin assignment of LCD interface is as below:

Pin No.	Signal	Function	Pin No.	Signal	Function
1	$V_{DD}$		21	G4	Green Data
2	$V_{DD}$	Dower Cymphy far Logio	22	G3	Green Data
3	$V_{DD}$	Power Supply for Logic	23	$V_{SS}$	GND
4	$V_{DD}$		24	G2	
5	NC	No Connection	25	G1	Green Data
6	DE	Data Enable Signal	26	G0	
7	$V_{SS}$	GND	27	V <sub>SS</sub>	GND
8	NC	No Connection	28	R5	
9	$V_{SS}$	GND	29	R4	Red Data
10	NC	No Connection	30	R3	
11	V <sub>SS</sub>	GND	31	V <sub>SS</sub>	GND
12	B5		32	R2	
13	B4	Blue Data	33	R1	Red Data
14	B3		34	R0	
15	V <sub>SS</sub>	GND	35	NC	No Connection
16	B2		36	N/	CND
17	B1	Blue Data	37	$V_{SS}$	GND
18	B0	]	38	CLK	Dot Clock
19	V <sub>SS</sub>	GND	39	V	CND
20	G5	Green Data	40	$V_{SS}$	GND

The backlight interface connector is BHR-03VS-1 made by JST, and pin assignment of backlight is as below:

Pin No.	Signal	Level	Function
1	$V_{LED}$ +	-	Power Supply for LED
2	NC	-	No connection
3	V <sub>LED</sub> -	-	GND



### 9.3 TIMING TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (Vsync) = 60 Hz to define.

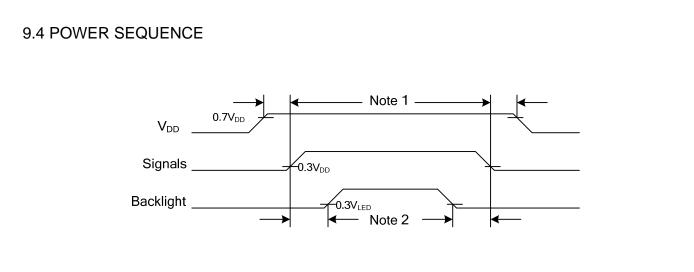
#### A. DE MODE

Item		Symbol	Min.	Тур.	Max.	Unit
	CLK Frequency	fclk	27.2	33.3	40	M Hz
Horizontal	Display Data	thd	800	800	800	
	Cycle Time	th	944	1056	1088	CLK
Martinal	Display Data	tvd	480	480	480	
Vertical	Cycle Time	tv	515	525	610	Н

#### B. CLOCK AND DATA INPUT TIMING

	ltem	Symbol	Min.	Тур.	Max.	Unit
CLK	Duty	Tcwh	40	50	60	%
CLK	Cycle Time	Tcph	-	30	-	
	Setup Time	Tvsu	5	-	-	
Vsync	Hold Time	Tvhd	5	-	-	
Hayna	Setup Time	Thsu	5	-	-	
Hsync	Hold Time	Thhd	5	-	-	ns
Data	Setup Time	Tdsu	5	-	-	
Data	Hold Time	Tdhd	5	-	-	
DE	Setup Time	Tesu	5	-	-	
DE	Hold Time	Tehd	5	-	-	

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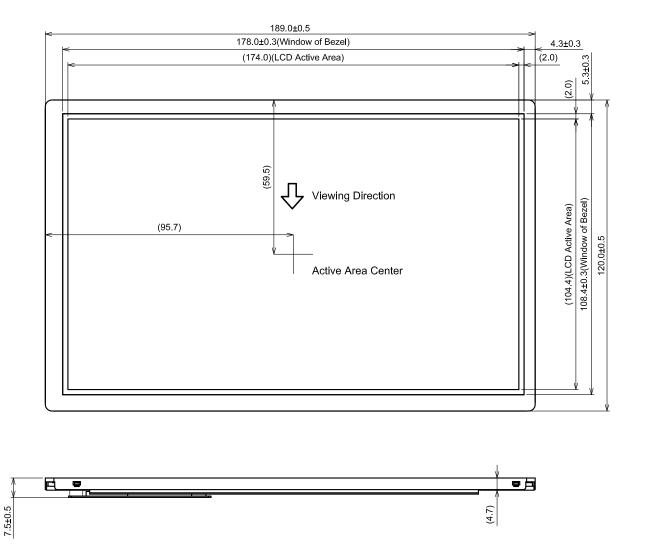
- Note 1: In order to avoid any damages, V<sub>DD</sub> has to be applied before all other signals. The opposite is true for power off where V<sub>DD</sub> has to be remained on until all other signals have been switch off. The recommended time period is 1 second. Hot plugging might cause display damage due to incorrect power sequence, please pay attention on interface connecting before power on.
- Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

### 9.5 DATA INPUT for DISPLAY COLOR

			F	Red	Dat	а			G	reen	Da	ta			E	Blue	Dat	а	
Input	color	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSE	3				LSB	MSE	3				LSB	MSE	3				LSB
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic Color	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Dasie 00101	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

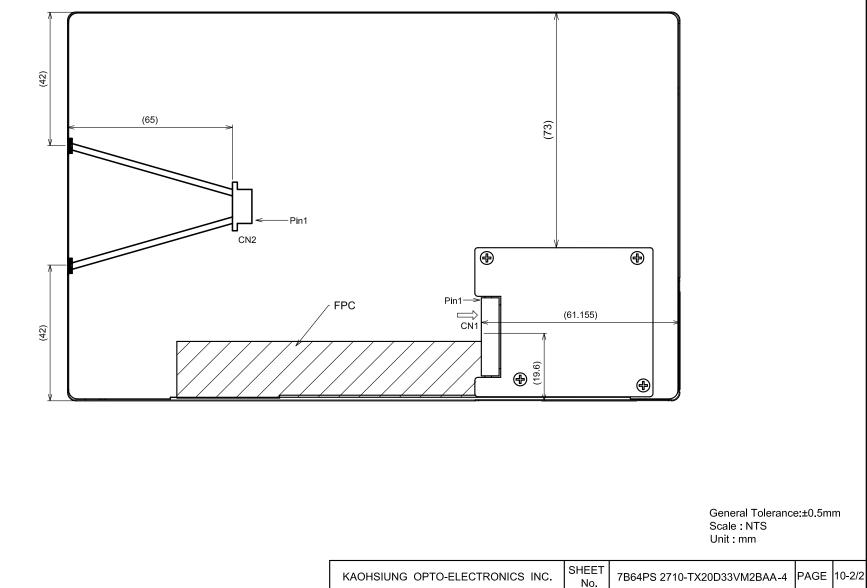
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### 10. OUTLINE DIMENSIONS 10.1 FRONT VIEW



General Tolerance:±0.5mm Scale : NTS Unit : mm

#### 10.2 REAR VIEW



General Tolerance:±0.5mm Scale : NTS Unit : mm

## **11. APPEARANCE STANDARD**

The appearance inspection is performed in a room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle  $\theta$  shown in Fig. 11.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

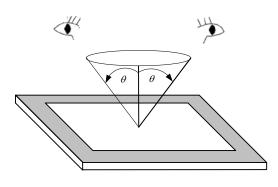


Fig. 11.1

#### 11.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 2 areas as shown in Fig.11.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area between A zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

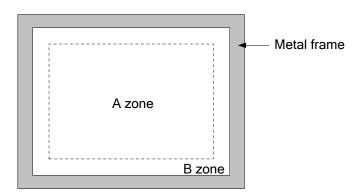


Fig. 11.2

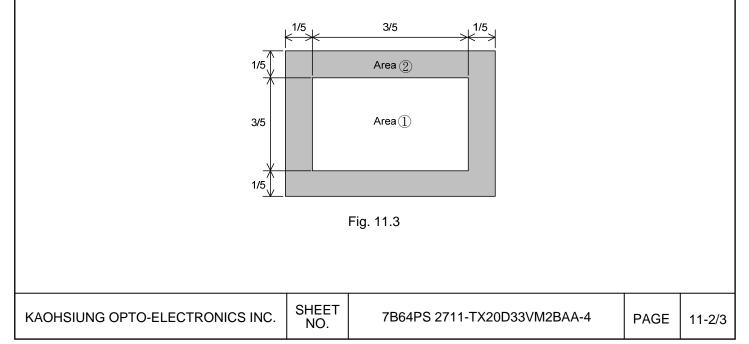
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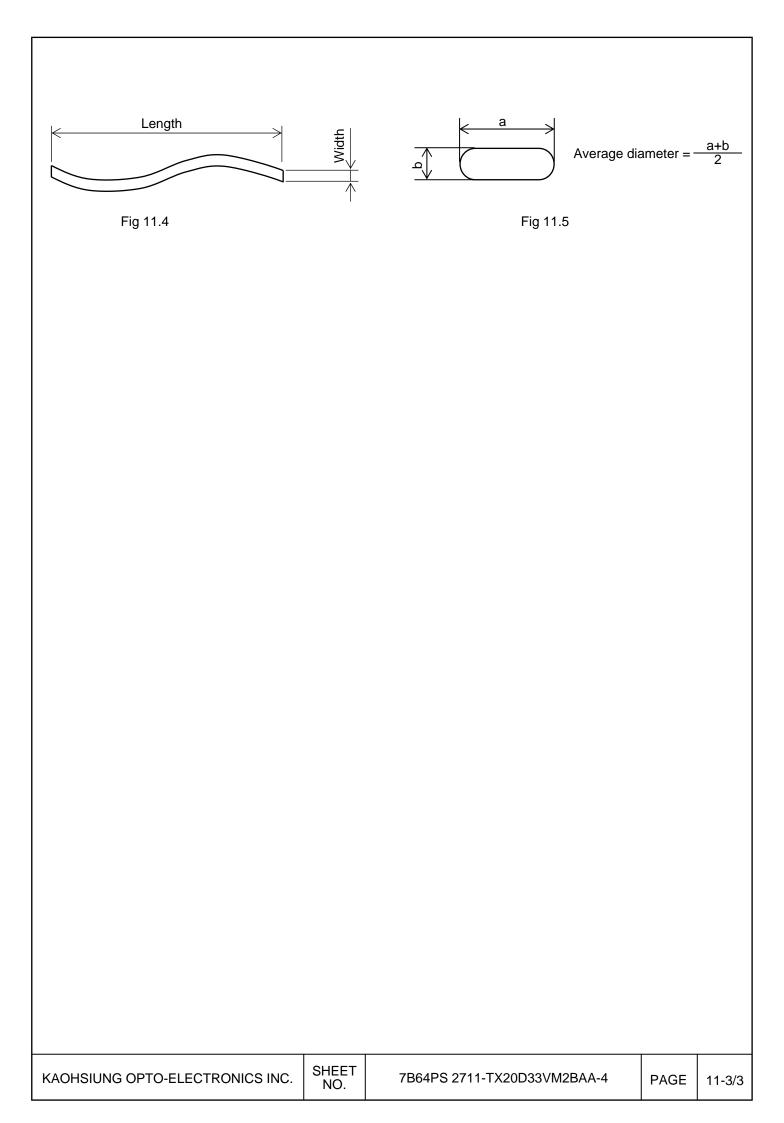
#### 11.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 11.4 and Fig. 11.5.

Item		Cr			Applied zone		
	Length (mm)	Width (mm)	Maximum number		Minimum space		
	L≦15	W≦0.02	Ignored	ł	-	•	
Scratches	L≦15 0	.02 <w≦0.1< td=""><td>5</td><td></td><td>-</td><td>A</td></w≦0.1<>	5		-	A	
	L>15	0.1 <w< td=""><td>0</td><td></td><td>-</td><td></td></w<>	0		-		
Dent		Serious one	is not allowed			А	
Wrinkles in polarizer		Serious one	is not allowed			А	
	Average diam	eter (mm)	Max	kimum n	umber		
Bubbles on polarizer	D<	0.3		Ignore	d	А	
Bubbles on polarizer	0.3≦D≦	0.6		4		A	
	0.6 <d< td=""><td></td><td></td><td colspan="2"></td></d<>						
	Length (mm)	Widt	h (mm)	Max	imum number	А	
	L≦2.0	W	/≦1.5	5		~	
1) Stains	L>2.0	1.5 <w< td=""><td colspan="5">1.5<w< th="">0Round (Dot shape)</w<></td></w<>	1.5 <w< th="">0Round (Dot shape)</w<>				
2) Foreign Materials							
3) Dark Spot	Average diameter (n	nm) Maximu	m number	Minimum Space			
	D<0.2	lgr	ored		-	А	
	$0.2 {\leq} D {\leq} 0.6$		4		-	~	
	0.6 <d< td=""><td></td><td>0</td><td></td><td>-</td><td colspan="2"></td></d<>		0		-		
	T	hose wiped out e	asily are acce	otable			
		Area①	Area(2)	Max	imum number		
Dot-Defect	Bright dot-defect	1 dot	2 dot		3 dot	А	
	Dark dot-defect	2 dot	3 dot		4 dot	(Note 1)	
	Bright + Dark poir	nt 3 dot	4 dot		5 dot		

Note 1: The Dot-Defect inspection within A zone (active area) would be divided into area ①, ② as Fig. 11.3 shown.





### **12. PRECAUTIONS**

#### 12.1 PRECAUTIONS of ESD

- 1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.
- 2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

#### **12.2 PRECAUTIONS of HANDLING**

- 1) In order to keep the appearance of display in good condition; please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.
- 2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.
- 3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.
- 4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.
- 5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.
- 6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.
- 7) Maximum pressure to the surface of the display must be less than  $1.96 \times 10^4$  Pa. If the area of adding pressure is less than  $1 \text{ cm}^2$ , the maximum pressure must be less than 1.96N.

#### **12.3 PRECAUTIONS OF OPERATING**

- 1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.
- 2) When the display is operating at significant low temperature, the response time will be slower than it at 25 C°. In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.
- 3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.
- 4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than  $\pm 100$  mV.

NO.

#### 12.4 PRECAUTIONS of STORAGE

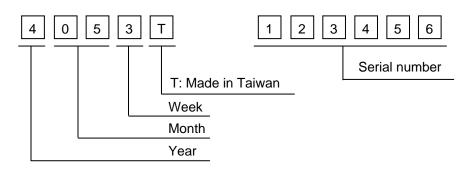
If the displays are going to be stored for years, please be aware the following notices.

- 1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
- 2) The recommended long-term storage temperature is between 10 C° ~35 C° and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
- 3) It would be better to keep the displays in the container, which is shipped from KOE, and do not unpack it.
- 4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

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### 13. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.13.3. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.



2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Mark	
4	
5	
6	
7	
8	

Month	Mark	Month	Mark
1	01	7	07
2	02	8	08
3	03	9	09
4	04	10	10
5	05	11	11
6	06	12	12

Week (Days)	Mark
1~7	1
8~14	2
15~21	3
22~28	4
29~31	5

3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.

4) The location of the lot mark is on the back of the display shown in Fig. 13.3.



Fig 13.3