

Kaohsiung Opto-Electronics Inc.

FOR MESSRS:	DATE : May	/ 29 <sup>th</sup>	,2014

# **CUSTOMER'S ACCEPTANCE SPECIFICATIONS**

# TX26D200VM5BPA

# Contents

No.	ITEM	SHEET No.	PAGE
1	COVER	7B64PS 2701-TX26D200VM5BPA-1	1-1/1
2	RECORD OF REVISION	7B64PS 2702-TX26D200VM5BPA-1	2-1/1
3	GENERAL DATA	7B64PS 2703-TX26D200VM5BPA-1	3-1/1
4	ABSOLUTE MAXIMUM RATINGS	7B64PS 2704-TX26D200VM5BPA-1	4-1/1
5	ELECTRICAL CHARACTERISTICS	7B64PS 2705-TX26D200VM5BPA-1	5-1/2~2/2
6	OPTICAL CHARACTERISTICS	7B64PS 2706-TX26D200VM5BPA-1	6-1/2~2/2
7	BLOCK DIAGRAM	7B64PS 2707-TX26D200VM5BPA-1	7-1/1
8	RELIABILITY TESTS	7B64PS 2708-TX26D200VM5BPA-1	8-1/1
9	LCD INTERFACE	7B64PS 2709-TX26D200VM5BPA-1	9-1/7~7/7
10	OUTLINE DIMENSIONS	7B64PS 2710-TX26D200VM5BPA-1	10-1/2~2/2
11	TOUCH PANEL	7B64PS 2711-TX26D200VM5BPA-1	11-1/2~2/2
12	APPEARANCE STANDARD	7B64PS 2712-TX26D200VM5BPA-1	12-1/3~3/3
13	PRECAUTIONS	7B64PS 2713-TX26D200VM5BPA-1	13-1/2~2/2
14	DESIGNATION OF LOT MARK	7B64PS 2714-TX26D200VM5BPA-1	14-1/1

ACCEPTED BY:	PROPOSED BY: Leulle
--------------	---------------------

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2701-TX26D200VM5BPA-1	PAGE	1-1/1	
				i '	1

2. RECORD OF REVISION						
DATE	SHEET No.		SUMMARY			
		01:555				
KAOHSIUNG OF	PTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2702-TX26D200VM5BPA-1	PAGE	2-1/1	

# 3. GENERAL DATA

### 3.1 DISPLAY FEATURES

This module is a 10.4" SVGA of 4:3 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially. This display is RoHS compliant, and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX26D200VM5BPA
Module Dimensions	230.0(W) mm x 180.2(H) mm x 12.2(D) mm
LCD Active Area	211.2(W) mm x 158.4(H) mm
Pixel Pitch	0.264(W) mm x 0.264(H) mm
Resolution	800 x 3(RGB)(W) x 600(H) Dots
Color Pixel Arrangement	R, G, B Vertical Stripe
LCD Type	Transmissive Color TFT; Normally White
Display Type	Active Matrix
Number of Colors	16.7M Colors (8-bit RGB)
Backlight	Light Emitting Diode (LED)
Weight	380g
Interface	LVDS; 20 pins
Power Supply Voltage	3.3V for LCD; 12V for Backlight
Power Consumption	1.16W for LCD; 5.28W for Backlight
Viewing Direction	Super Wide Version
Touch Panel	Resistive type; Film on glass; 4-wire type; Anti-glare surface

# 4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	$V_{DD}$	0	5.0	V	-
Input Voltage of Logic	VI	-0.3	V <sub>DD</sub> +0.3	V	Note 1
Operating Temperature	T <sub>op</sub>	-20	70	°C	Note 2
Storage Temperature	T <sub>st</sub>	-30	80	°C	Note 2
Backlight Input Voltage	$V_{LED}$	-	15	٧	-

- Note 1: The rating is defined for the signal voltages of the interface such as CLK and pixel data pairs.
- Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:
  - Background color, contrast and response time would be different in temperatures other than  $25\,^{\circ}\mathrm{C}\,.$
  - Operating under high temperature will shorten LED lifetime.

SHEET
NO.

# 5. ELECTRICAL CHARACTERISTICS

### 5.1 LCD CHARACTERISTICS

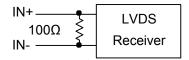
 $T_a = 25$  °C, Vss = 0V

PAGE

5-1/2

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	$V_{DD}$	-	3.0	3.3	3.6	V	-
Differential Input	.,	"H" level	-	-	+100	.,	
Voltage for LVDS Receiver Threshold	Vı	"L" level	-100	-	-	mV	Note 1
Power Supply Current	I <sub>DD</sub>	$V_{DD}$ - $V_{SS}$ =3.3 $V$	-	350	483	mA	Note 2,3
Frame Frequency	$f_{\it Frame}$	-	-	60	66	Hz	
CLK Frequency	$f_{\mathit{CLK}}$	-	32.3	40	50	MHz	-

Note 1: VCM 1.2V is common mode voltage of LVDS transmitter and receiver. The input terminal of LVDS receiver is terminated with  $100\Omega$ .



- Note 2: An all black check pattern is used when measuring  $I_{DD}$ .  $f_{Frame}$  is set to 60Hz.
- Note 3: 1.0A fuse is applied in the module for I<sub>DD</sub>. For display activation and protection purpose, power supply is recommended larger than 2.5A to start the display and break fuse once any short circuit occurred.

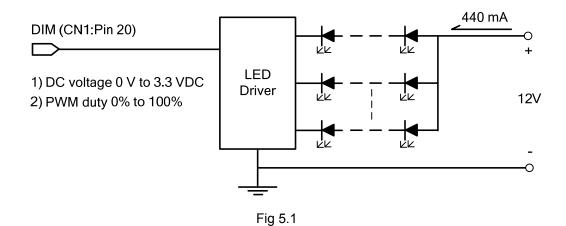
KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2705-TX26D200VM5BPA-1	
---------------------------------	--------------	------------------------------	--

### 5.2 BACKLIGHT CHARACTERISTICS

 $T_a = 25 \, {}^{\circ}C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
LED Input Voltage	$V_{LED}$	-	11.0	12.0	13.0	V	Note1
1505 10 (		0V; 0% duty	390	440	490	A	N ( 0
LED Forward Current	I <sub>LED</sub>	3.3VDC; 100% duty	50	60	70	mA	Note 2
LED lifetime	-	I <sub>LED</sub> =440 mA	-	70K	-	hrs	Note 3

- Note 1: As Fig. 5.1 shown, LED current is constant, 440 mA, controlled by the LED driver when applying 12V  $V_{\text{LED}}$ .
- Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1K ~ 10K Hz with 3.3V amplitude.
- Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 440 mA at  $25^{\circ}$ C.



# 6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25°C.
- In the dark room less than 100 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$$T_a = 25 \, ^{\circ}C, f_{Frame} = 60 \, \text{Hz}, \text{Vdd} = 3.3 \text{V}$$

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Brightness of	f White	-		640	800	-	cd/m²	Note 1
Brightness Ur	niformity	-	$\phi = 0^{\circ}, \theta = 0^{\circ},$	70	-	-	%	Note 2
Contrast F	Ratio	CR	I <sub>LED</sub> = 440 mA	200	400	-	-	Note 3
Response	Time	Tr + Tf	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	20	-	ms	Note 4
NTSC Ra	atio	-	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	50	-	%	-
		$\theta$ x	$\phi = 0^{\circ}$ , CR $\geq 10$	-	80	-		
) (i a coista a co		$\theta$ x'	φ = 180 °, CR ≥ 10	-	80	-	Degree	Note 5
Viewing A	ngie	$\theta$ y	φ = 90°, CR ≥ 10	_	80	-		
		$\theta$ y'	$\phi = 270^{\circ}, CR \ge 10$	-	80	-		
	Dod	Χ		0.55	0.60	0.65		
	Red	Υ		0.28	0.33	0.38		
	0	Χ		0.29	0.34	0.39		
Color	Green	Υ		0.55	0.60	0.65		Note C
Chromaticity	Dive	Χ	$\phi = 0^{\circ}, \theta = 0^{\circ}$	0.10	0.15	0.20	-	Note 6
	Blue	Υ		0.10	0.15	0.20		
	\\/\bits	Х		0.24	0.29	0.34		
	White	Y		0.29	0.34	0.39		

Note 1: The brightness is measured from 9 point of the panel, P1~P9 in Fig. 6.2, for the average value.

Note 2: The brightness uniformity is calculated by the equation as below:

Brightness uniformity = 
$$\frac{\text{Min. Brightness}}{\text{Max. Brightness}}$$
 X100%

SHEET

NO.

KAOHSIUNG OPTO-ELECTRONICS INC.

which is based on the brightness values of the 9 points in active area measured by BM-5 as

7B64PS 2706-TX26D200VM5BPA-1

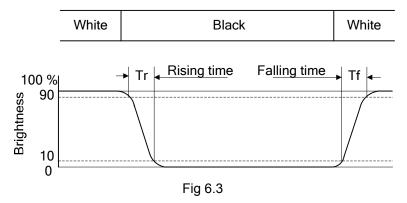
PAGE

6-1/2

Note 3: The Contrast ratio is measured from the center point of the panel, P5, and defined as the following equation:

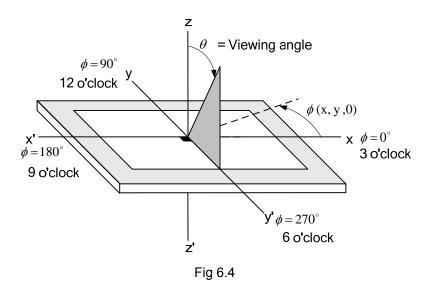
CR = Brightness of White
Brightness of Black

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 90% brightness to 10% brightness when the data is from white to black. Oppositely, Falling time is the period from 90% brightness rising to 10% brightness.



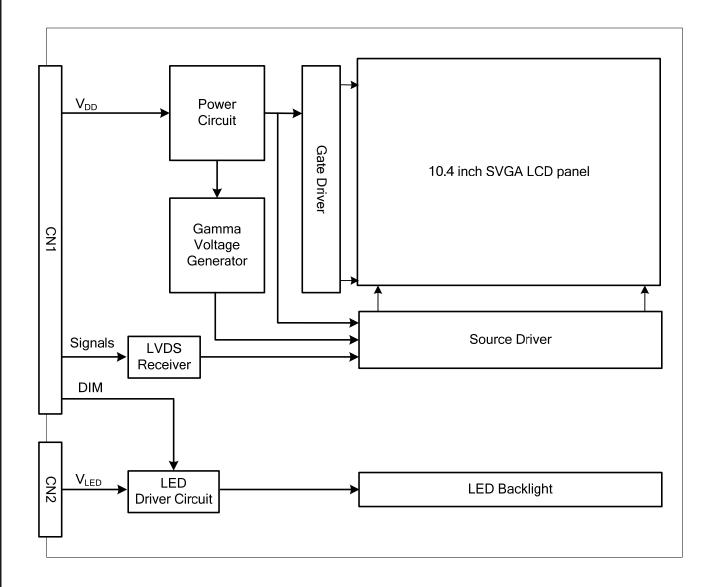
Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle  $\phi$  is used to represent viewing directions, for instance,  $\phi = 270^{\circ}$  means 6 o'clock, and  $\phi = 0^{\circ}$  means 3 o'clock. Moreover, angle  $\theta$  is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.



Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

# 7. BLOCK DIAGRAM

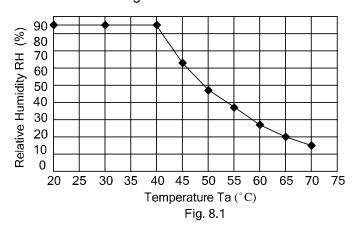


Note: Signals are CLK and pixel data pairs.

# 8. RELIABILITY TESTS

Test Item	Condition					
High Temperature	1) Operating 2) 70	240 hrs				
Low Temperature	1) Operating 2) -20	240 hrs				
High Temperature	1) Storage 2) 80	240 hrs				
Low Temperature	1) Storage 2) -30	240 hrs				
Heat Cycle	1) Operating 2) -20 ~70 3) 3hrs~1hr~3hrs	240 hrs				
Thermal Shock	<ol> <li>1) Non-Operating</li> <li>2) -35</li></ol>	240 hrs				
High Temperature & Humidity	1) Operating 2) 40 & 85%RH 3) Without condensation	240 hrs (Note 3)				
Vibration	1) Non-Operating 2) 20 ~ 200 Hz 3) 2G 4) X, Y, and Z directions	1 hr for each direction				
Mechanical Shock	<ul> <li>1) Non-Operating</li> <li>2) 10 ms</li> <li>3) 50G</li> <li>4) ±X, ±Y and ±Z directions</li> </ul>	Once for each direction				
ESD	<ol> <li>Operating</li> <li>Tip: 150 pF, 330 Ω</li> <li>Air discharge for glass: ± 8KV</li> <li>Contact discharge for metal frame: ± 8KV</li> </ol>	1) Glass: 9 points 2) Metal frame: 8 points (Note 4)				

- Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.
- Note 2: The display is not guaranteed for use in corrosive gas environments.
- Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 40 , the humidity needs to be reduced as Fig. 8.1 shown.



Note 4: All pins of LCD interface (CN1) have been tested by  $\pm$  100V contact discharge of ESD under non-operating condition.

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2708-TX26D200VM5BPA-1	PAGE	8-1/1	
---------------------------------	--------------	------------------------------	------	-------	--

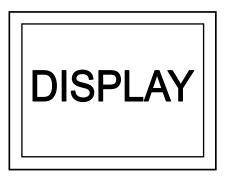
## 9. LCD INTERFACE

### 9.1 INTERFACE PIN CONNECTIONS

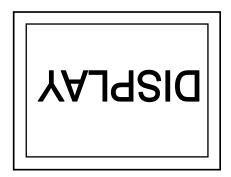
The display interface connector is CN1 FI-SEB20P-HF13E made by JAE and pin assignment is as below:

Pin No.	Signal	Signal	Pin No.	Signal	Signal		
1	$V_{DD}$	Power Supply for Logic	11	IN2-	Divel Date		
2	SD	Scan Direction Control (Note 1)	12	IN2+	Pixel Data		
3	$V_{SS}$	CND	13	V <sub>SS</sub>	GND		
4	$V_{SS}$	GND	14	CLK IN-	Pixel Clock		
5	INO-	Divel Date	15	CLK IN+	Pixei Clock		
6	IN0+	Pixel Data	16	V <sub>SS</sub>	GND		
7	$V_{SS}$	GND	17	IN3-	Divisi Data		
8	IN1-	Divel Date	18	IN3+	Pixel Data		
9	IN1+	Pixel Data	19	NC	No Connection		
10	V <sub>SS</sub>	GND	20	DIM	Note 3		

Note 1: Scan direction is available to be switched as below.



SD: High or Open (Default)



SD: Low

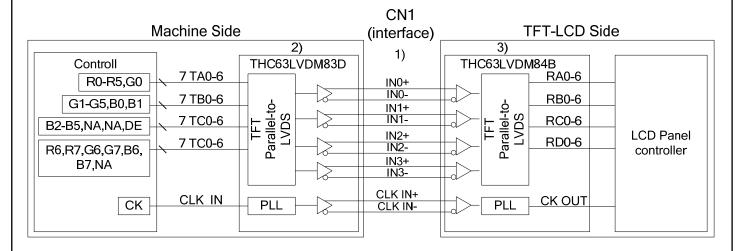
- Note 2: INn- and INn+ (n=0,1,2,3), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.
- Note 3: Note 3: Normal brightness: 0V or 0% PWM duty; Brightness control: 0V to 3.3V DC or 0% to 100% PWM duty.

The backlight interface connector CN2 is SM02(8.0)B-BHS-1-TB made by JST, and pin assignment o is below:

Pin No.	Signal	Level	Function
1	$V_{LED}$ +	-	Power Supply for LED
2	V <sub>LED</sub> -	-	GND

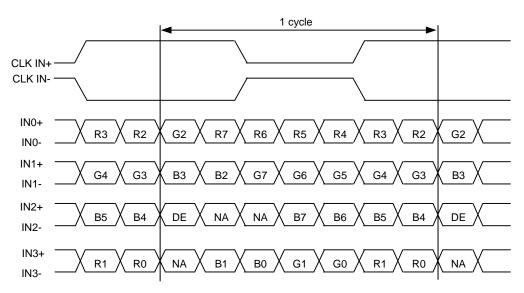
KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2709-TX26D200VM5BPA-1	PAGE	9-1/7	
---------------------------------	--------------	------------------------------	------	-------	--

#### 9.2 LVDS INTERFACE



- Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+,-) is used in differential mode.
- Note 2: The recommended transmitter, THC63LVDM83R, is made by Thine or equivalent, which is not contained in the module.
- Note 3: The receiver built-in the module is THC63LVDM84B.

### 9.3 LVDS DATA FORMAT



DE: Display Enable NA: Not Available

7B64PS 2709-TX26D200VM5BPA-1

9-2/7

# 9.4 TIMING CHART th = 1056 CLK (1H) DE CLK 40M Hz (typ 46 CLK (typ.) 210CLK (typ.) thd = 800 CLK (fixed) Invalid data Display data Invalid data R [0:7] G [0:7] B [0:7] Fig. 9.1 Horizontal Timing tv = 635 H (60 Hz)DE tvd = 600 H (fixed)23H (typ.) 12H (typ.) Invalid lines Display lines Invalid lines **RGB** Fig. 9.2 Vertical Timing Tcph Tcwh CLK 1st 30% Tdsu Tdhd Tcwl 70% 1st RGB 2nd RGB 800 RGB Data 30% Tehd Tesu 70% DE Fig. 9.3 Setup & Hold Time SHEET KAOHSIUNG OPTO-ELECTRONICS INC. 7B64PS 2709-TX26D200VM5BPA-1 **PAGE** 9-3/7 NO.

### 9.5 TIME TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (Vsync) = 60 Hz to define. If 60 Hz is not the aim to set, less than 66 Hz for Vsync is recommended to apply for better performance by other parameter combination as the definitions in section 5.1.

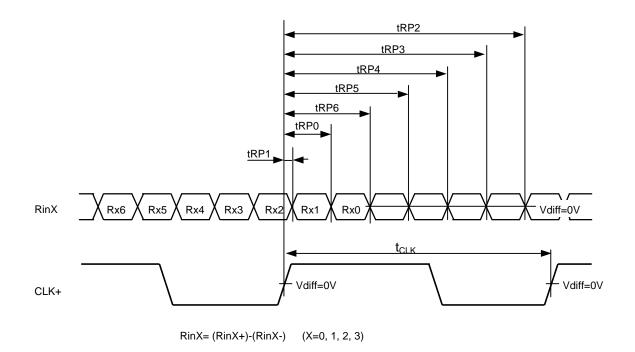
### A. Horizontal and Vertical Timing

	Item	Symbol	Min.	Тур.	Max.	Unit
	CLK Frequency	fclk	32.3	40	50	M Hz
Horizontal	Display Data	thd		01.14		
	Cycle Time	th	862	1056	1200	CLK
Mantiaal	Display Data	tvd				
Vertical	Cycle Time	tv	624	635	700	Н

### B. Setup and Hold Time

	Item	Symbol	Min.	Тур.	Max.	Unit
CLX	Duty	Tcwh	40	50	60	%
CLK	Cycle Time	Tcph	20	25	1	
Data	Setup Time	Tdsu	8	-	1	
Data	Hold Time	Tdhd	8	-	1	ns
DE	Setup Time	Tesu	8	-	-	
DE	Hold Time	Tehd	8	-	-	

### 9.6 LVDS RECEIVER TIMING



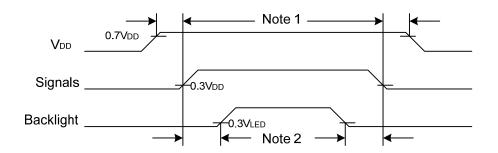
	Item	Symbol	Min.	Тур.	Max.	Unit
CLK	Cycle frequency	1/tcLK	32.3	40	50	MHz
	0 data position	tRP0	1/7* t <sub>CLK</sub> -0.49	1/7* t <sub>CLK</sub>	1/7* t <sub>CLK</sub> +0.49	
	1st data position	tRP1	-0.49	0	+0.49	
DiaV	2nd data position	tRP2	6/7* t <sub>CLK</sub> -0.49	6/7* t <sub>CLK</sub>	6/7* t <sub>CLK</sub> +0.49	
RinX	3rd data position	tRP3	5/7* t <sub>CLK</sub> -0.49	5/7* t <sub>CLK</sub>	5/7* t <sub>CLK</sub> +0.49	ns
(X=0,1,2,3)	4th data position	tRP4	4/7* t <sub>CLK</sub> -0.49	4/7* t <sub>CLK</sub>	4/7* t <sub>CLK</sub> +0.49	
	5th data position	oth data position tRP5		3/7* t <sub>CLK</sub>	3/7* t <sub>CLK</sub> +0.49	
	6th data position	tRP6	2/7* t <sub>CLK</sub> -0.49	2/7* t <sub>CLK</sub>	2/7* t <sub>CLK</sub> +0.49	

# 9.7 DATA INPUT for DISPLAY COLOR

					Red	Data	l					G	reen	Dat	a					ı	Blue	Data	ì		
Input		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	В6	B5	B4	ВЗ	B2	В1	В0
colo	r	MSB							LSB	MSB					•		LSB	MSB			•				LSB
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	=	:	:	:	:
Cicon	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Diao	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
															I						I				

KAOHSIUNG OPTO-ELECTRONICS INC	).
--------------------------------	----

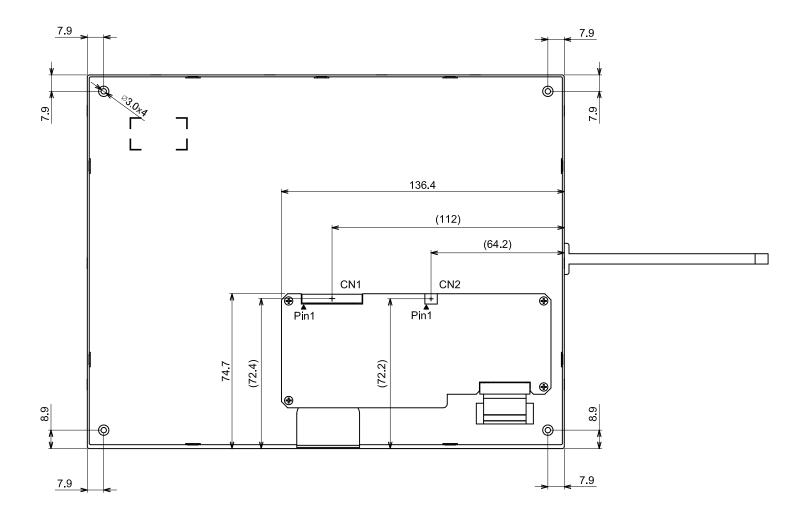
### 9.8 POWER SEQUENCE



- Note 1: In order to avoid any damages, V<sub>DD</sub> has to be applied before all other signals. The opposite is true for power off where V<sub>DD</sub> has to be remained on until all other signals have been switch off. The recommended time period is 1 second. Hot plugging might cause display damage due to incorrect power sequence, please pay attention on interface connecting before power on.
- Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

# 10. OUTLINE DIMENSIONS 10.1 FRONT VIEW 230.0 224.4±0.3 (T/P Outline) 1.7 213.2±0.3 (T/P A.A) 8.4 211.2 (LCD Active Area) 9.4 P1.0x3=3.0±0.1 Top View \_10.0±0.5\_ 5.0±0.3 160.4±0.3 (T/P A.A) 175.4±0.3 (T/P Outline) 158.4(LCD Active Area) - A.A. Center Detail A (4) Detial A 9.8 115.0 115.0 Stiffener Tape General Tolerance:±0.5mm Scale: NTS $0.3\pm0.05$ Unit: mm Conductive Side SHEET Detail A 7B64PS 2710-TX26D200VM5BPA-1 PAGE 10-1/2 KAOHSIUNG OPTO-ELECTRONICS INC.

### 10.2 REAR VIEW



General Tolerance:±0.5mm Scale: NTS

Unit : mm

KAOHSIUNG OPTO-ELECTRONICS INC. SHEET No. 7B64PS 2710-TX26D200VM5BPA-1 PAGE 10-2/2

# 11. TOUCH PANEL

The type of touch panel used on this display is resistive, analog, 4-wire and film on glass, and more characteristics are shown as below:

#### 11.1 OPERATING CONDITIONS

Item	Specification	Remarks			
Operating Voltage	5VDC	-			

### 11.2 ELECTRICAL CHARACTERISTICS

Item		Specification	Remarks		
Resistance	stance X1-X2				
Between Terminal	Y1-Y2	<b>210~600</b> Ω	_		
Insulation Resistance	X-Y	20M $\Omega$ min.	At 25V DC		
12	X	±1.5% max.	Note 4		
Linearity	Y	±1.5% max.	Note 1		
Chattering		10ms max.	-		

Note 1: The test conditions and equipments of linearity are as below:

- Material of pen: poly-acetal resin

- End shape: R 0.8 mm

- Test force: 150 g

- Pitch: 10 mm

- Test area is shown in Fig. 11.1

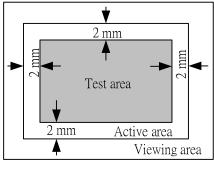
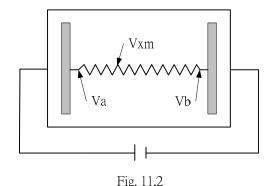


Fig. 11.1



As shown in Fig. 11.2, applying voltage meter to measure Va, Vb and Vxm, where Va is the maximum voltage in the active area; Vb is the minimum voltage in the active area; Vxm is the measured voltage of point x selected by random. Afterwards, the linearity can be calculated by following equation:

$$Linearity = \frac{|Vxi - Vxm|}{Va - Vb} \times 100\%,$$

where Vxi is the idea voltage of point x.

The method to measure the linearity of Y-axis is the same as above.

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2711-TX26D200VM5BPA-1	PAGE	11-1/2	
---------------------------------	--------------	------------------------------	------	--------	--

### 11.3 MECHANICAL CHARACTERISTICS

Item	Specification	Remarks	
Pen Input Pressure	1.2N max.	R0.8, Polyacetal Pen	
Finger	1.2N max.	R8.0, Silicon Rubber	
Surface Hardness	3H min.	JIS K 5400	

### 11.4 OPTICAL CHARACTERISTICS

Item	Specification	Remarks
Transmittance	78% min.	-

### 11.5 SAFETY AND ATTENTIONS

- 1) Do not put heavy shock or stress on the touch panel.
- 2) Please use soft cloth or absorbent cotton with ethanol to clean the touch panel by gently wiping. Moreover, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the touch panel's surface.
- 3) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean the display's surface.
- 4) UV protection is recommended to avoid the possibility of performance degrading when touch panel is likely applied under UV environment for a long period of time.

SHEET	•
NO.	

## 12. APPEARANCE STANDARD

The appearance inspection is performed in a room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle  $\theta$  shown in Fig. 12.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

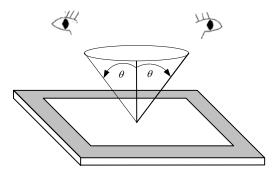


Fig. 12.1

### 12.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 2 areas as shown in Fig.12.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area between A zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

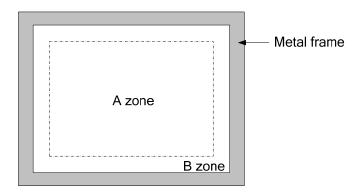


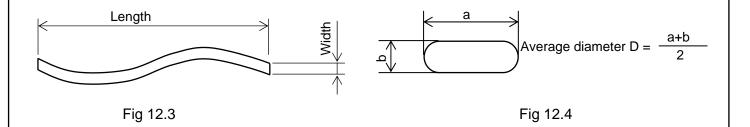
Fig. 12.2

### 12.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 12.3 and Fig. 12.4.

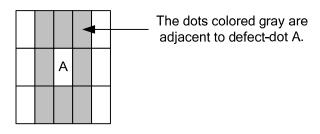
Item	Criteria				Applied zone			
	Length (mm)	gth (mm) Widt		Maximum n	umber	Minimum space		
Canatahaa	Ignored		W≦0.02	Ignored		-	A D	
Scratches	L≦40	0.02	<w≦0.04< td=""><td>10</td><td></td><td>-</td><td>A,B</td></w≦0.04<>	10		-	A,B	
	L≦20		W < 0.04	10	-			
Dent			Serious one is	s not allowed	ot allowed		А	
Wrinkles in polarizer			Serious one is	s not allowed			Α	
	Average dia	ametei	r (mm)	Ma	ximum	number		
		D≦0.3	3		Ignor	ed		
Bubbles on polarizer	0.3<	D≦0.	5		10		Α	
	0.5<	D≦1.	0		5			
	1.0<	D			non	е		
			Filamentous	(Line shape)				
	Length (mm)		Width	(mm)	Maximum number			
	L : Ignored		W≦0.06		Ignored		A,B	
	L≦1.0 1.0 <l 0.<="" td=""><td>0.06 &lt; W</td><td colspan="2">0.00 &lt; 1/1</td><td>Ignored</td><td></td></l>		0.06 < W	0.00 < 1/1		Ignored		
1) Stains			0.06 < W		(See Dot shape)			
2) Foreign Materials	Round (Dot shape)							
3) Dark Spot	Average diameter (mm) Maximum			number	Mir	imum Space		
	D≦0.45 lg		Igno	red		-	A,B	
	0.45 <d≦0.7< td=""><td>7</td><td colspan="2">5</td><td></td><td>-</td><td>A,D</td></d≦0.7<>	7	5			-	A,D	
	0.7 <d< td=""><td></td><td colspan="2">none</td><td></td><td>-</td><td></td></d<>		none			-		
	Those wiped out eas	ily are a	acceptable					
		Ty	ype		Max	imum number		
	1		1 c	lot		4		
			2 adjacent dot		2			
	Bright dot-defect		3 adjacent dot or above					
			Density					
Dot-Defect			In total		6		Α	
Dot-Defect			1 c	1 dot		5	(Note 1)	
			2 adjacent dot		2 ve Not allowed 3/φ 20mm			
			3 adjacent dot or above					
			Density					
			In total		5			
		In t	total			11		

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2712-TX26D200VM5BPA-1	PAGE	12-2/3	
---------------------------------	--------------	------------------------------	------	--------	--



Note 1: The definitions of dot defect are as below:

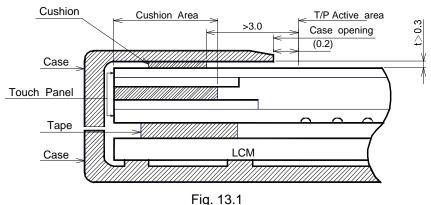
- The defect area of the dot must be bigger than half of a dot.
- For bright dot-defect, showing black pattern, the dot's brightness must be over 30% brighter than others.
- For dark dot-defect, showing white pattern, the dot's brightness must be under 70% darker than others.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 12.5.
- The Density of dot defect is defined in the area within diameter  $\phi$  =20mm.



## 13. PRECAUTIONS

### 13.1 PRECAUTIONS of TOUCH PANEL

- 1) Please refer to Fig. 13.1 for housing the display with touch panel into applications. The Fig. 13.1 shows some points as below:
- The cushion needs to be designed between housing and touch panel in order to avoid unexpected pressure to cause any wrong reactions, and the cushion should be located in the cushion area.
- The housing should not cover the active area of touch panel as the figure shown.



### 13.2 PRECAUTIONS of ESD

- 1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.
- 2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

#### 13.3 PRECAUTIONS of HANDLING

- 1) In order to keep the appearance of display in good condition, please do not rub any surfaces of the displays by using sharp tools harder than 3H, especially touch panel, metal frame and polarizer.
- 2) Please do not stack the displays as this may damage the surface. In order to avoid any injuries, please avoid touching the edge of the glass or metal frame and wore gloves during handling.
- 3) Touching the polarizer or terminal pins with bare hand should be avoided to prevent staining and poor electrical contact.
- 4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.
- 5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.
- 6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanent damages.
- 7) Maximum pressure to the surface of the display must be less than 1.96 x 10<sup>4</sup> Pa. If the area of applied pressure is less than 1 cm<sup>2</sup>, the maximum pressure must be less than 1.96N.

SHEET	
NO.	

#### 13.4 PRECAUTIONS of OPERATING

- 1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.
- 2) When the display is operating at significant low temperature, the response time will be slower than it at 25 °C . In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.
- 3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.
- 4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than  $\pm$  100 mV.

### 13.5 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

- 1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
- 2) The recommended long term storage temperature is between 10 °C ~35 °C and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
- 3) It would be better to keep the displays in the container, which is shipped from KOE, and do not unpack it.
- 4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

# 14. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.14.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

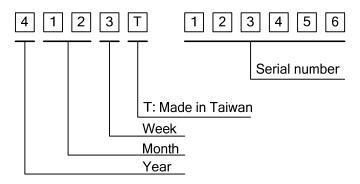


Fig. 14.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2014	4
2015	5
2016	6
2017	7
2018	8

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Lot Mark
1~7 days	1
8~14 days	2
15~21 days	3
22~28 days	4
29~31 days	5

- 3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.
- 4) The location of the lot mark is on the back of the display shown in Fig. 14.2.



Fig. 14.2