KOE

JDI Group

Kaohsiung Opto-Electronics Inc.

FOR MESSRS : _____ DATE : Sep. 25st ,2012

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX31D37VM0CPA

Contents

ACCEPTED BY: _____ PROPOSED BY:

2. RECORD OF REVISION

DATE	SHEET No.	SUMMARY
Sep. 25,'12	7B64PS-2710-TX31D37VM0CPA-2 Page 10-1/2	10.1 Front View Revised：Define scales with correct scan direction (180° rotated).
	7B64PS-2710-TX31D37VM0CPA-2 Page 10-2/2	10.2 Rear View Revised：Define scales with correct scan direction (180° rotated).

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 12.1" SVGA of 4:3 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R (red), G (green), B (blue) sequentially. This display is RoHS compliant, COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX31D37VM0CPA
Module Dimensions	280.0(W) mm x 210.0(H) mm x 14.9 (D) mm typ.
LCD Active Area	246.0(W) mm x 184.5(H) mm
Pixel Pitch	0.3075(W) mm x 0.3075 (H) mm
Resolution	800 x 3(RGB)(W) x 600(H) dots
Color Pixel Arrangement	R, G, B Vertical stripe
LCD Type	Transmissive Color TFT; Normally Black
Display Type	Active Matrix
Number of Colors	16.7M Colors
Backlight	24 LEDs (3 series x 8)
Weight	930 typ. (g)
Interface	LVDS; 20 pins
Power Supply Voltage	3.3V for LCD; 12V for Backlight
Power Consumption	1.815W for LCD; 9.6W for Backlight
Viewing Direction	Super Wide Version (In-Plane Switching)
Touch Panel	Resistive type; Film on glass; 4-wire type; Anti-glare surface

4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V_{DD}	0	4.0	V	-
Input Voltage of Logic	V_I	-0.3	V_{DD}+0.3	V	Note 1
Operating Temperature	Top	-20	70	˚C	Note 2
Storage Temperature	Tst	-30	80	˚C	Note 2
Backlight Input Voltage	V_{LED}	-	15	V	-

Note 1: The rating is defined for the signal voltages of the interface such as CLK and pixel data pairs.

Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25˚C.
- Operating under high temperature will shorten LED lifetime.

5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

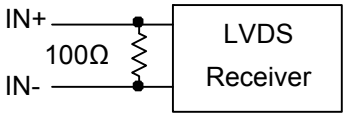
$T_a = 25\ ^\circ C,\ V_{SS} = 0V$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Differential Input Voltage for LVDS Receiver Threshold	VI	"H" level	-	-	+100	mV	Note 1
		"L" level	-100	-	-		
Voltage Input for AMode	VF	"H" level	$0.7V_{DD}$	-	V_{DD}	V	COMS Level
		"L" level	0	-	$0.3V_{DD}$		
Power Supply Current	I_{DD}	IV_{DD}=3.3V	-	550	600	mA	Note 2
Vsync Frequency	f_v	-	-	60	75	Hz	-
Hsync Frequency	f_H	-	-	37.7	50.6	KHz	Note 3
CLK Frequency	f_{CLK}	-	37	40	43	MHz	

Note 1: VCM=+1.2V

VCM is common mode voltage of LVDS transmitter/receiver.

The input terminal of LVDS transmitter is terminated with 100Ω.



Note 2: An all white check pattern is used when measuring I_{DD}. f_v is set to 60 Hz.

Note 3: For LVDS transmitter input.

Note 4: 1.0A fuse is applied in the module for I_{DD}. For display activation and protection purpose, power supply is recommended larger than 2.5A to start the display and break fuse once any short circuit occurred.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\ ^\circ C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	V_{LED}	-	11.7	12	12.3	V	Note1
LED Forward Current (Dim Control)	I_{LED}	0V; 0% duty	-	800	-	mA	Note 2
		3.3VDC; 100% duty	-	60	72		
LED lifetime	-	800 mA	-	70K	-	hrs	Note 3

Note 1: As Fig. 5.1 shown, LED current is constant, 800 mA, controlled by the LED driver when applying 12V V_{LED}.

Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1K ~ 10K Hz with 3.3V amplitude.

Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 800 mA at 25°C.

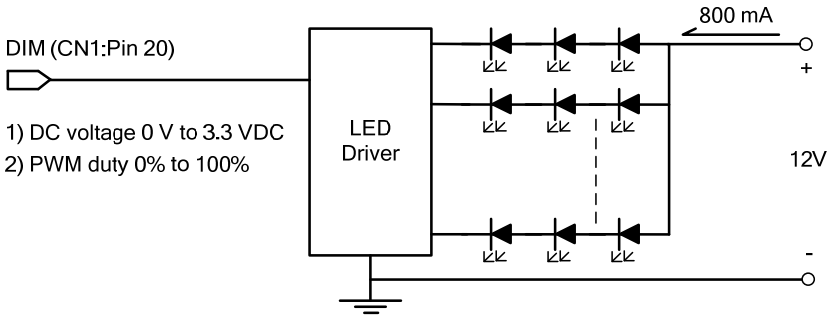


Fig 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25°C.
- In the dark room around 500~1000 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$T_a = 25\ ^\circ C, f_v = 60\,\text{Hz}, V_{DD} = 3.3\text{V}$

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White		-	$\phi = 0^\circ, \theta = 0^\circ$,	380	480	-	cd/m^2	Note 1
Brightness Uniformity		-	I_{LED}= 90	70	-	-	%	Note 2
Contrast Ratio		CR	mA/series	500	1000	-	-	Note 3
Response Time (Rising + Falling)		$T_r + T_f$	$\phi = 0^\circ, \theta = 0^\circ$	-	30	65	ms	Note 4
Viewing Angle		θ x	$\phi = 0^\circ$, CR ≥ 10	75	85	-	Degree	Note 5
		θ x′	$\phi = 180^\circ$, CR ≥ 10	75	85	-		
		θ y	$\phi = 90^\circ$, CR ≥ 10	75	85	-		
		θ y′	$\phi = 270^\circ$, CR ≥ 10	75	85	-		
Color Chromaticity	Red	X	$\phi = 0^\circ, \theta = 0^\circ$	0.58	0.63	0.68	-	Note 6
		Y		0.30	0.35	0.40		
	Green	X		0.28	0.33	0.38		
		Y		0.57	0.62	0.67		
	Blue	X		0.10	0.15	0.20		
		Y		0.07	0.12	0.17		
	White	X		0.28	0.33	0.38		
		Y		0.33	0.38	0.43		

Note 1: The brightness is measured from the panel center point, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \times 100\%$$

, which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.

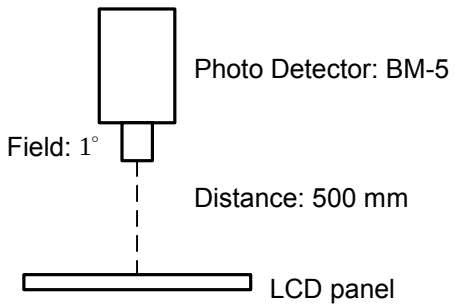


Fig. 6.1

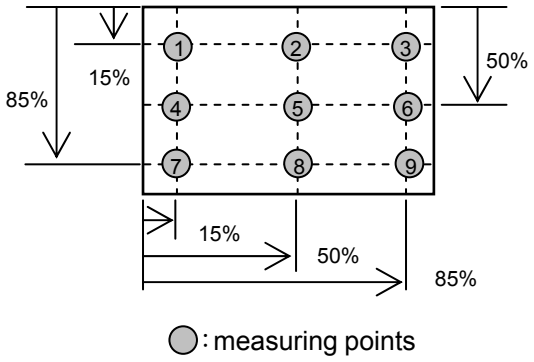


Fig. 6.2

Note 3: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness falling to 10% brightness.

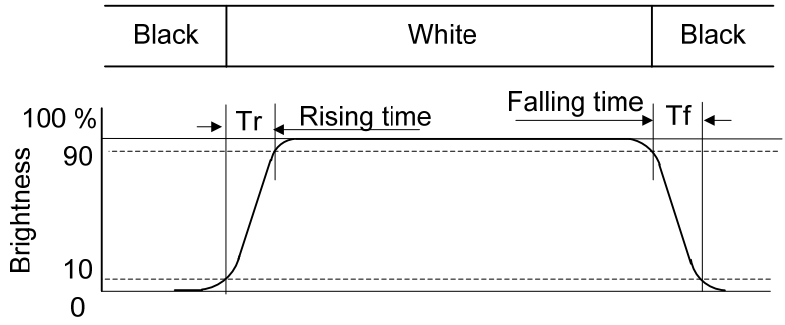


Fig 6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^\circ$ means 6 o'clock, and $\phi = 0^\circ$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

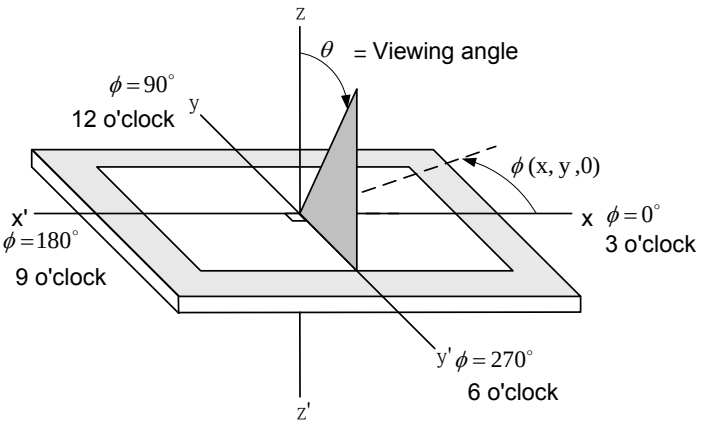
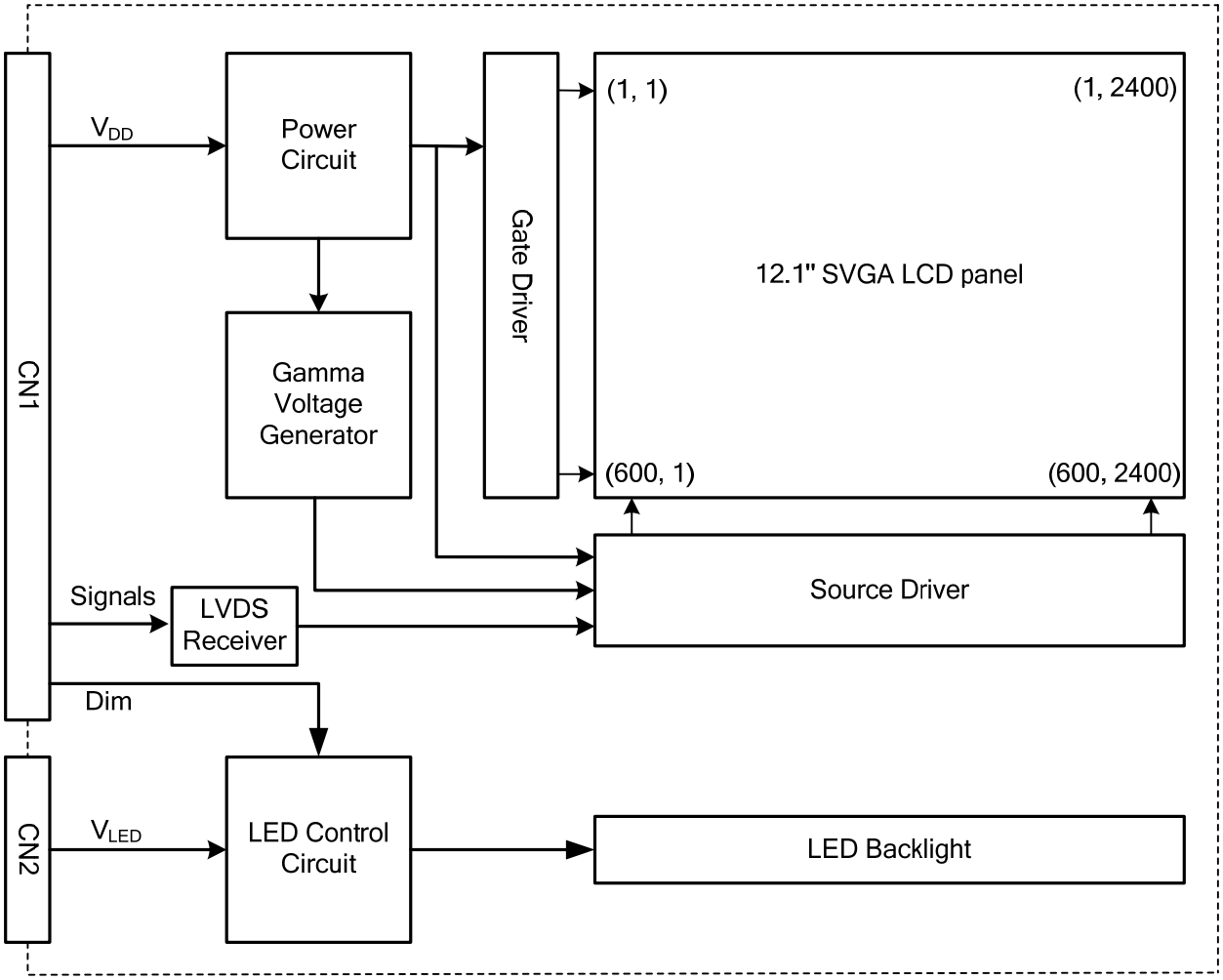


Fig 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



Note1: Signals are CLK and pixel data pairs.

8. RELIABILITY TESTS

Test Item	Condition	
High Temperature	1) Operating 2) 70°C	240 hrs
Low Temperature	1) Operating 2) -20°C	240 hrs
High Temperature	1) Storage 2) 80°C	240 hrs
Low Temperature	1) Storage 2) -30°C	240 hrs
Heat Cycle	1) Operating 2) –20°C ↔ 70°C 3) 1hr~2hr~1hr	240 hrs
Thermal Shock	1) Non-Operating 2) -35°C ↔ 85°C 3) 0.5 hr ↔ 0.5 hr	240 hrs
High Temperature & Humidity	1) Operating 2) 40°C 85%RH 3) Without condensation (Note3)	240 hrs
Vibration	1) Non-Operating 2) 10～150 Hz 3) 3G 4) X, Y, and Z directions	1hr for each direction
Mechanical Shock	1) Non-Operating 2) 10 ms 3) 50G 4) ±X, ± Y and ±Z directions	Once for each direction
ESD	1) Operating 2) Tip: 150 pF, 330Ω 3) Air discharge for glass: ± 8KV 4) Contact discharge for metal frame: ± 8KV	1) Glass: 9 points 2) Metal frame: 8 points (Note4)

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

Note 2: The display is not guaranteed for use in corrosive gas environments.

Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 40℃, the humidity needs to be reduced as Fig. 8.1 shown.

Note 4: All pins of LCD interface(CN1) have been tested by ±100V contact discharge of ESD under non-operating condition.

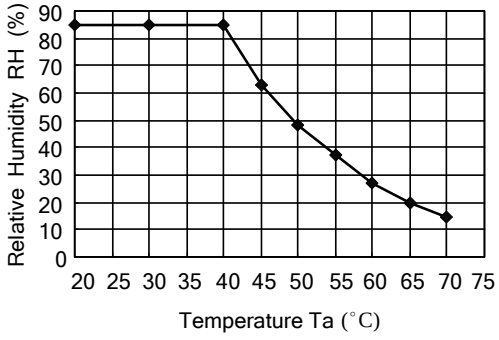


Fig. 8.1

9. LCD INTERFACE

9.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN1) is FI-SEB20P-HF13E made by JAE and Pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	V_{DD}	Power Supply for Logic	11	IN2-	Pixel data
2	V_{DD}		12	IN2+	
3	V_{SS}	GND	13	V_{SS}	GND
4	V_{SS}		14	CLK IN-	Pixel Clock
5	IN0-	Pixel data	15	CLK IN+	
6	IN0+		16	V_{SS}	GND
7	V_{SS}	GND	17	IN3-	Pixel Clock
8	IN1-	Pixel data	18	IN3+	
9	IN1+		19	AMODE	LVDS Data Mapping Setting
10	V_{SS}	GND	20	DIM	Note 2

Note 1: IN n- and IN n+ (n=0, 1, 2), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.

Note 2: Normal brightness: 0V or 0% PWM duty; Brightness control: 0V to 3.3V DC or 0% to 100% PWM duty.

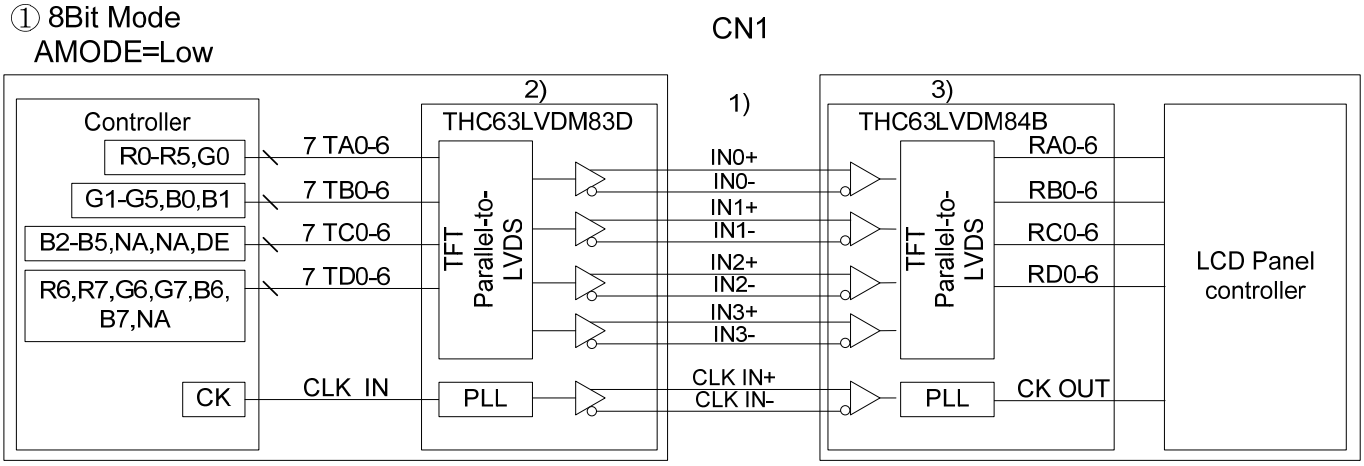
The backlight connector (CN2) is SM02(8.0)B-BHS-1-TB(LF)(SN) made by JST, and pin assignment is as below:

Pin No.	Signal	Signal
1	V_{LED}	12VDC
2	GND	Ground

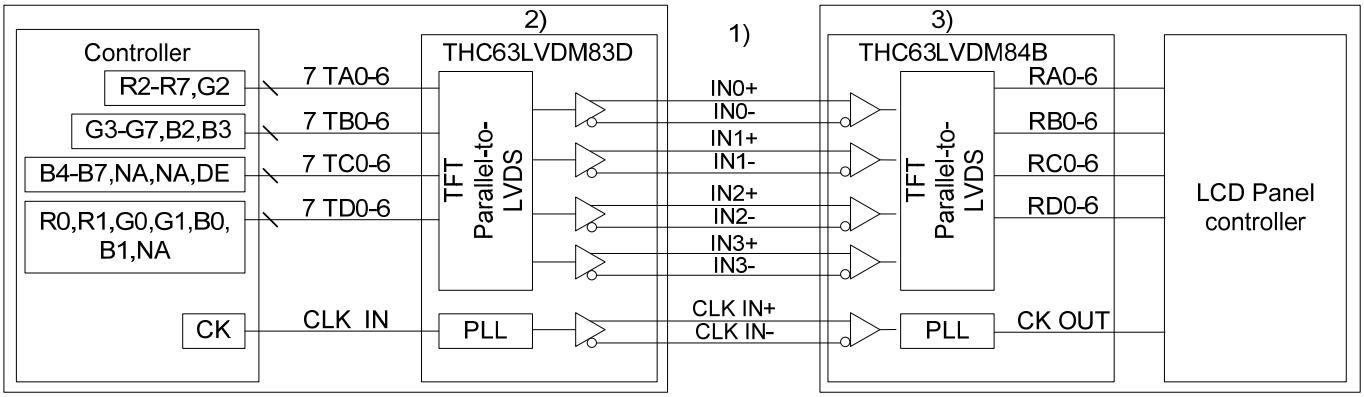
9.2 LVDS INTERFACE

Machine Side TFT-LCD Side

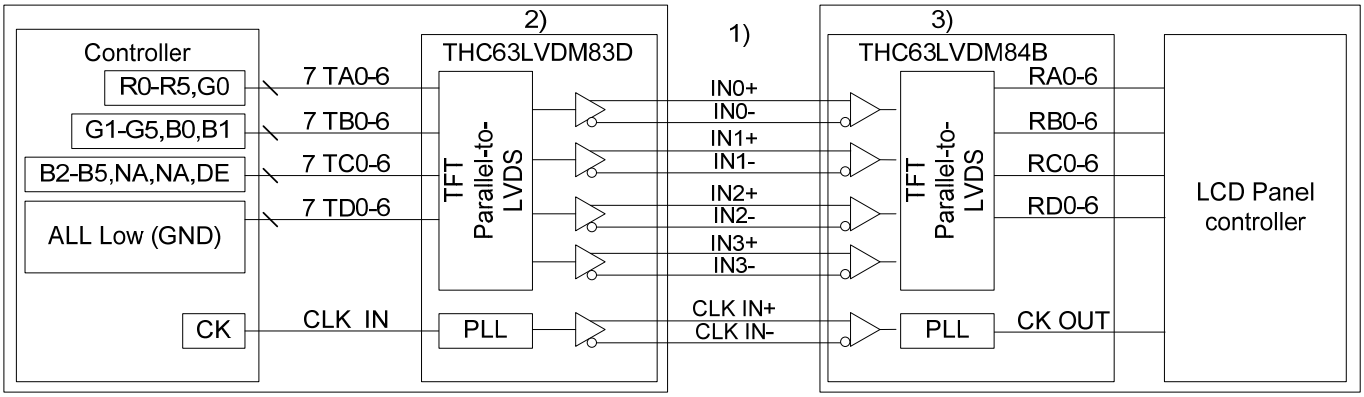
① 8Bit Mode
AMODE=Low



② 8Bit Mode
AMODE=High



③ 6Bit Mode
AMODE=High & Low



Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines(+,-) is used in differential mode.

Note 2: Transmitter Made by Thine : THC63LVDM83D equivalent.
Transmitter is not contained in Module.

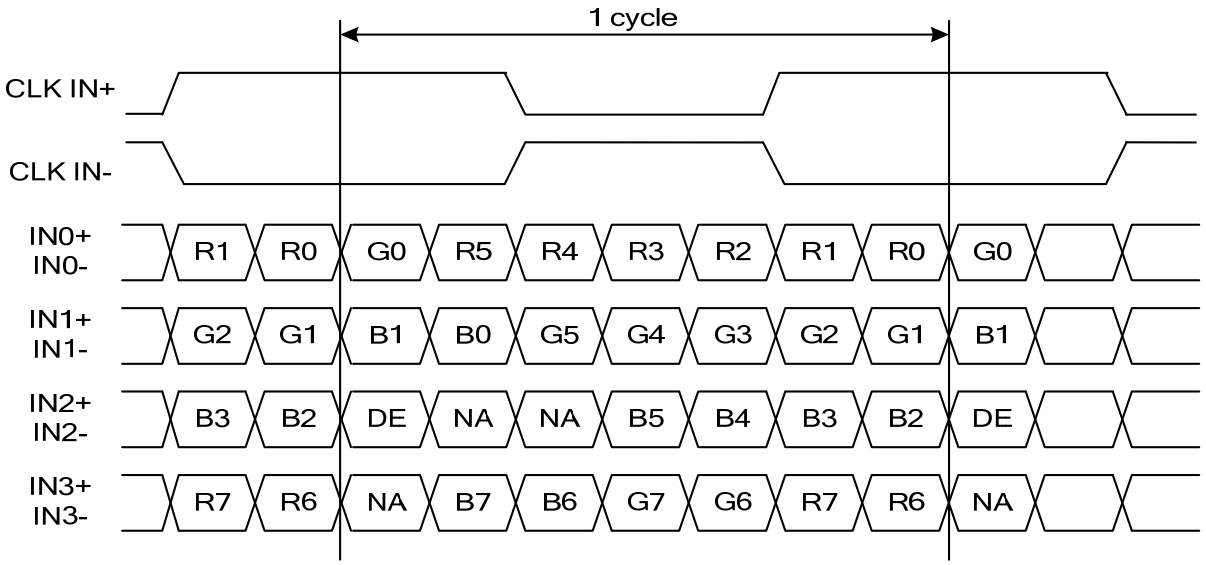
Note 3: Receiver : with built-in TCON IC.

9.3 DATA MAPPING

1) 8 Bit Mode

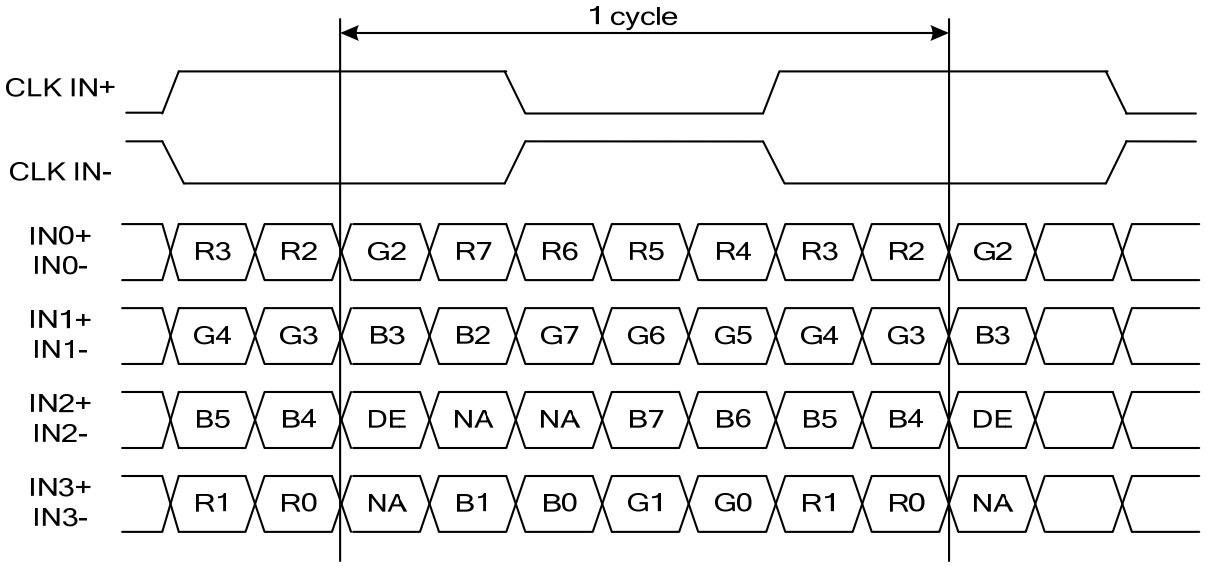
Transmitter		AMODE	
Pin No.	Data	=Low	=High
51	TA0	R0 (LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7 (MSB)
4	TA6	G0 (SLB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7 (MSB)
15	TB5	B0 (LSB)	B2
19	TB6	B1	B3
20	TC0	B2	B4
22	TC1	B3	B5
23	TC2	B4	B6
24	TC3	B5	B7 (MSB)
27	TC4	(NA)	(NA)
28	TC5	(NA)	(NA)
30	TC6	DE	DE
50	TD0	R6	R0 (LSB)
2	TD1	R7 (MSB)	R1
8	TD2	G6	G0 (LSB)
10	TD3	G7 (MSB)	G1
16	TD4	B6	B0 (LSB)
18	TD5	B7 (MSB)	B1
25	TD6	(NA)	(NA)

<AMODE=Low>



DE : Display Enable
NA : Not Availble

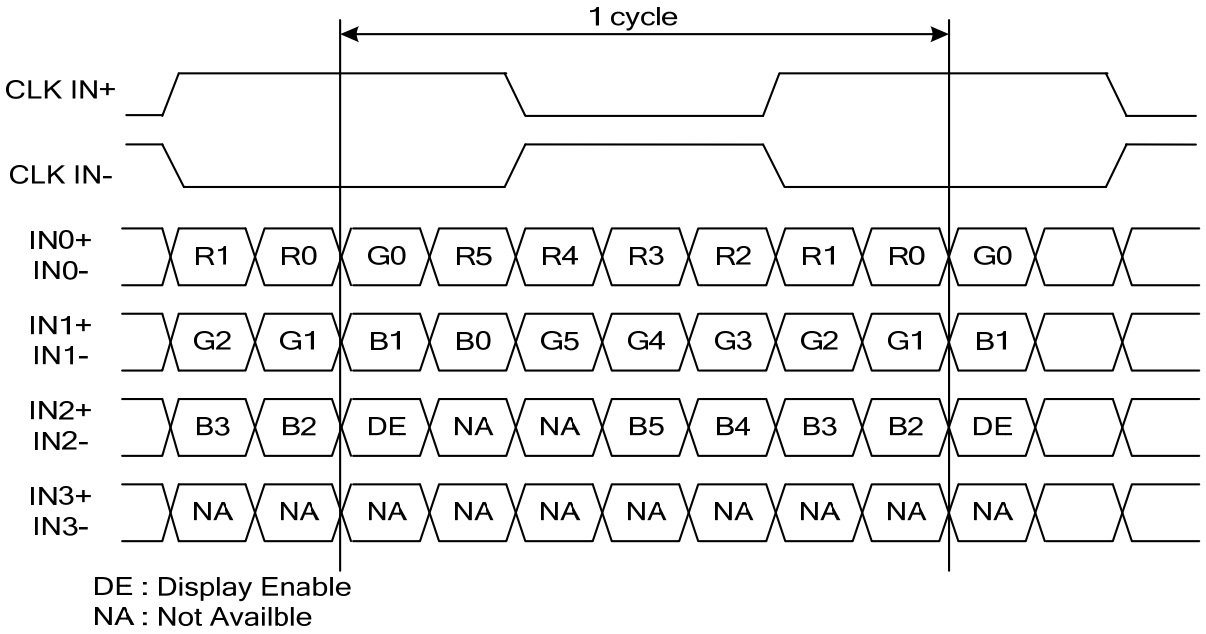
<AMODE=High>



DE : Display Enable
NA : Not Availble

2) 6 Bit Mode

Transmitter		AMODE	
Pin No.	Data	=Low	=High
51	TA0	R0 (LSB)	R0 (LSB)
52	TA1	R1	R1
54	TA2	R2	R2
55	TA3	R3	R3
56	TA4	R4	R4
3	TA5	R5 (MSB)	R5 (MSB)
4	TA6	G0 (SLB)	G0 (LSB)
6	TB0	G1	G1
7	TB1	G2	G2
11	TB2	G3	G3
12	TB3	G4	G4
14	TB4	G5 (MSB)	G5 (MSB)
15	TB5	B0 (LSB)	B0 (LSB)
19	TB6	B1	B1
20	TC0	B2	B2
22	TC1	B3	B3
23	TC2	B4	B4
24	TC3	B5 (MSB)	B5 (MSB)
27	TC4	(NA)	(NA)
28	TC5	(NA)	(NA)
30	TC6	DE	DE
50	TD0	GND	GND
2	TD1	GND	GND
8	TD2	GND	GND
10	TD3	GND	GND
16	TD4	GND	GND
18	TD5	GND	GND
25	TD6	(NA)	(NA)



DE : Display Enable
NA : Not Availble

9.4 DATA INPUT for DISPLAY COLOR(8BIT MODE)

Input color		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

(6BIT MODE)

Input		Red Data						Green Data						Blue Data					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
color		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

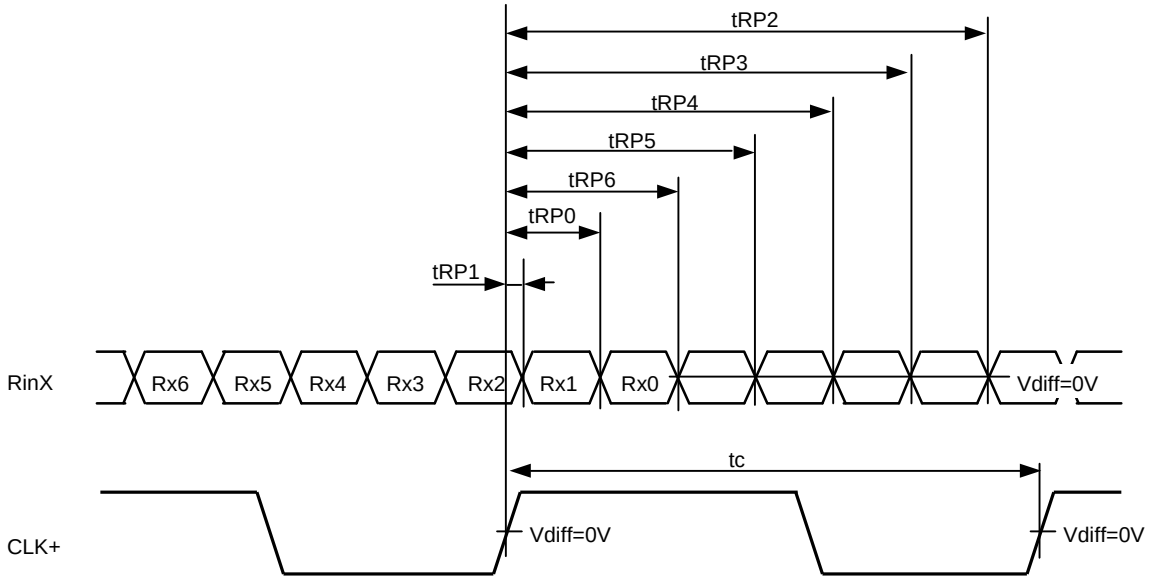
Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

9.5 INTERFACE TIMING

(1) LVDS Receiver Timing

(Interface of TFT module)

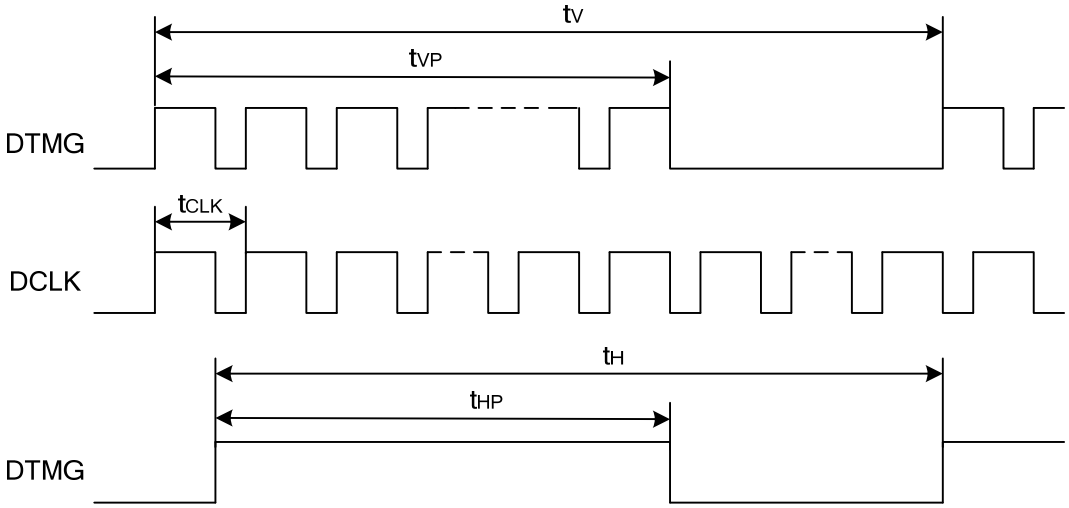


RinX=(RinX+)-(RinX-) (X=0,1,2)

Item		Symbol	Min.	Typ.	Max.	Unit	Note
DCLK	FREQUENCY	1/tCLK	37	40	43	MHz	
RinX (X=0,1,2)	0 data position	tRP0	1/7*tCLK –0.4	1/7*tCLK	1/7*tCLK +0.4	ns	
	1st data position	tRP1	-0.4	0	+0.4		
	2nd data position	tRP2	6/7*tCLK –0.4	6/7*tCLK	6/7*tCLK +0.4		
	3rd data position	tRP3	5/7*tCLK –0.4	5/7*tCLK	5/7*tCLK +0.4		
	4th data position	tRP4	4/7*tCLK –0.4	4/7*tCLK	4/7*tCLK +0.4		
	5th data position	tRP5	3/7*tCLK –0.4	3/7*tCLK	3/7*tCLK +0.4		
	6th data position	tRP6	2/7*tCLK –0.4	2/7*tCLK	2/7*tCLK +0.4		

(2) Timing converter timing

(Input timing for transmitter)



The timings except mentiond above are referd to the specifications of your transmitter.

Item		Symbol	Min.	Typ.	Max.	Unit
DCLK	Cycle time	t_{CLK}	23.3	25.0	27.0	ns
DTMG	Horizontal Cycle	t_H	850	1060	1260	t_{CLK}
	Horizontal Valid Data width	t_{HD}	800	800	800	
	Vertical Cycle	tv	603	628	728	t_H
	Vertical Valid Data width	t_{VD}	600	600	600	

Note 1: It counts by a typical value of line cycle time.

(3) POWER SEQUENCE

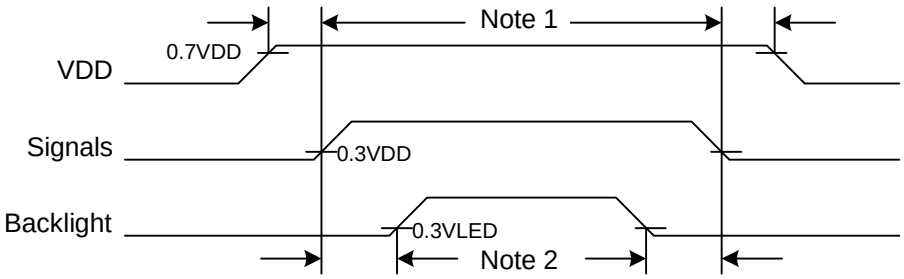


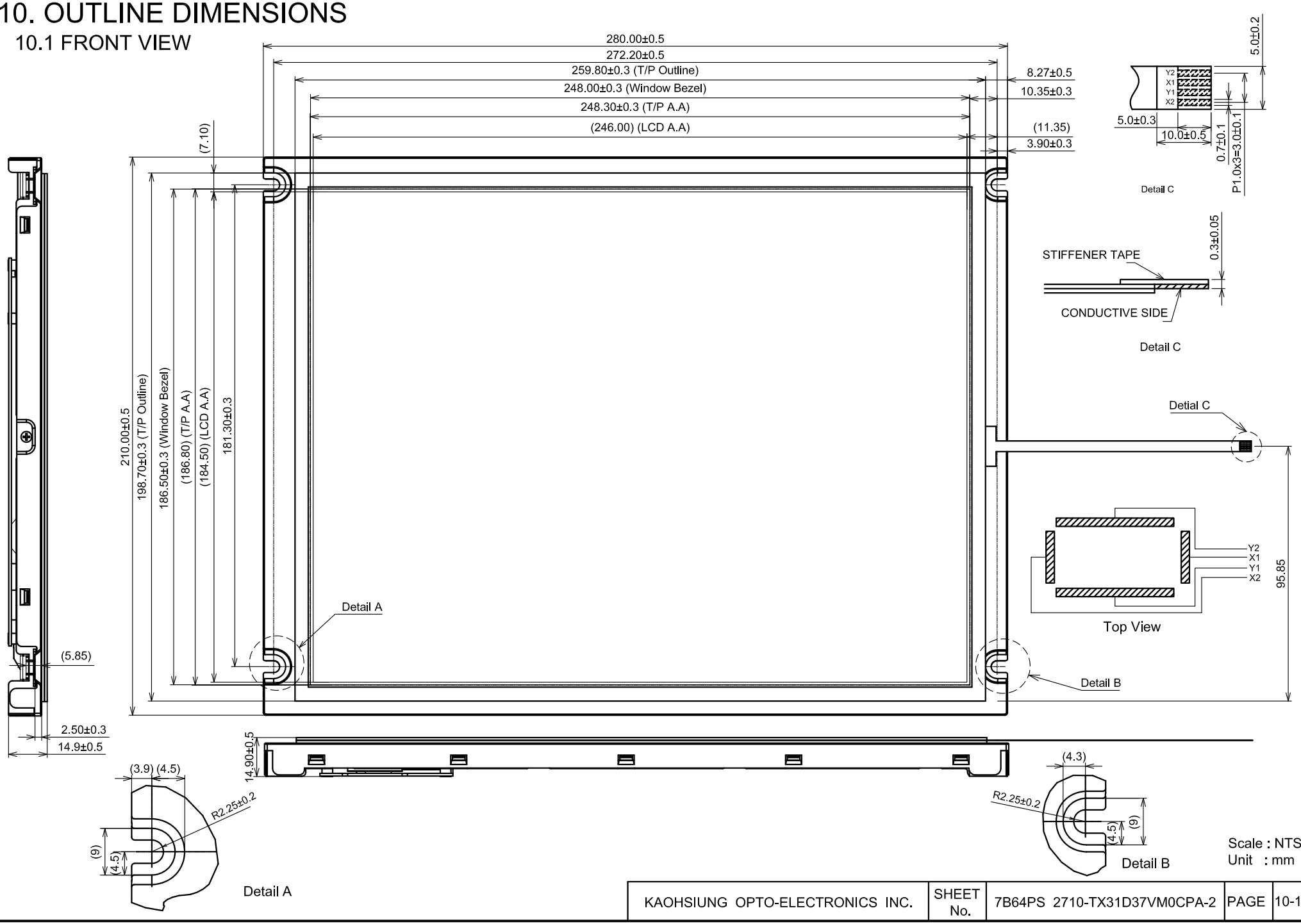
Fig. 8.7 Power Sequence Timing

Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is 1 second.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

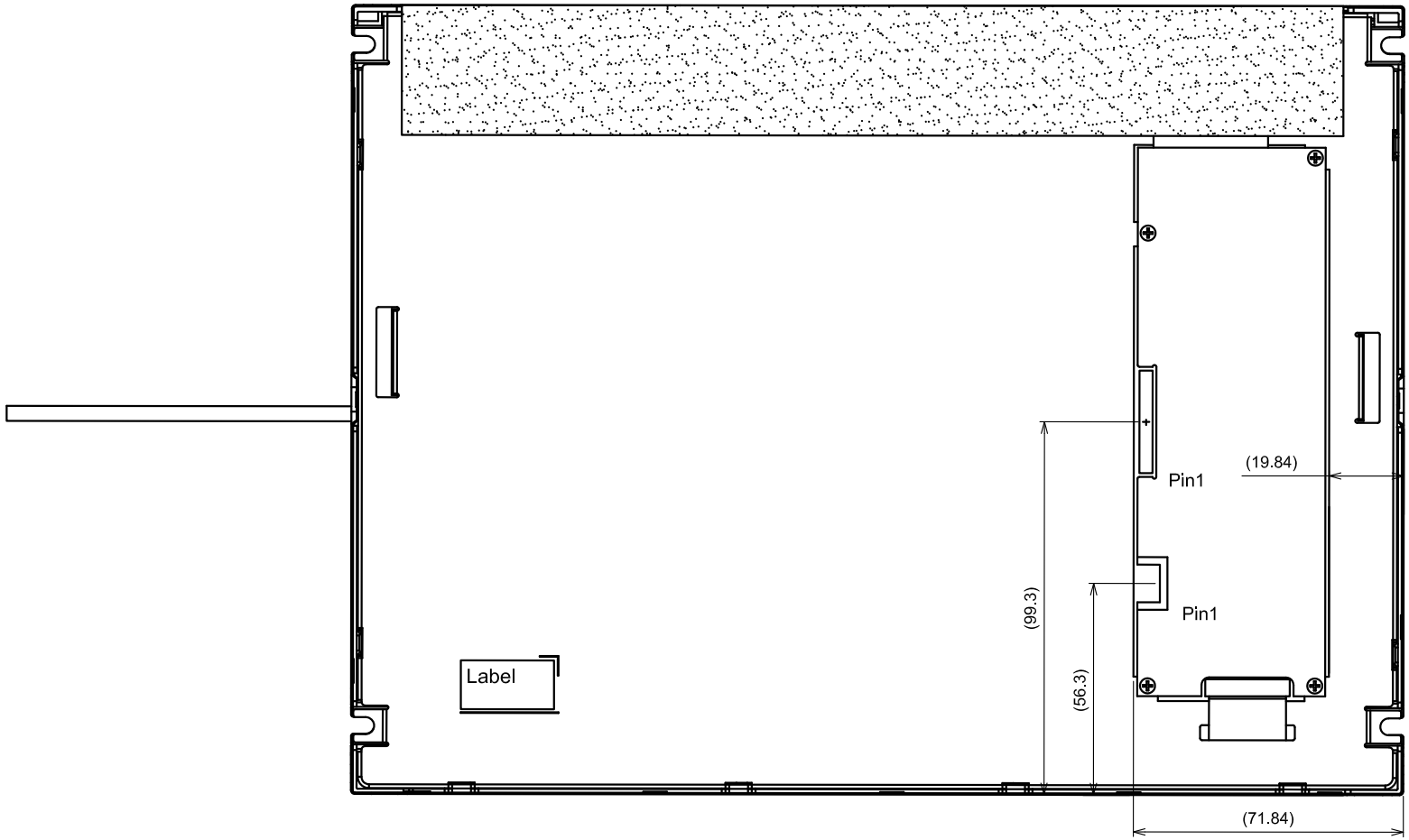
10. OUTLINE DIMENSIONS

10.1 FRONT VIEW



KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET No.	7B64PS 2710-TX31D37VM0CPA-2	PAGE	10-1/2

10.2 REAR VIEW



Scale : NTS
Unit : mm

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET No.	7B64PS 2710-TX31D37VM0CPA-2	PAGE	10-2/2

11. TOUCH PANEL

The type of touch panel used on this display is resistive, analog, 4-wire and film on glass, and more characteristics are shown as below:

11.1 OPERATING CONDITIONS

Item	Specification	Remarks
Operating Voltage	5VDC	-

11.2 ELECTRICAL CHARACTERISTICS

Item		Specification	Remarks
Resistance Between Terminal	X1-X2	340~1020Ω	-
	Y1-Y2	270~550Ω	
Insulation Resistance	X-Y	20MΩmin.	At 25V DC
Linearity	X	±1.5% max.	Note 1
	Y	±1.5% max.	
Chattering		10ms max.	-

Note 1: The test conditions and equipments of linearity are as below:

- Material of pen: poly-acetal resin
- End shape: R 0.8 mm
- Test force: 150 g
- Pitch: 10 mm
- Test area is shown in Fig. 11.1

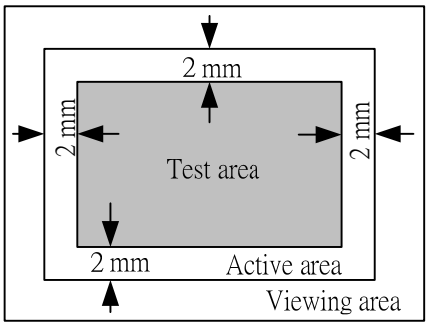


Fig. 11.1

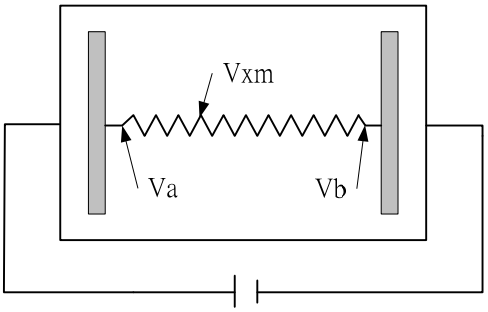


Fig. 11.2

As shown in Fig. 11.2, applying voltage meter to measure Va, Vb and Vxm, where Va is the maximum voltage in the active area; Vb is the minimum voltage in the active area; Vxm is the measured voltage of point x selected by random. Afterwards, the linearity can be calculated by following equation:

$$Linearity = \frac{|Vxi - Vxm|}{Va - Vb} \times 100\%,$$

where Vxi is the idea voltage of point x.

The method to measure the linearity of Y-axis is the same as above.

11.3 MECHANICAL CHARACTERISTICS

Item	Specification	Remarks
Pen Input Pressure	20~80g	R0.8, Polyacetal Pen
Finger	20~80g	R8.0, Silicon Rubber
Surface Hardness	3H min.	JIS K 5400

11.4 OPTICAL CHARACTERISTICS

Item	Specification	Remarks
Transmittance	77% min.	-

11.5 SAFETY AND ATTENTIONS

1) Do not put heavy shock or stress on the touch panel.

2) Please use soft cloth or absorbent cotton with ethanol to clean the touch panel by gently wiping. Moreover, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the touch panel's surface.

3) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean the display's surface.

4) UV protection is recommended to avoid the possibility of performance degrading when touch panel is likely applied under UV environment for a long period of time.

12. APPEARANCE STANDARD

The appearance inspection is performed in a dark room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 12.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

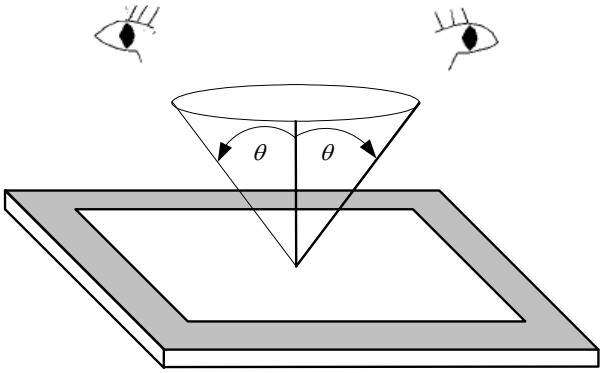


Fig 12.1

12.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 3 areas as shown in Fig.12.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area, which extended 1 mm out from LCD active area; C zone is the area between B zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

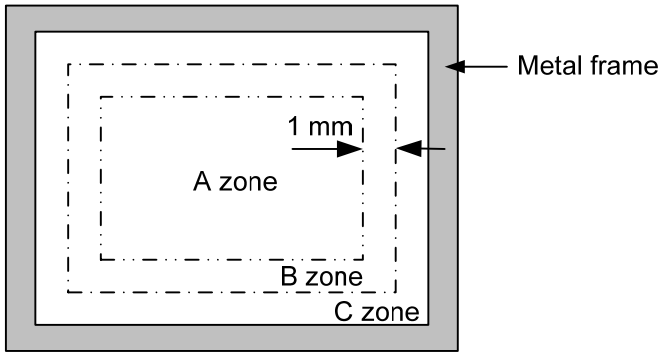


Fig 12.2

12.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 12.3 and Fig. 12.4.

<table>
<tr><th>Item</th><th colspan="4">Criteria</th><th>Applied zone</th></tr>
<tr><td rowspan="8">Scratches</td><td>Length (mm)</td><td>Width (mm)</td><td>Maximum number</td><td>Minimum space</td><td rowspan="8">A,B</td></tr>
<tr><td>Ignored</td><td>W≦0.01</td><td>Ignored</td><td>-</td></tr>
<tr><td>L≦40</td><td>W≦0.02</td><td>10</td><td>-</td></tr>
<tr><td>L≦20</td><td>W≦0.04</td><td>10</td><td>-</td></tr>
<tr><td colspan="4">Round (Dot Shape)</td></tr>
<tr><td>Average diameter (mm)</td><td colspan="2">Maximum number</td><td>Minimum space</td></tr>
<tr><td>D≦0.2</td><td colspan="2">Ignore</td><td>-</td></tr>
<tr><td>D≦0.4</td><td colspan="2">10</td><td>-</td></tr>
<tr><td>Dent</td><td colspan="4">Serious one is not allowed</td><td>A</td></tr>
<tr><td>Wrinkles in polarizer</td><td colspan="4">Serious one is not allowed</td><td>A</td></tr>
<tr><td rowspan="4">Bubbles on polarizer</td><td colspan="2">Average diameter (mm)</td><td colspan="2">Maximum number</td><td rowspan="4">A</td></tr>
<tr><td colspan="2">D≦0.3</td><td colspan="2">Ignored</td></tr>
<tr><td colspan="2">0.3＜D≦0.5</td><td colspan="2">10</td></tr>
<tr><td colspan="2">0.5＜D≦1.0</td><td colspan="2">5</td></tr>
<tr><td rowspan="15">1) Stains
2) Foreign Materials
3) Dark Spot</td><td colspan="4">Filamentous (Line shape)</td><td rowspan="3">A,B</td></tr>
<tr><td>Length (mm)</td><td rowspan="2">Width (mm)</td><td rowspan="2">Maximum number</td></tr>
<tr><td>Ignored</td></tr>
<tr><td>L≦1.0</td><td rowspan="2">0.06＜W</td><td>Ignored</td><td rowspan="2">A,B</td></tr>
<tr><td>1.0≦L</td><td>Dot Shape</td></tr>
<tr><td colspan="4">Round (Dot shape)</td><td rowspan="7">A,B</td></tr>
<tr><td>Average diameter (mm)</td><td>Maximum number</td><td>Minimum Space</td></tr>
<tr><td>D≦0.45</td><td>Ignored</td><td>-</td></tr>
<tr><td>0.45＜D≦0.7</td><td>5</td><td>-</td></tr>
<tr><td>0.7＜D</td><td>None</td><td>-</td></tr>
<tr><td>In total</td><td colspan="2">Filamentous + Round=10</td></tr>
<tr><td colspan="3">Those wiped out easily are acceptable</td></tr>
<tr><td rowspan="10">Dot-Defect
(Note 1)</td><td></td><td>Type</td><td>Maximum number</td><td rowspan="10">A</td></tr>
<tr><td rowspan="4">Bright dot-defect</td><td>1 dot</td><td>4</td></tr>
<tr><td>2 adjacent dot</td><td>1</td></tr>
<tr><td>3 adjacent dot or above</td><td>Not allowed</td></tr>
<tr><td>In total</td><td>5</td></tr>
<tr><td rowspan="4">Dark dot-defect</td><td>1 dot</td><td>5</td></tr>
<tr><td>2 adjacent dot</td><td>2</td></tr>
<tr><td>3 adjacent dot or above</td><td>Not allowed</td></tr>
<tr><td>In total</td><td>5</td></tr>
<tr><td colspan="2">In total</td><td>10</td></tr>
</table>

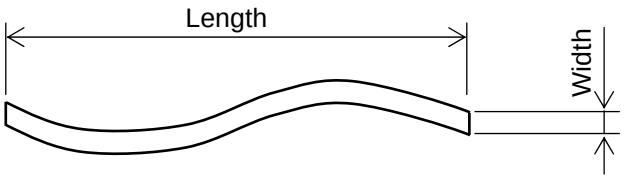
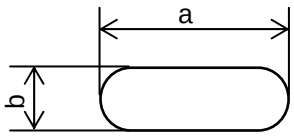


Fig 12.3



Average diameter = $\frac{a+b}{2}$

Fig 12.4

Note 1: The definitions of dot defect are as below:

- The defect area of the dot must be bigger than half of a dot.
- For bright dot-defect, showing black pattern, the dot's brightness must be over 30% brighter than others.
- For dark dot-defect, showing white pattern, the dot's brightness must be under 70% darker than others.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 12.5.
- The Density of dot defect is defined in the area within diameter ϕ =20mm.

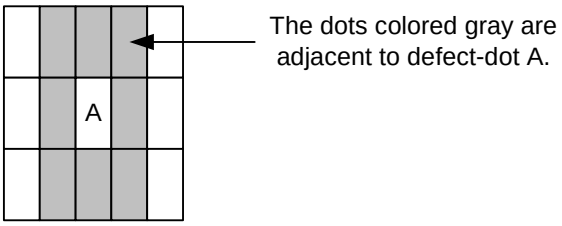


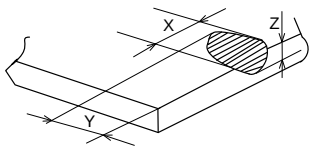
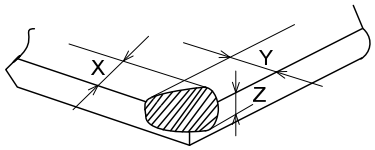
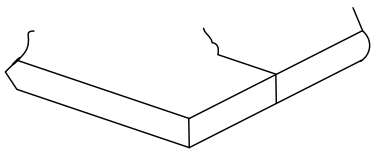
Fig 12.5

12.3 TOUCH PANEL APPEARANCE SPECIFICATION

The specification as below is defined by the amount of unexpected material in different zones of touch panel.

Item	Criteria			Applied zone
Scratches	Width (mm)	Length (mm)	Maximum number	A
	W≧0.1	L≧10	Not allowed	
	0.10＞W≧0.05	10＞L	4 pcs max.	
	0.05＞W	10＞L	Ignored	
Foreign Materials	Filamentous (Line shape)			A
	Width (mm)	Length (mm)	Maximum number	
	W＞0.05	3＜L	Not allowed	
	0.05≧W	3≧L	Ignored	
	Round (Dot shape)			A
	Average diameter (mm)		Maximum number	
	D＞0.3		Not allowed	
	0.3≧D＞0.2		3 pcs max.	
	0.2≧D＞0.1		5 pcs max.	
	D≦0.1		Ignored	

The limitation of glass flaw occurred on touch panel is defined in the table as below.

Item	Specifications	
Edge flaw		X ≤ 5.0 mm Y ≤ 1.0 mm Z ≤ Thickness
Corner flaw		X ≤ 3.0 mm Y ≤ 3.0 mm Z ≤ Thickness
Progressive flaw		Not allowed

13. PRECAUTIONS

13.1 PRECAUTIONS of TOUCH PANEL

1) Please refer to Fig. 13.1 for housing the display with touch panel into applications. The Fig. 13.1 shows some points as below:

- The cushion needs to be designed between housing and touch panel in order to avoid unexpected pressure to cause any wrong reactions, and the cushion should be located in the cushion area.

- The housing should not cover the active area of touch panel as the figure shown.

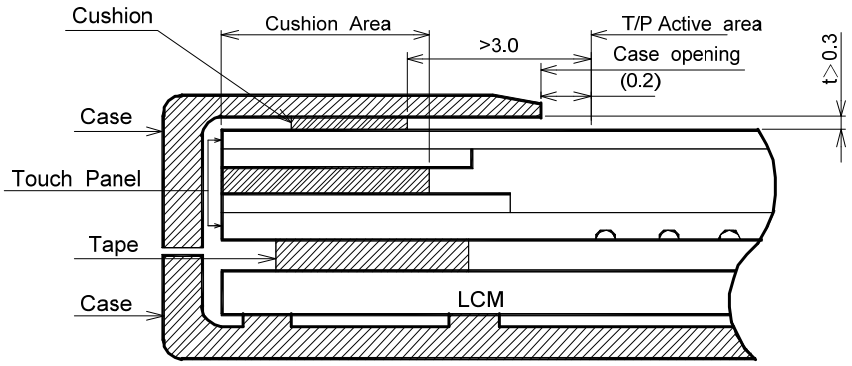


Fig. 13.1

13.2 PRECAUTIONS OF ESD

1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.

2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

13.3 PRECAUTIONS OF HANDLING

1) In order to keep the appearance of display in good condition, please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.

2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.

3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.

4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.

5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.

6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.

7) Maximum pressure to the surface of the display must be less than 1.96×10^4 Pa. If the area of adding pressure is less than 1 cm^2, the maximum pressure must be less than 1.96N.

13.4 PRECAUTIONS OF OPERATING

1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.

2) When the display is operating at significant low temperature, the response time will be slower than it at 25℃. In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.

3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.

4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than ±100 mV.

13.5 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.

2) The recommended long term storage temperature is between 10℃~35℃ and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.

3) It would be better to keep the displays in the container, which is shipped from KOE, and do not unpack it.

4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

14. DESIGNATION OF LOT MARK

1) The lot mark is showing in Fig.14.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

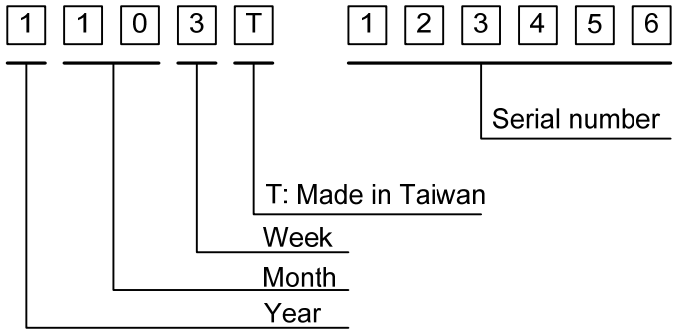


Fig. 14.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2012	2
2013	3
2014	4
2015	5
2016	6

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Lot Mark
1~7 days	1
8~14 days	2
15~21 days	3
22~28 days	4
29~31 days	5

3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.

4) The location of the lot mark is on the back of the display shown in Fig. 14.2.

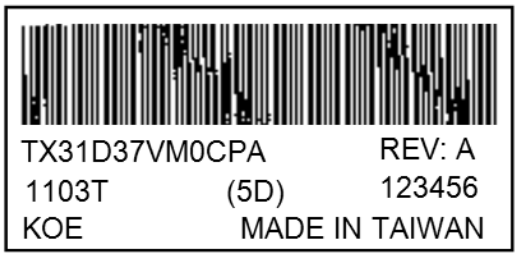


Fig. 14.2