

KOE

JDI Group

Kaohsiung Opto-Electronics Inc.

FOR MESSRS : _____

DATE : May 1st, 2012


CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX31D38VM2BAA

Contents

| No. | ITEM | SHEET No. | PAGE |
|-----|----------------------------|------------------------------|------------|
| 1 | COVER | 7B64PS 2701- TX31D38VM2BAA-2 | 1-1/1 |
| 2 | RECORD OF REVISION | 7B64PS 2702- TX31D38VM2BAA-2 | 2-1/1 |
| 3 | GENERAL DATA | 7B64PS 2703- TX31D38VM2BAA-2 | 3-1/1 |
| 4 | ABSOLUTE MAXIMUM RATINGS | 7B64PS 2704- TX31D38VM2BAA-2 | 4-1/1 |
| 5 | ELECTRICAL CHARACTERISTICS | 7B64PS 2705- TX31D38VM2BAA-2 | 5-1/2~2/2 |
| 6 | OPTICAL CHARACTERISTICS | 7B64PS 2706- TX31D38VM2BAA-2 | 6-1/2~2/2 |
| 7 | BLOCK DIAGRAM | 7B64PS 2707- TX31D38VM2BAA-2 | 7-1/1 |
| 8 | RELIABILITY TESTS | 7B64PS 2708- TX31D38VM2BAA-2 | 8-1/1 |
| 9 | LCD INTERFACE | 7B64PS 2709- TX31D38VM2BAA-2 | 9-1/7~7/7 |
| 10 | OUTLINE DIMENSIONS | 7B64PS 2710- TX31D38VM2BAA-2 | 10-1/2~2/2 |
| 11 | APPEARANCE STANDARD | 7B64PS 2711- TX31D38VM2BAA-2 | 11-1/3~3/3 |
| 12 | PRECAUTIONS | 7B64PS 2712- TX31D38VM2BAA-2 | 12-1/2~2/2 |
| 13 | DESIGNATION OF LOT MARK | 7B64PS 2713- TX31D38VM2BAA-2 | 13-1/1 |

ACCEPTED BY: _____

PROPOSED BY: 

2. RECORD OF REVISION

| DATE | SHEET No. | SUMMARY |
|------------|-----------|---|
| May 01,'12 | All pages | Company name changed: KAOHSIUNG HITACHI ELECTRONICS CO.,LTD. ↓ KAOHSIUNG OPTO-ELECTRONICS INC. |
| | | |

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 12.3" HSXGA of 8:3 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R (red), G (green), B (blue) sequentially. This display is RoHS compliant, COG (chip on glass) technology and LED backlight are applied on this display.

| | |
|-------------------------|--|
| Part Name | TX31D38VM2BAA |
| Module Dimensions | 320(W) mm x 130(H) mm x (12.8)(D) mm typ. |
| LCD Active Area | 293.76(W) mm x 110.16(H) mm |
| Pixel Pitch | 0.2295(W) mm x 0.2295(H) mm |
| Resolution | 1280 x 3(RGB)(W) x 480(H) dots |
| Color Pixel Arrangement | R, G, B Vertical stripe |
| LCD Type | Transmissive Color TFT; Normally White |
| Display Type | Active Matrix |
| Number of Colors | 262k Colors |
| Backlight | LED (Lifetime: 70 Khrs) |
| Weight | 586 g |
| Interface | LVDS; 20 pins |
| Power Supply Voltage | 3.3V for LCD; 12V for Backlight |
| Power Consumption | 1.848 W for LCD; 10.08 W for backlight |
| Viewing Direction | 12 o'clock (without image inversion and least brightness change) 6 o'clock (contrast peak located at) |

4. ABSOLUTE MAXIMUM RATINGS

| Item | Symbol | Min. | Max. | Unit | Remarks |
|-------------------------|--------|------|---------|------|---------|
| Supply Voltage | VDD | 0 | 4.0 | V | - |
| Input Voltage of Logic | VI | -0.3 | VDD+0.3 | V | Note 1 |
| Operating Temperature | Top | -30 | 80 | °C | Note 2 |
| Storage Temperature | Tst | -40 | 90 | °C | Note 2 |
| Backlight Input Voltage | VLED | - | 15 | V | Note 3 |

Note 1: It shall be applied to pixel data signal, clock signal and control Pin.

Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25°C.
- Operating under high temperature will shorten LED lifetime.

Note 3: Do not operate at or near the maximum rating listed for extended periods of time. Exposure to such conditions may adversely impact product reliability and result in failures not covered by warranty.

5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

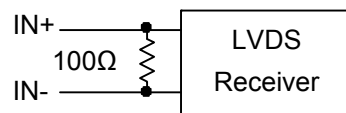
$T_a = 25\text{ }^\circ\text{C}$, $V_{SS} = 0\text{V}$

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit | Remarks |
|--|-----------|---------------|------|------|------|------|----------|
| Power Supply Voltage | VDD | - | 3.0 | 3.3 | 3.6 | V | - |
| Differential Input Voltage for LVDS Receiver Threshold | VI | VIH | - | - | +100 | mV | Note 1 |
| | | VIL | -100 | - | - | | |
| Power Supply Current | IDD | VDD-VSS =3.3V | - | 560 | 680 | mA | Note 2,3 |
| Vsync Frequency | f_v | - | - | 60 | 66 | Hz | Note 4 |
| Hsync Frequency | f_H | - | - | 31.8 | 33.3 | KHz | |
| DCLK Frequency | f_{CLK} | - | - | 43.2 | 48 | MHz | |

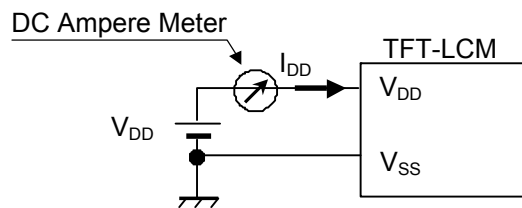
Note 1: $V_{CM} = +1.2\text{V}$

VCM is common mode voltage of LVDS transmitter/receiver.

The input terminal of LVDS transmitter is terminated with 100Ω .



Note 2: An all black check pattern is used when measuring I_{DD} , f_v is set to 60Hz.



Note 3: 1.0A fuse is applied in the module for I_{DD} . For display activation and protection purpose, power supply is recommended larger than 2.5A to start the display and break fuse once any short circuit occurred.

Note 4: For LVDS transmitter input.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25^\circ C$

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit | Remarks |
|--------------------------------------|--------|-------------------|------|------|------|------|---------|
| LED Input Voltage | VLED | - | 11.5 | 12 | 12.5 | V | Note1 |
| LED Forward Current (Dim Control) | ILED | 0V; 0% duty | - | 840 | 890 | mA | Note 2 |
| | | 3.3VDC; 100% duty | - | 70 | 120 | | |
| LED lifetime | - | 840 mA | - | 70K | - | hrs | Note 3 |

Note 1: As Fig. 5.1 shown, LED current is constant, 840 mA, controlled by the LED driver when applying 12V VLED.

Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1K ~ 10K Hz with 3.3V amplitude.

Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 840 mA at 25°C.

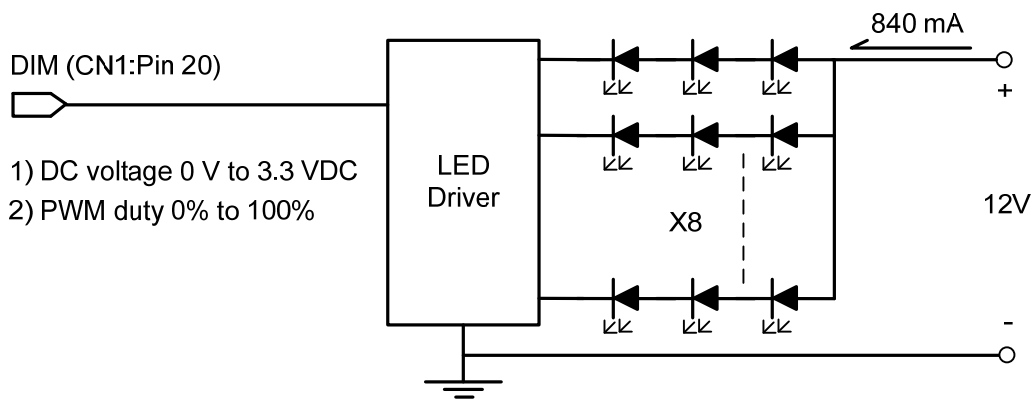


Fig 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25 °C .
- In the dark room around 500~1000 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$$T_a = 25 \text{ }^\circ\text{C}, f_v = 60 \text{ Hz}, VDD = 3.3\text{V}$$

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit | Remarks | |
|-------------------------------------|---------------|--|------------------------------------|------|------|-------------------|---------|--------|
| Brightness of White | - | $\phi = 0^\circ, \theta = 0^\circ$, ILED= 840 mA | 800 | 1000 | - | cd/m ² | Note 1 | |
| Brightness Uniformity | - | | 70 | - | - | % | Note 2 | |
| Contrast Ratio | CR | | 400 | 800 | - | - | Note 3 | |
| Response Time (Rising + Falling) | $T_r + T_f$ | $\phi = 0^\circ, \theta = 0^\circ$ | - | 25 | - | ms | Note 4 | |
| NTSC Ratio | - | $\phi = 0^\circ, \theta = 0^\circ$ | - | 60 | - | % | - | |
| Viewing Angle | θ_x | $\phi = 0^\circ, CR \geq 10$ | 60 | 80 | - | Degree | Note 5 | |
| | $\theta_{x'}$ | $\phi = 180^\circ, CR \geq 10$ | 60 | 80 | - | | | |
| | θ_y | $\phi = 90^\circ, CR \geq 10$ | 40 | 60 | - | | | |
| | $\theta_{y'}$ | $\phi = 270^\circ, CR \geq 10$ | 60 | 80 | - | | | |
| Color Chromaticity | Red | X | $\phi = 0^\circ, \theta = 0^\circ$ | 0.55 | 0.60 | 0.65 | - | Note 6 |
| | | Y | | 0.31 | 0.36 | 0.41 | | |
| | Green | X | | 0.31 | 0.36 | 0.41 | | |
| | | Y | | 0.55 | 0.60 | 0.65 | | |
| | Blue | X | | 0.10 | 0.15 | 0.20 | | |
| | | Y | | 0.05 | 0.10 | 0.15 | | |
| | White | X | | 0.27 | 0.32 | 0.37 | | |
| | | Y | | 0.30 | 0.35 | 0.40 | | |

Note 1: The brightness is measured from center point of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \times 100\%$$

, which is based on the brightness values of the 9 points measured by BM-7 as shown in Fig. 6.2.

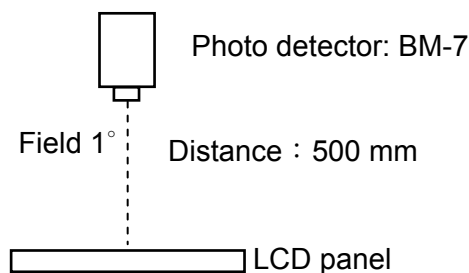


Fig. 6.1

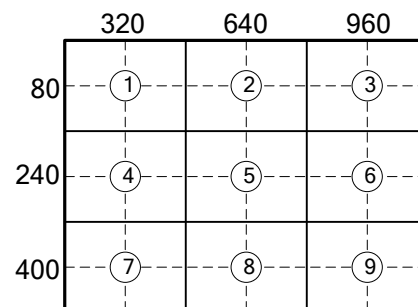


Fig. 6.2

Note 3: The contrast ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 90% brightness to 10% brightness when the data is from white to black. Oppositely, Falling time is the period from 10% brightness rising to 90% brightness.

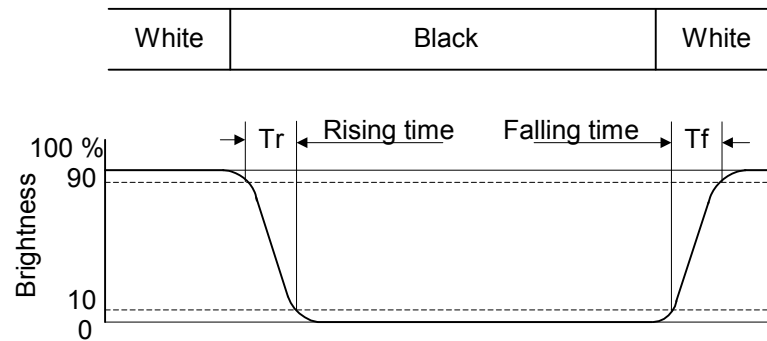


Fig . 6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^\circ$ means 6 o'clock, and $\phi = 0^\circ$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The viewing direction of this display is 12 o'clock, which means that a photograph with gray scale would not be reversed in color and the brightness change would be less from this direction. However, the best contrast peak would be located at 6 o'clock.

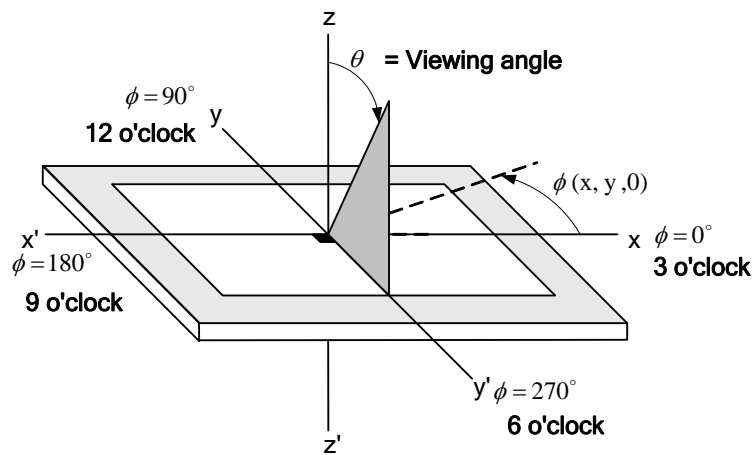
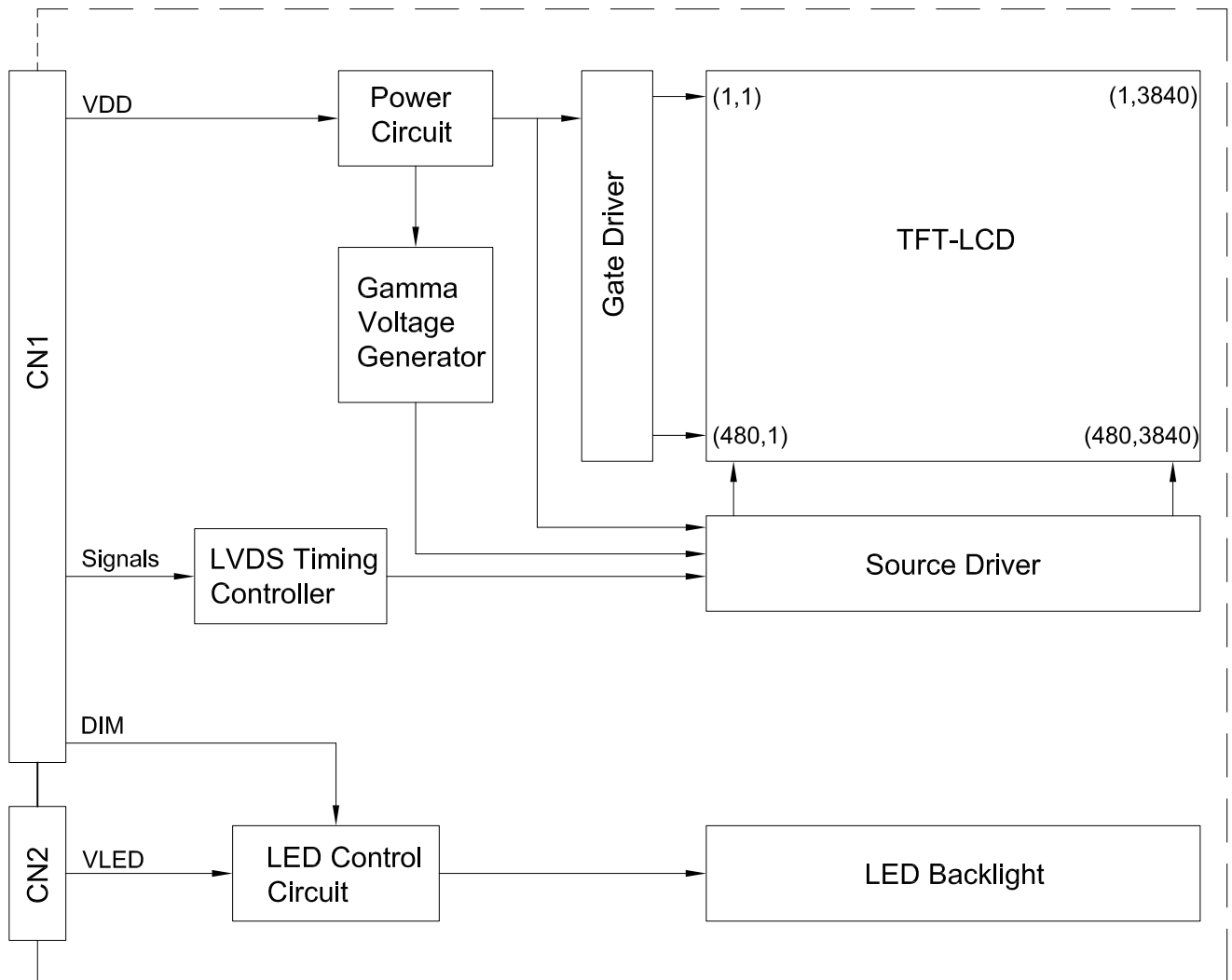


Fig. 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7 .BLOCK DIAGRAM



Note: Signals are CLK, and pixel data pairs.

8. RELIABILITY TESTS

| Test Item | Condition | |
|-----------------------------|---|---|
| High Temperature | 1) Operating 2) 80 °C | 500 hrs |
| Low Temperature | 1) Operating 2) -30 °C | 500 hrs |
| High Temperature | 1) Storage 2) 90 °C | 500 hrs |
| Low Temperature | 1) Storage 2) -40 °C | 500 hrs |
| Heat Cycle | 1) Operating 2) -30 °C ↔ 80 °C 3) 3hrs~1hr~3hrs | 500 hrs |
| Thermal Shock | 1) Non-Operating 2) -35 °C ↔ 85 °C 3) 0.5 hr ↔ 0.5 hr | 500 hrs |
| High Temperature & Humidity | 1) Operating 2) 40 °C & 85%RH 3) Without condensation (Note4) | 500 hrs |
| Vibration | 1) Non-Operating 2) 10~200 Hz 3) 5G 4) X, Y, and Z directions | 1 hr for each direction |
| Mechanical Shock | 1) Non-Operating 2) 10 ms 3) 80G 4) ±X, ±Y and ±Z directions | Once for each direction |
| ESD | 1) Operating 2) Tip: 150 pF, 330 Ω 3) Air discharge for glass: ± 12KV 4) Contact discharge for metal frame: ± 15KV | 1) Glass: 9 points 2) Metal frame: 8 points (Note3) |

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

Note 2: The display is not guaranteed for use in corrosive gas environments.

Note 3: All pins of LCD interface(CN1) have been tested by ± 100V contact discharge of ESD under non-operating condition.

Note 4: Under the condition of high temperature & humidity, if the temperature is higher than 40°C, the humidity needs to be reduced as Fig. 8.1 shown.

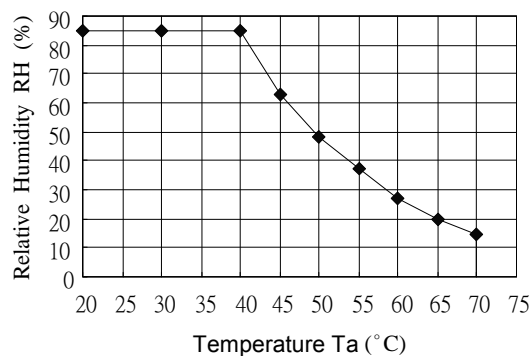


Fig. 8.1

9. LCD INTERFACE

9.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN1) is FI-SEB20P-HF13E made by JAE and pin assignment is as below:

| Pin No. | Symbol | Signal |
|---------|---------|--|
| 1 | VDD | Power Supply for Logic |
| 2 | VDD | |
| 3 | VSS | GND |
| 4 | VSS | |
| 5 | IN0- | R0~R5, G0 |
| 6 | IN0+ | |
| 7 | VSS | GND |
| 8 | IN1- | G1~G5, B0~B1 |
| 9 | IN1+ | |
| 10 | VSS | GND |
| 11 | IN2- | B2~B5, DE |
| 12 | IN2+ | |
| 13 | VSS | GND |
| 14 | CLK IN- | Pixel Clock |
| 15 | CLK IN+ | |
| 16 | VSS | GND |
| 17 | NC | No Connection |
| 18 | NC | |
| 19 | VSS | GND |
| 20 | DIM | Normal Brightness: 0V or 0% PWM Duty Brightness Control: 0V to 3.3 VDC or 0% to 100% PWM Duty |

Note 1: IN n- and IN n+ (n=0, 1, 2), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.

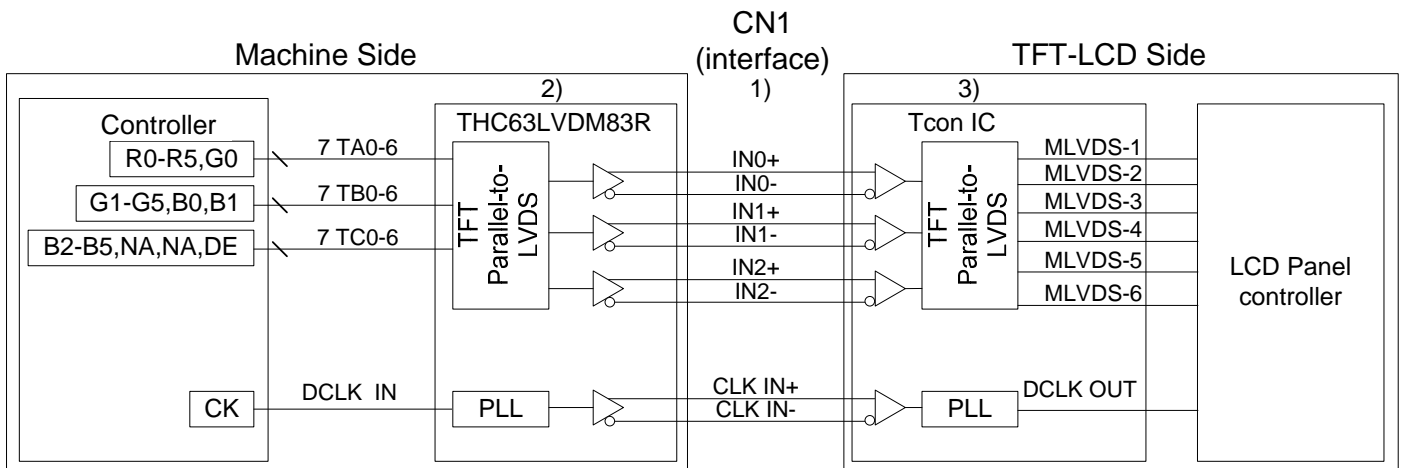
Note 2: All VSS pins should be connected to GND(0V), Metal bezel is connected internally to VSS.

Note 3: Normal brightness: 0V or 0% PWM duty; Brightness Control: 0V to 3.3V DC or 0% to 100% PWN duty.

The backlight connector (CN2) is SM02(8.0)B-BHS-1-TB (LF)(SN) made by JST, and pin assignment is as below:

| Pin No. | Symbol | Signal |
|---------|--------|--------|
| 1 | VLED | 12VDC |
| 2 | GND | Ground |

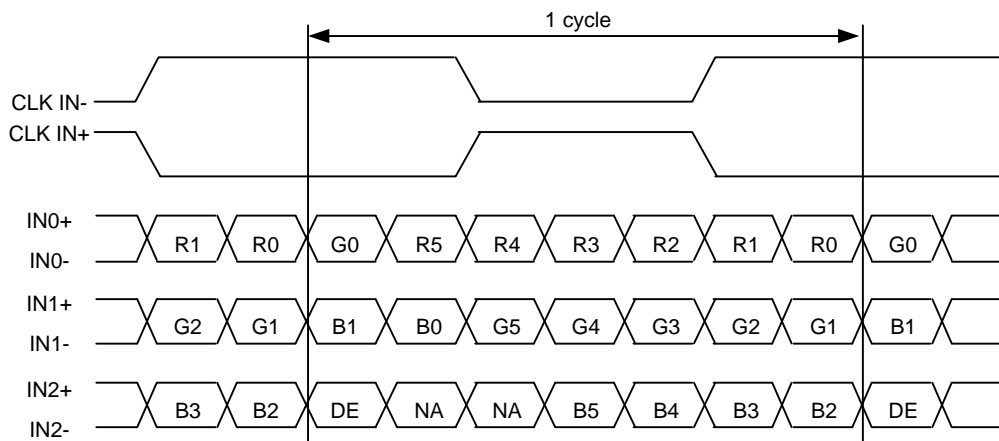
9.2 LVDS INTERFACE



Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.

Note 2: The recommended transmitter, THC63LVDM83R, is made by Thine or equivalent, which is not contained in the module.

9.3 LVDS DATA FORMAT



DE: Display Enable

NA: Not Available

9.4 INTERFACE TIMING SPECIFICATIONS

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (Vsync) = 60 Hz to define. If 60 Hz is not the aim to set, less than 66 Hz for Vsync is recommended to apply for better performance by other parameter combination as the definitions in section 5.1.

| Item | | Symbol | Min. | Typ. | Max. | Unit |
|----------------------|-----------------------------|----------------------|------|------|------|-----------|
| DCLK | Cycle frequency | $1/t_{CLK}$ | 39.6 | 43.2 | 48 | MHz |
| | Low level width | t_{WCL} | 10 | - | - | ns |
| | High level width | t_{WCH} | 10 | - | - | |
| | Rise / Fall time | t_{rCLK}, t_{fCLK} | - | - | 12 | |
| | Duty | D | 0.4 | 0.5 | 0.6 | - |
| DE | Set up time | t_{SI} | 8 | - | - | ns |
| | Hold time | t_{HI} | 8 | - | - | |
| | Rise / Fall time | t_{r}, t_{f} | - | - | 12 | ns |
| | Horizontal cycle | t_H | 1320 | 1360 | 1440 | t_{CLK} |
| | Horizontal valid data width | t_{HD} | 1280 | 1280 | 1280 | |
| | Horizontal porch width | t_{HB} | 40 | 80 | 160 | |
| | Vertical cycle | t_V | 500 | 530 | 555 | t_H |
| | Vertical valid data width | t_{VD} | 480 | 480 | 480 | |
| Vertical porch width | t_{VB} | 20 | 50 | 75 | | |
| Data | Set up time | t_{SD} | 8 | - | - | ns |
| | Hold time | t_{HD} | 8 | - | - | |
| | Rise / Fall time | t_{Dr}, t_{Df} | - | - | 12 | ns |

9.5 TIMING CHART

Data Enable (DE) is the signal to determine valid data, and the timing of DE can be determined from Horizontal and Vertical timing as below. For this display, only DE and DCLK are the essential signals. Horizontal and Vertical timing are not necessary to connect to display interface after DE has been generated and input.

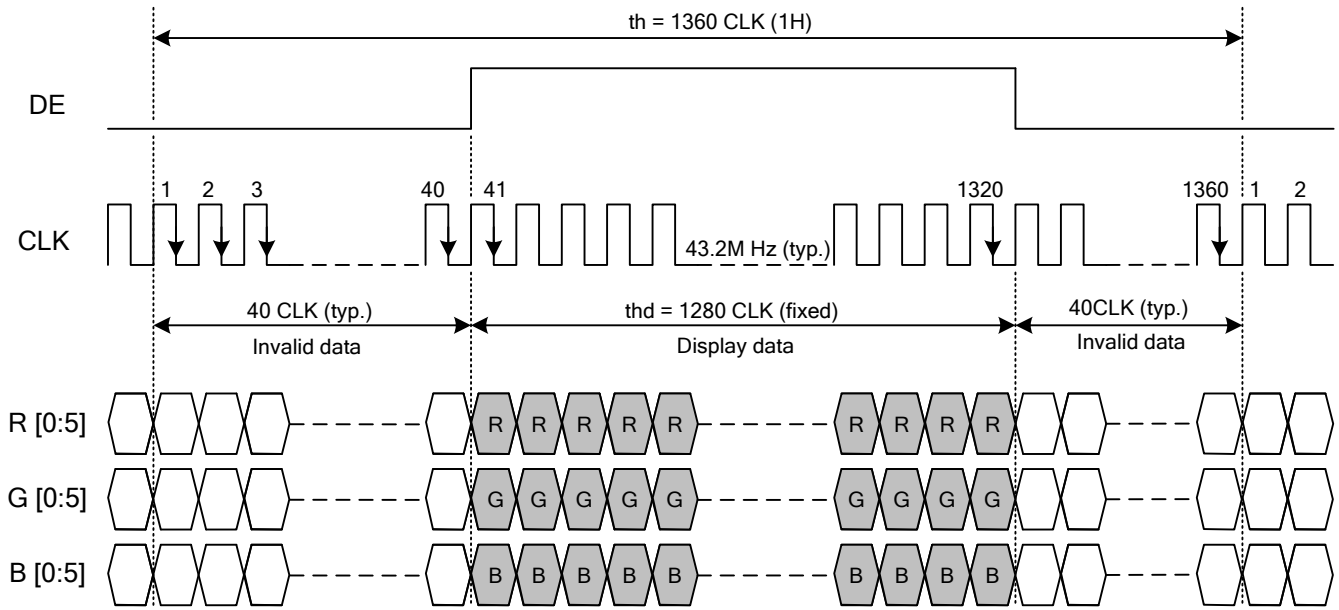


Fig. 8.3 Horizontal Timing

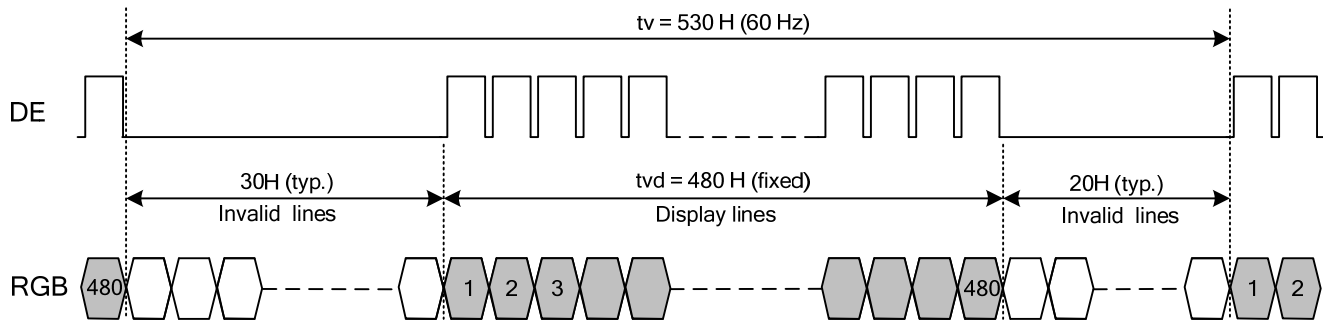
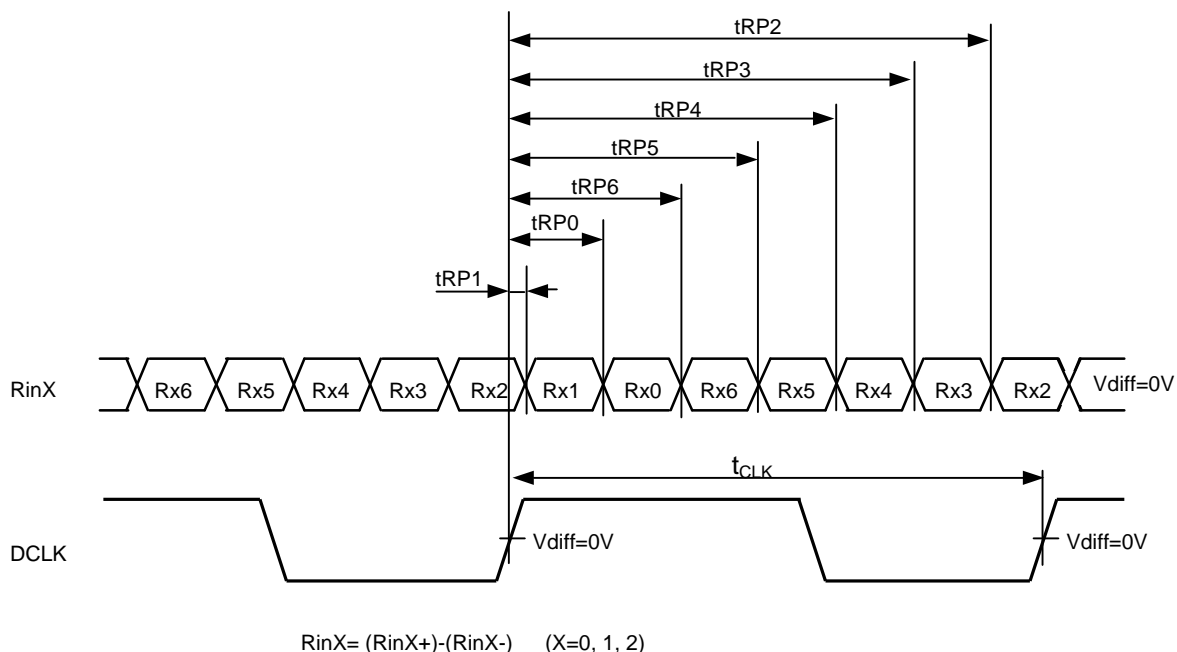


Fig. 8.4 Vertical Timing

9.6 LVDS RECEIVER TIMING

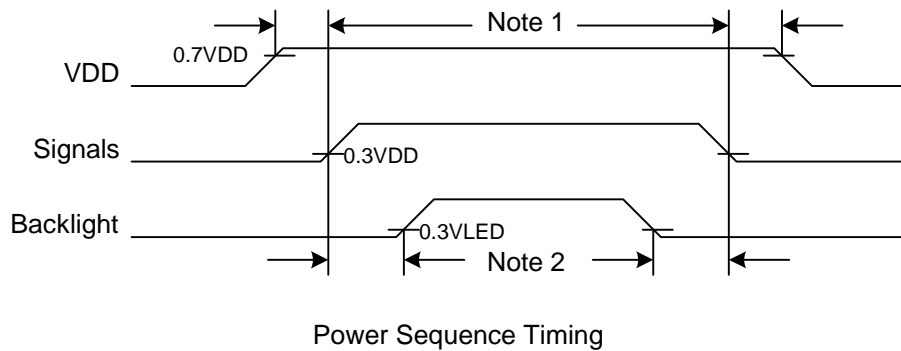


| Item | Symbol | Min. | Typ. | Max. | Unit | |
|-------------------|-------------------|-------------|-----------------------|-----------------|-----------------------|-----|
| DCLK | Frequency | $1/t_{CLK}$ | 39.6 | 43.2 | 48 | MHz |
| RinX (X=0,1,2) | 0 data position | t_{RP0} | $1/7 * t_{CLK} - 0.4$ | $1/7 * t_{CLK}$ | $1/7 * t_{CLK} + 0.4$ | ns |
| | 1st data position | t_{RP1} | -0.4 | 0 | +0.4 | |
| | 2nd data position | t_{RP2} | $6/7 * t_{CLK} - 0.4$ | $6/7 * t_{CLK}$ | $6/7 * t_{CLK} + 0.4$ | |
| | 3rd data position | t_{RP3} | $5/7 * t_{CLK} - 0.4$ | $5/7 * t_{CLK}$ | $5/7 * t_{CLK} + 0.4$ | |
| | 4th data position | t_{RP4} | $4/7 * t_{CLK} - 0.4$ | $4/7 * t_{CLK}$ | $4/7 * t_{CLK} + 0.4$ | |
| | 5th data position | t_{RP5} | $3/7 * t_{CLK} - 0.4$ | $3/7 * t_{CLK}$ | $3/7 * t_{CLK} + 0.4$ | |
| | 6th data position | t_{RP6} | $2/7 * t_{CLK} - 0.4$ | $2/7 * t_{CLK}$ | $2/7 * t_{CLK} + 0.4$ | |

9.7 DATA INPUT for DISPLAY COLOR

| | COLOR & Gray Scale | Data Signal | | | | | | | | | | | | | | | | | |
|-------------|--------------------|-------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| | | R5 | R4 | R3 | R2 | R1 | R0 | G5 | G4 | G3 | G2 | G1 | G0 | B5 | B4 | B3 | B2 | B1 | B0 |
| Basic Color | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red (0) | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green (0) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue (0) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Cyan | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Magenta | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Yellow | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Red | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red (62) | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red (61) | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Red (1) | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red (0) | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Green | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green (62) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green (61) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Green (1) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green (0) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| Blue | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue (62) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| | Blue (61) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Blue (1) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 |
| Blue (0) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | |

9.8 POWER SEQUENCE

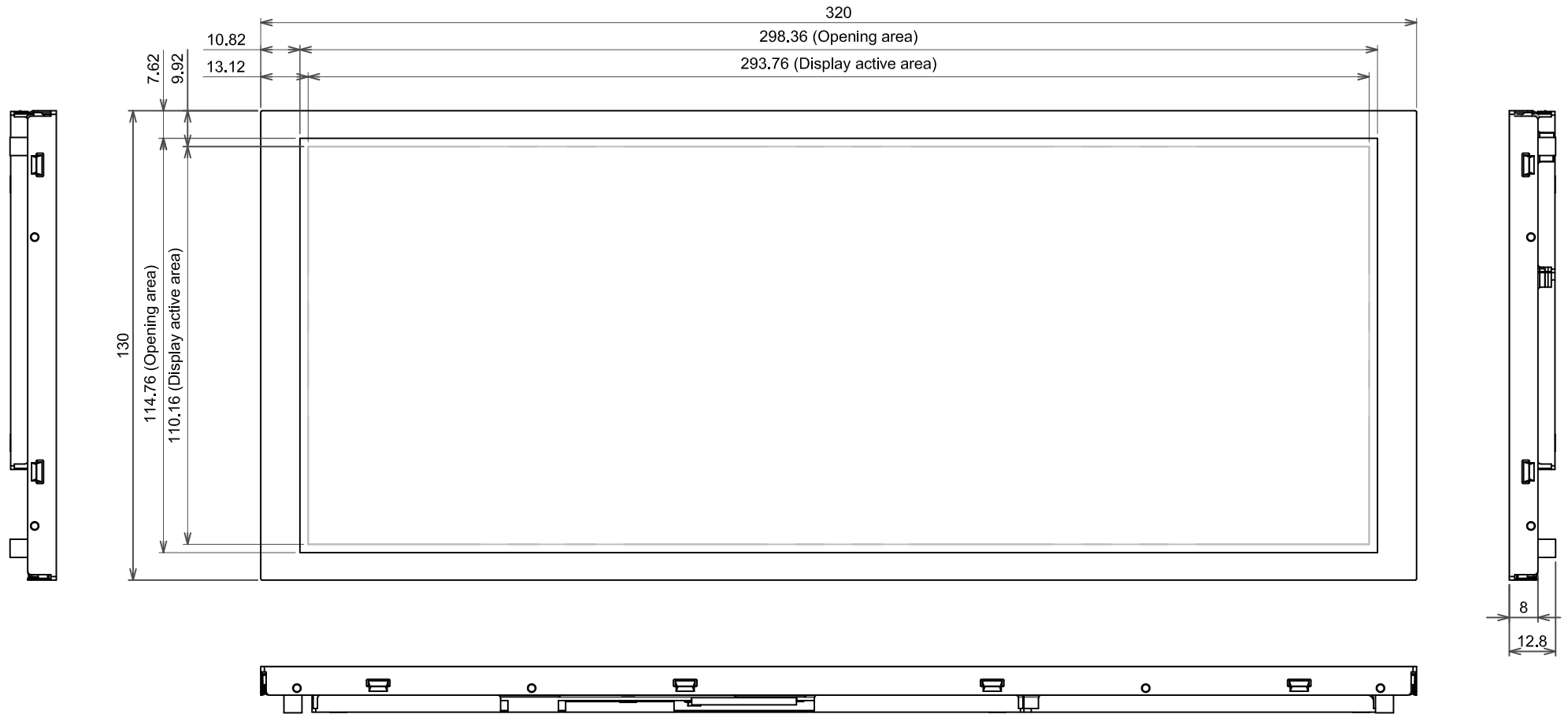


Note 1: In order to avoid any damages, VDD has to be applied before all other signals. The opposite is true for power off where VDD has to be remained on until all other signals have been switch off. The recommended time period is 1 second. Hot plugging might cause display damage due to incorrect power sequence, please pay attention on interface connecting before power on.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

10. OUTLINE DIMENSIONS

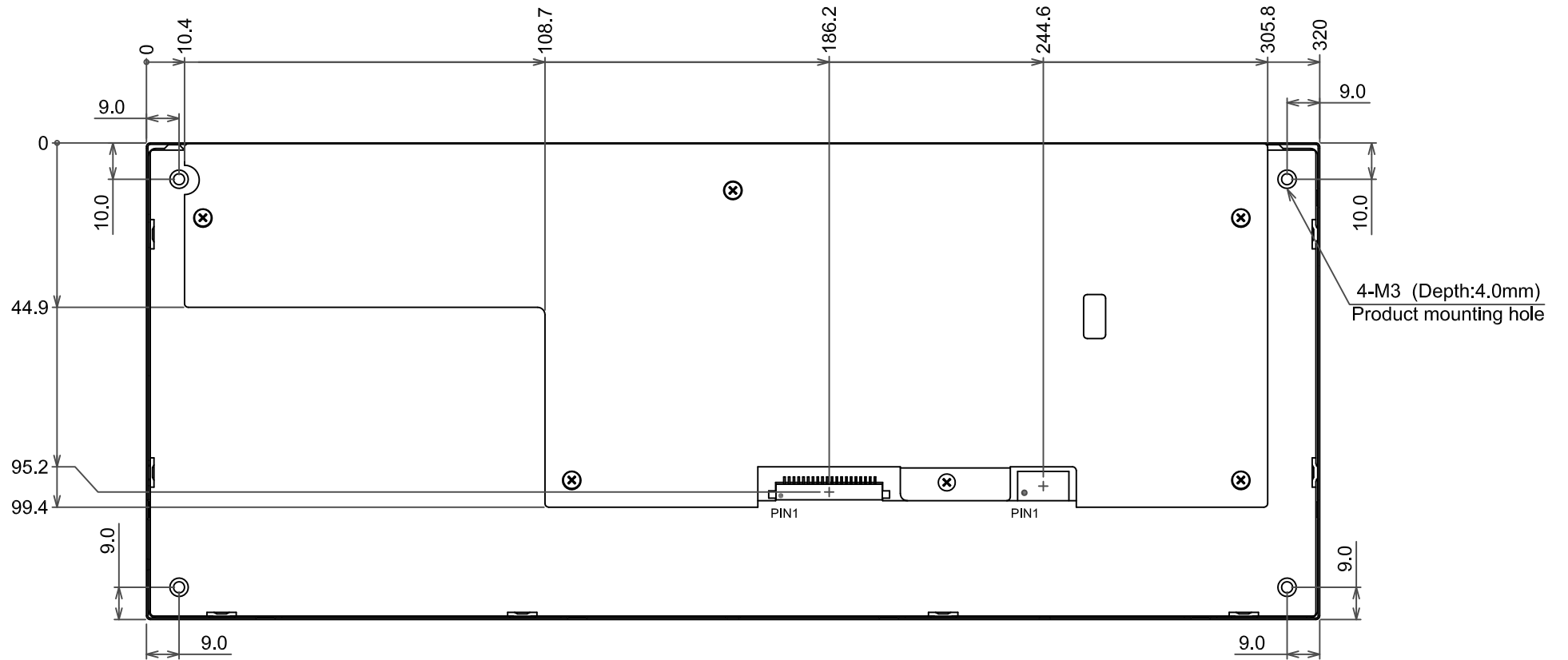
10.1 FRONT VIEW



Note. General tolerance ± 0.5

Scale : NTS
Unit : mm

10.2 REAR VIEW



Note. General tolerance ± 0.5

11. APPEARANCE STANDARD

The appearance inspection is performed in a dark room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 11.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

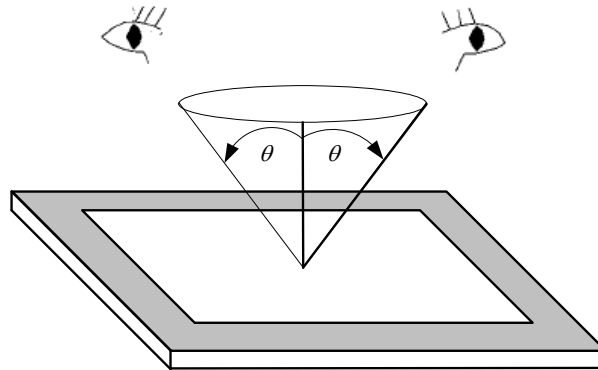


Fig 11.1

11.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 3 areas as shown in Fig.11.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area, which extended 1 mm out from LCD active area; C zone is the area between B zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

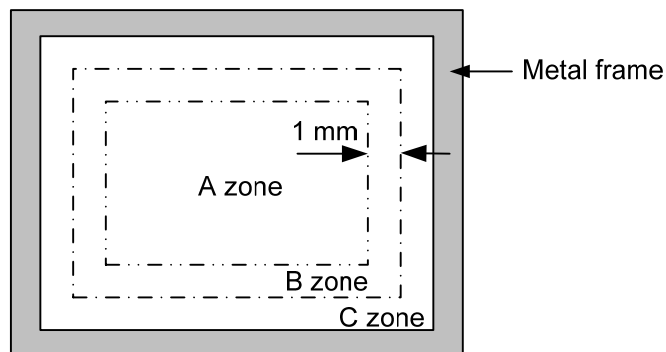


Fig 11.2

11.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 11.3 and Fig. 11.4.

| Item | Criteria | | | | Applied zone |
|---|---------------------------------------|-------------------------|----------------|----------------|----------------|
| Scratches | Length (mm) | Width (mm) | Maximum number | Minimum space | A,B |
| | Ignored | $W \leq 0.01$ | Ignored | - | |
| | $L \leq 40$ | $W \leq 0.02$ | 10 | - | |
| | $L \leq 20$ | $W \leq 0.04$ | 10 | - | |
| | Round (Dot Shape) | | | | |
| | Average diameter (mm) | Maximum number | Minimum space | | |
| | $D \leq 0.2$ | Ignore | - | | |
| | $D \leq 0.4$ | 10 | - | | |
| Dent | Serious one is not allowed | | | | A |
| Wrinkles in polarizer | Serious one is not allowed | | | | A |
| Bubbles on polarizer | Average diameter (mm) | | Maximum number | | A |
| | $D \leq 0.3$ | | Ignored | | |
| | $0.3 < D \leq 0.5$ | | 10 | | |
| | $0.5 < D \leq 1.0$ | | 5 | | |
| 1) Stains 2) Foreign Materials 3) Dark Spot | Filamentous (Line shape) | | | | A,B |
| | Length (mm) | Width (mm) | | Maximum number | |
| | Ignored | | | | |
| | $L \leq 1.0$ | | | Ignored | |
| | $1.0 \leq L$ | $0.06 < W$ | | Dot Shape | |
| | Round (Dot shape) | | | | A,B |
| | Average diameter (mm) | Maximum number | Minimum Space | | |
| | $D \leq 0.45$ | Ignored | - | | |
| | $0.45 < D \leq 0.7$ | 5 | - | | |
| | $0.7 < D$ | None | - | | |
| | In total | Filamentous + Round=10 | | | |
| | Those wiped out easily are acceptable | | | | |
| | Dot-Defect (Note 1) | | | Type | Maximum number |
| Bright dot-defect | | 1 dot | 4 | | |
| | | 2 adjacent dot | 1 | | |
| | | 3 adjacent dot or above | Not allowed | | |
| | | In total | 5 | | |
| Dark dot-defect | | 1 dot | 5 | | |
| | | 2 adjacent dot | 2 | | |
| | | 3 adjacent dot or above | Not allowed | | |
| | | In total | 5 | | |
| In total | | 10 | | | |

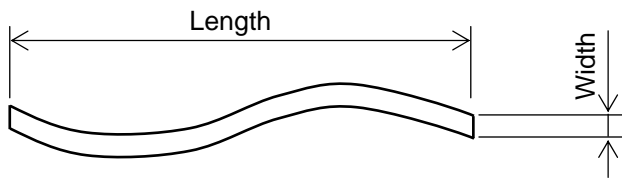


Fig 11.3

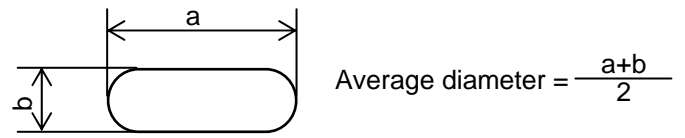


Fig 11.4

Note 1: The definitions of dot defect are as below:

- The defect area of the dot must be bigger than half of a dot.
- For bright dot-defect, showing black pattern, the dot's brightness must be over 30% brighter than others.
- For dark dot-defect, showing white pattern, the dot's brightness must be under 70% darker than others.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 11.5.
- The Density of dot defect is defined in the area within diameter $\phi = 20\text{mm}$.

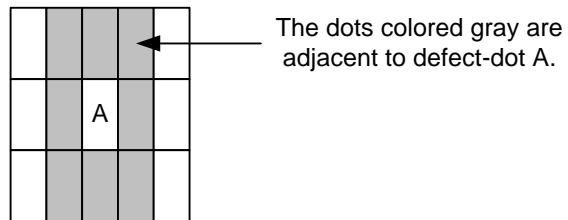


Fig 11.5

12. PRECAUTIONS

12.1 PRECAUTIONS OF ESD

- 1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.
- 2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

12.2 PRECAUTIONS OF HANDLING

- 1) In order to keep the appearance of display in good condition, please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.
- 2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.
- 3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.
- 4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.
- 5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.
- 6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.
- 7) Maximum pressure to the surface of the display must be less than $1,96 \times 10^4$ Pa. If the area of adding pressure is less than 1 cm^2 , the maximum pressure must be less than 1.96N.

12.3 PRECAUTIONS OF OPERATING

- 1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.
- 2) When the display is operating at significant low temperature, the response time will be slower than it at 25°C . In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.
- 3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.
- 4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than $\pm 100 \text{ mV}$.

12.4 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

- 1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
- 2) The recommended long term storage temperature is between 10 C° ~35 C° and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
- 3) It would be better to keep the displays in the container, which is shipped from Hitachi, and do not unpack it.
- 4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

13. DESIGNATION OF LOT MARK

1) The lot mark is showing in Fig.13.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

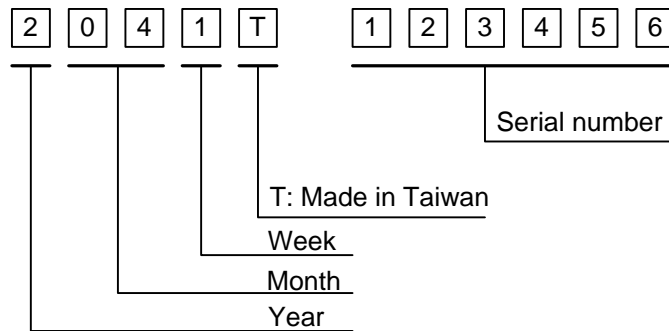


Fig. 13.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

| Year | Lot Mark |
|------|----------|
| 2012 | 2 |
| 2013 | 3 |
| 2014 | 4 |
| 2015 | 5 |
| 2016 | 6 |

| Month | Lot Mark | Month | Lot Mark |
|-------|----------|-------|----------|
| Jan. | 01 | Jul. | 07 |
| Feb. | 02 | Aug. | 08 |
| Mar. | 03 | Sep. | 09 |
| Apr. | 04 | Oct. | 10 |
| May | 05 | Nov. | 11 |
| Jun. | 06 | Dec. | 12 |

| Week | Lot Mark |
|------------|----------|
| 1~7 days | 1 |
| 8~14 days | 2 |
| 15~21 days | 3 |
| 22~28 days | 4 |
| 29~31 days | 5 |

3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.

4) The location of the lot mark is on the back of the display shown in Fig. 13.2.

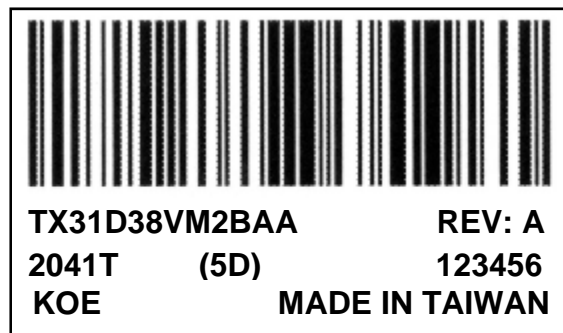


Fig. 13.2